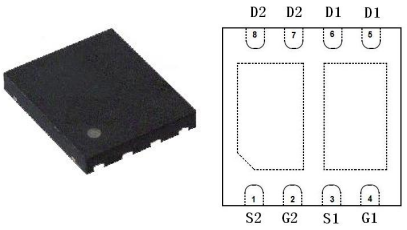
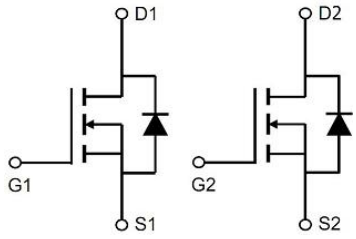
General Description：

CS21N03 AQ2-1, the silicon N-channel Enhanced VDMOSFETs, is obtained by the high density Trench technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. This device is suitable for use as a load switch and PWM applications.. The package form is DFN3×3-8L, which accords with the RoHS standard.

V_{DSS}	30	V
I_D	21	A
P_D	13.8	W
$R_{DS(ON)Typ}$	17	mΩ



Top View



Features：

- **Fast Switching**
- **Low ON Resistance(Rdson≤21 mΩ)**
- **Low Gate Charge**
- **Low Reverse transfer capacitances**
- **100% Single Pulse avalanche energy Test**

Applications：

Power switch circuit of adaptor and charger.

TV screen drive.

Absolute（T_A= 25℃ unless otherwise specified）

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-to-Source Voltage	30	V
I_D	Continuous Drain Current Tc= 25°C	21	A
	Continuous Drain Current Tc = 100 °C	13	A
I_{DM} [a1]	Pulsed Drain Current Tc= 25°C	84	A
V_{GS}	Gate-to-Source Voltage	±20	V
E_{AS} [a2]	Avalanche Energy	80.6	mJ
P_D	Power Dissipation T_C= 25°C	13.8	W
	Derating Factor above 25°C	0.11	W/℃
T_J， T_{stg}	Operating Junction and Storage Temperature Range	150，–55 to 150	℃

Electrical Characteristics（T_j= 25℃ unless otherwise specified）:

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	30	--	--	V
I_{DSS}	Drain to Source Leakage Current	V_{DS} = 30V, V_{GS}= 0V, Tj = 25℃	--	--	1	μA
		V_{DS} =24V, V_{GS}= 0V, Tj = 125℃	--	--	100	
$I_{GSS(F)}$	Gate to Source Forward Leakage	V_{GS}=20V	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	V_{GS} =-20V	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	V_{GS}=10V,I_D=10A	--	17	21	mΩ
		V_{GS}=4.5V,I_D=8A	--	33	42	mΩ
$V_{GS(TH)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	1.8	2.3	2.8	V
Pulse width tp≤300μs, δ ≤2%						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1MHz	--	3	--	Ω
C_{iss}	Input Capacitance	V_{GS} = 0V V_{DS} =15V f = 1.0MHz	--	590	--	pF
C_{oss}	Output Capacitance		--	158	--	
C_{rss}	Reverse Transfer Capacitance		--	85	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	V_{GS}=10V,Rg=3Ω V_{DD}=15V,I_D=10A	--	7.5	--	ns
tr	Rise Time		--	3.7	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	21	--	
t_f	Fall Time		--	5.2	--	
Q_g(V_{GS}=4.5V)	Total Gate Charge	V_{GS}=10V V_{DD}=15V,I_D=10A	--	7	--	nC
Q_g(V_{GS}=10V)	Total Gate Charge		--	13	--	
Q_{gs}	Gate to Source Charge		--	2.1	--	
Q_{gd}	Gate to Drain (“Miller”)Charge		--	4.3	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I_S	Continuous Source Current (Body Diode)	T_C= 25℃	--	--	21	A
I_{SM}	Maximum Pulsed Current (Body Diode)		--	--	84	A
V_{SD}	Diode Forward Voltage	I_S=10A,V_{GS}=0V	--	--	1.2	V
trr	Reverse Recovery Time	di/dt=100A/us Is=10A	--	14.6	--	ns
Qrr	Reverse Recovery Charge		--	6.9	--	nC
Pulse width tp≤300μs, δ ≤2%						

Symbol	Parameter	Max.	Units
$R_{\theta JC}$	Junction-to-Csae	9	℃/W
$R_{\theta JA}$	Junction-to-Ambient	75	℃/W

[a1]: Repetitive rating; pulse width limited by maximum junction temperature
[a2]: L=1mH, V_{DD}=15V, Ias=12.7A,Start T_J=25℃
[a3]: Recommend soldering temperature defined by IPC/JEDEC J-STD 020

Characteristics Curve:

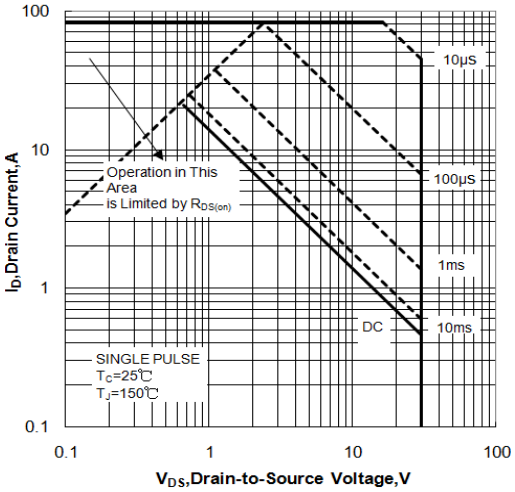


Figure 1. Maximum Safe Operating Area

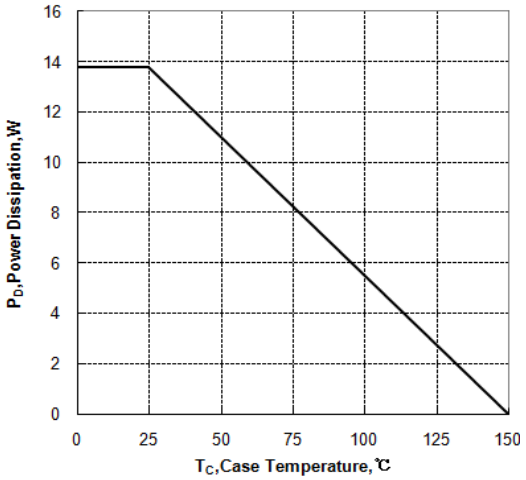


Figure 2. Maximum Power Dissipation vs Case Temperature

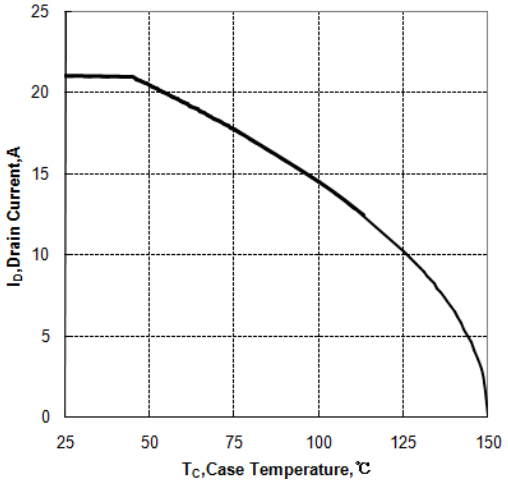


Figure 3. Maximum Continuous Drain Current vs

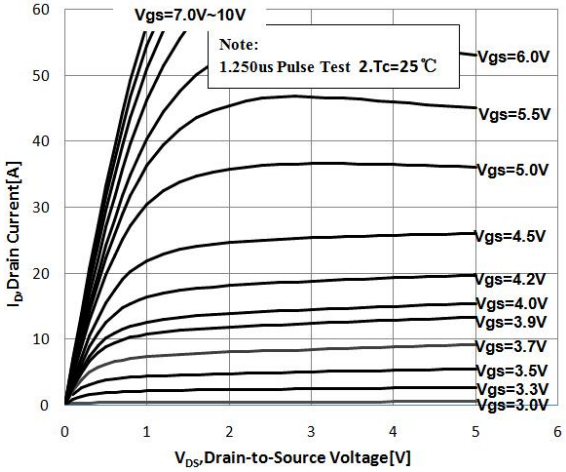


Figure 4. Typical output Characteristics

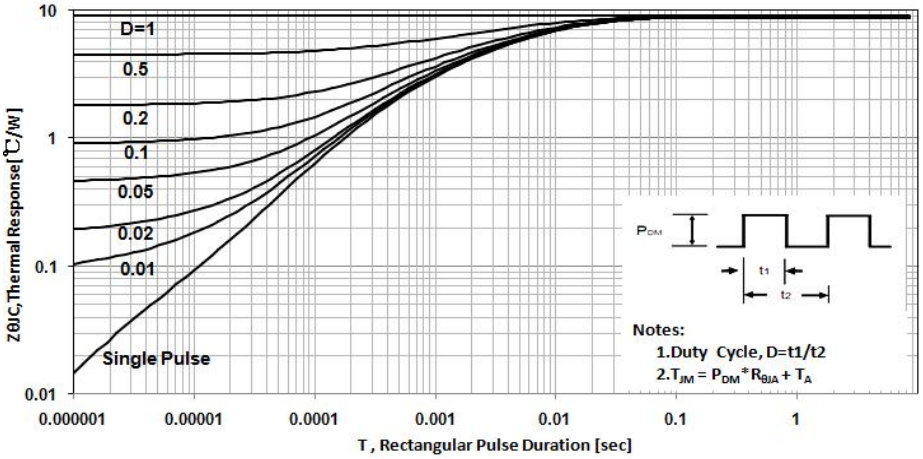


Figure 5 Maximum Effective Thermal Impedance , Junction to Case

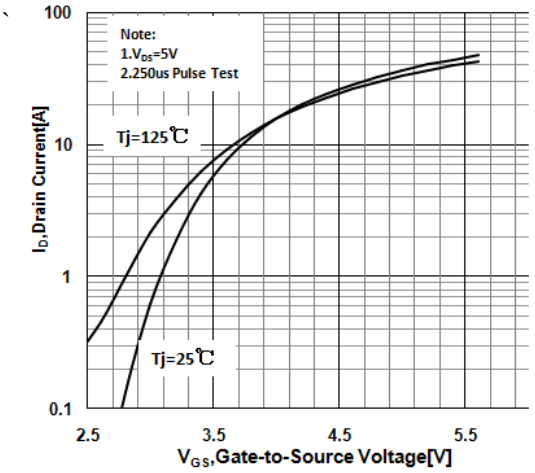


Figure 6 Typical Transfer Characteristics

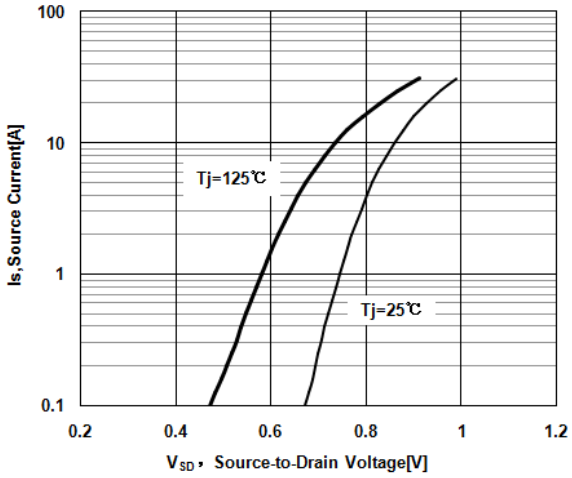


Figure 7 Typical Body Diode Transfer Characteristics

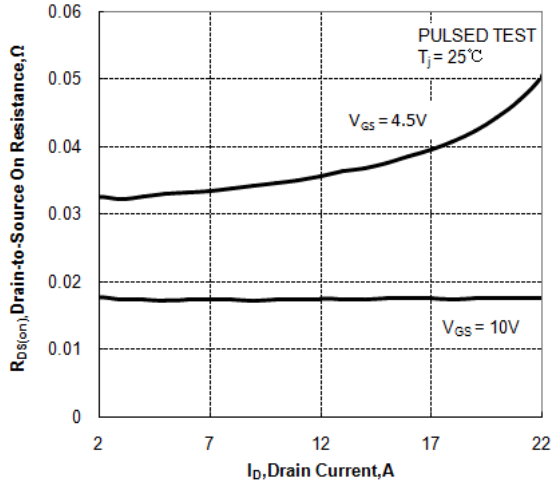


Figure 8. Drain-to-Source On Resistance vs Drain Current

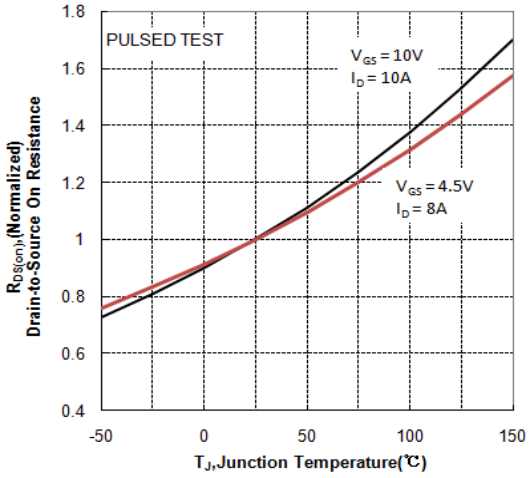


Figure 9. Normalized On Resistance vs Junction Temperature

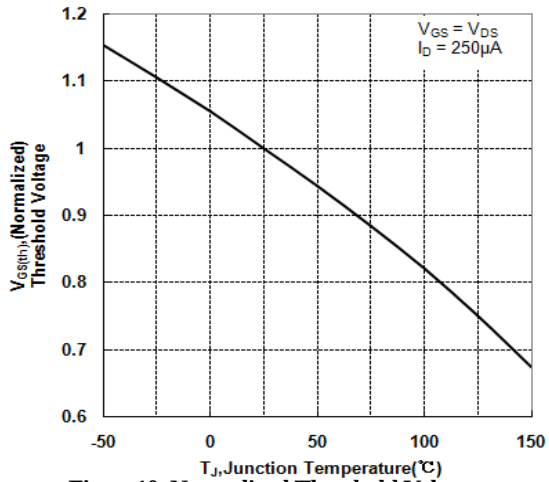


Figure10. Normalized Threshold Voltage vs Junction Temperature

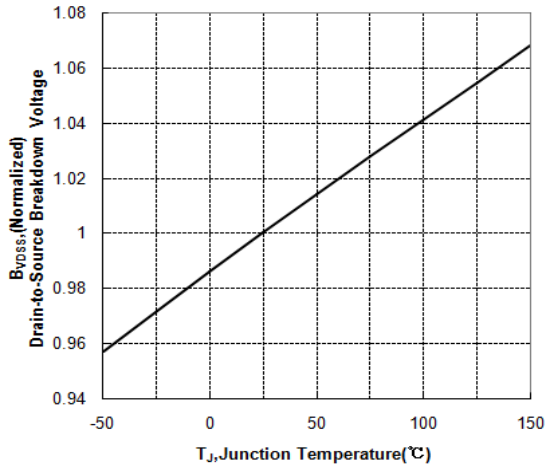


Figure 11. Normalized Breakdown Voltage vs Junction Temperature

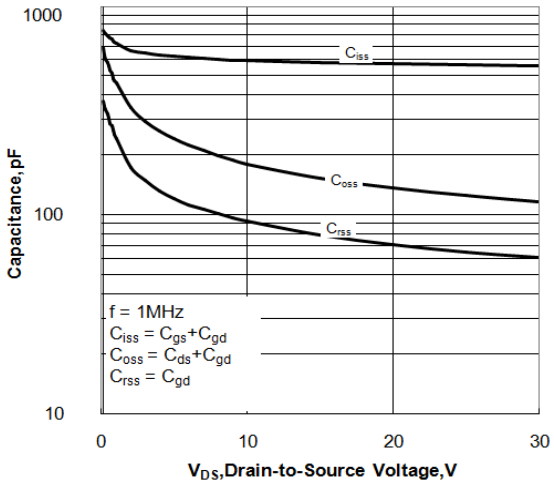


Figure 12. Capacitance Characteristics

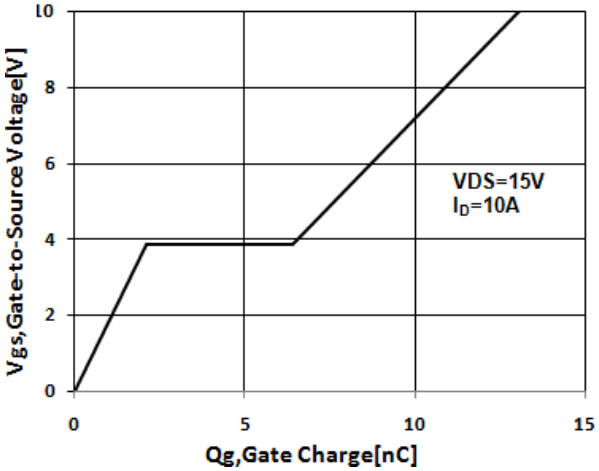


Figure 13 Typical Gate Charge vs Gate to Source Voltage

Test Circuit and Waveform

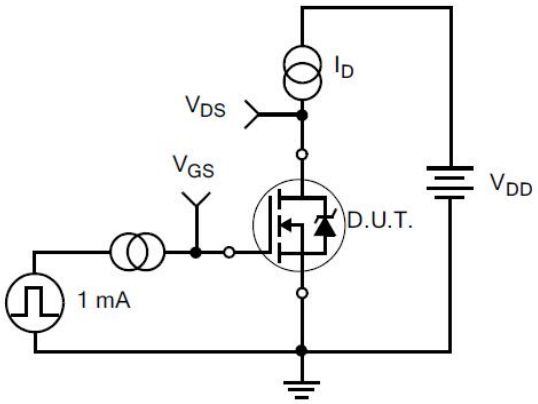


Figure 14. Gate Charge Test Circuit

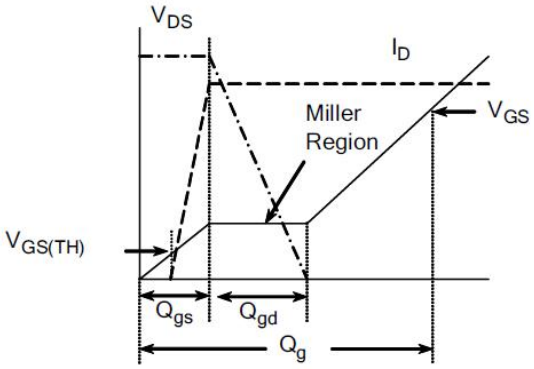


Figure 15. Gate Charge Waveforms

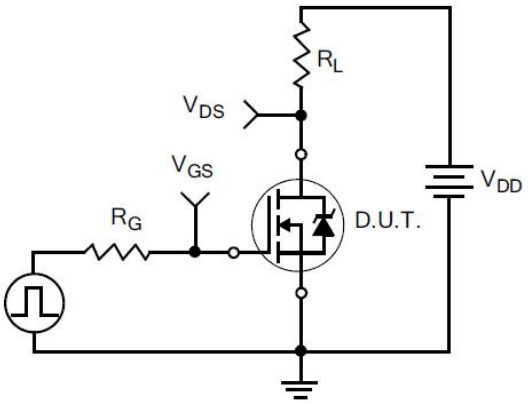


Figure 16. Resistive Switching Test Circuit

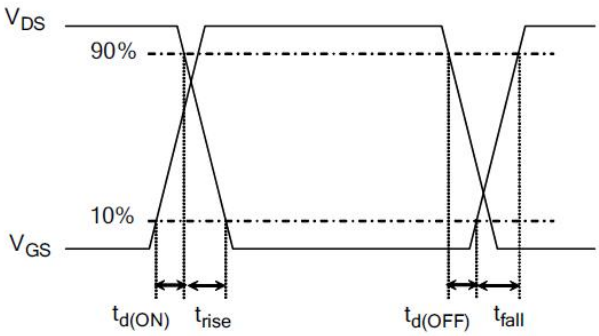


Figure 17. Resistive Switching Waveforms

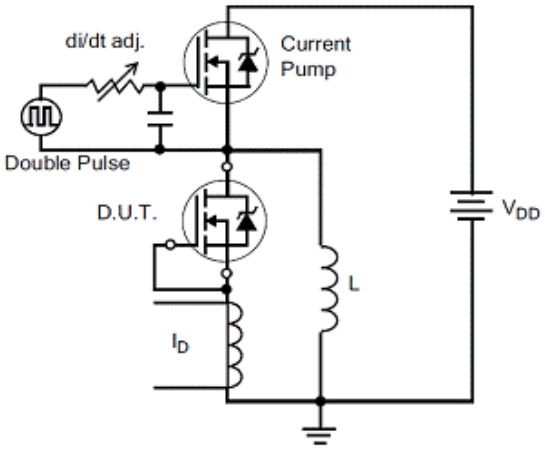


Figure 18. Diode Reverse Recovery Test Circuit

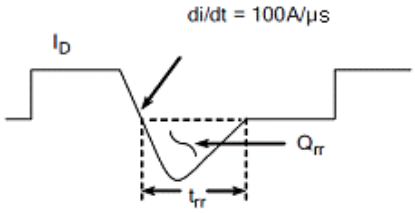


Figure 19. Diode Reverse Recovery Waveform

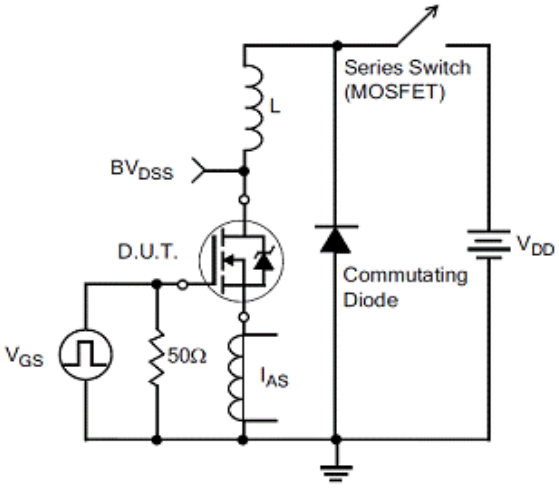


Figure20.Unclamped Inductive Switching Test Circuit

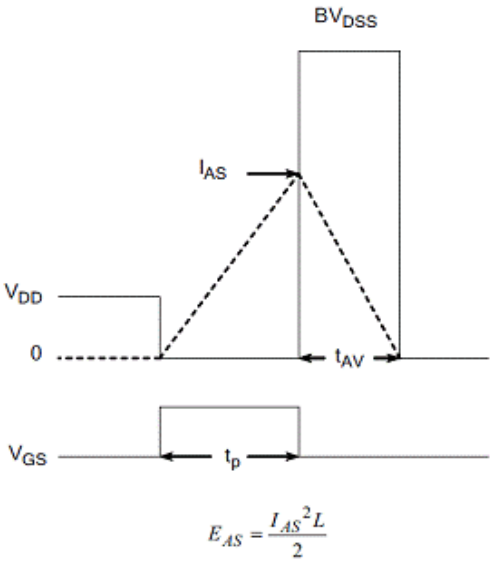
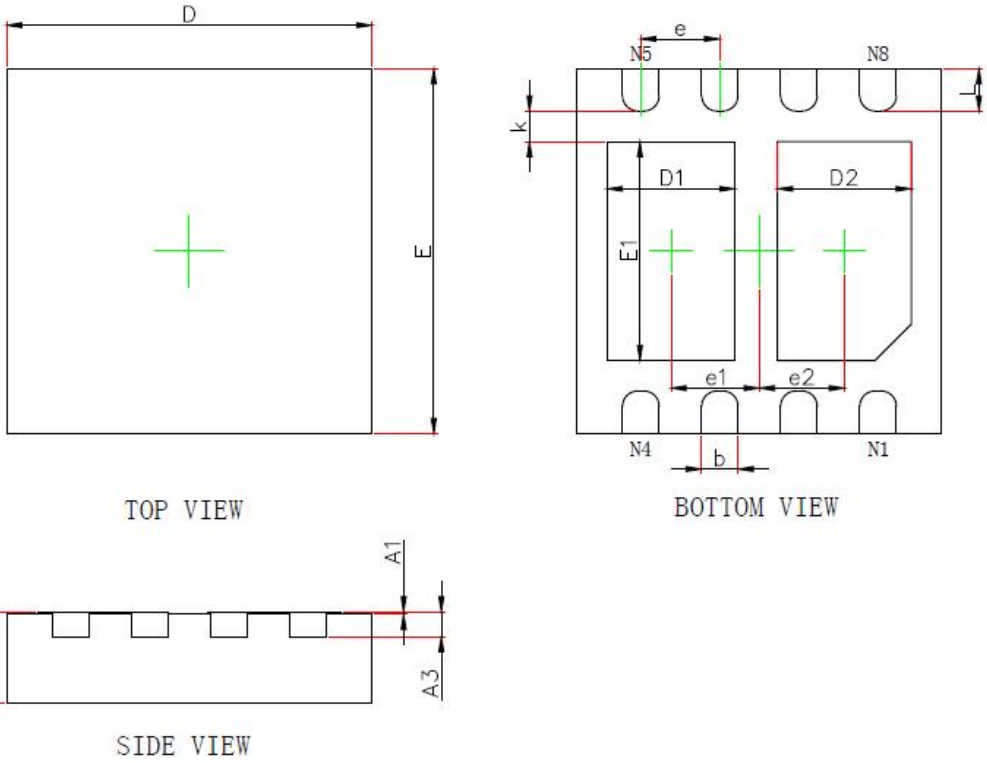


Figure21.Unclamped Inductive Switching Waveform

Package Information：



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	2.900	3.100	0.114	0.122
E	2.900	3.100	0.114	0.122
D1	0.950	1.150	0.037	0.045
E1	1.700	1.900	0.067	0.075
D2	1.000	1.200	0.039	0.047
k	0.200MIN.		0.008MIN.	
b	0.250	0.350	0.010	0.014
e	0.650TYP.		0.026TYP.	
e1	0.725TYP.		0.029TYP.	
e2	0.700TYP.		0.028TYP.	
L	0.250	0.450	0.010	0.018

DFN3×3-8L Package

The name and content of poisonous and harmful material in products

Part's Name	Hazardous Substance									
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE	DIBP	DEHP	DBP	BBP
Limit	≤0.1%	≤0.1%	≤ 0.01%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○	○	○	○	○
Molding	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○	○	○	○	○
Electric glue	○	○	○	○	○	○	○	○	○	○
Note	○: Means the hazardous material is under the criterion of 2011/65/EU. ×: Means the hazardous material exceeds the criterion of 2011/65/EU. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.									

Warnings

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

WUXI CHINA RESOURCES HUAJING MICROELECTRONICS CO., LTD.

Add: No.14 Liangxi RD. Wuxi, Jiangsu, China Mail:214061 http://www. crhj.com.cn
Tel: +86 0510-85807228 Fax: +86- 0510-85800864

Marketing Part: Post: 214061 Tel: +86 0510-81805277/81805336
Fax: +86 0510-85800360/85803016

Application and Service: Post: 214061 Tel / Fax: +86- 0510-81805243/81805110