华润微电子(重庆)有限公司

CRJL99N65G2BF

SJMOS N-MOSFET 650V, 91mΩ, 26A

Features

- CRM(CQ) Super_Junction technology
- Much lower Ron*A performance for On-state efficiency
- Better efficiency due to very low FOM
- Ultra-fast body diode

Product Summary

$V_{DS,min}$	650V
$R_{DS(on),typ}$	91mΩ
I_D	26A

Applications

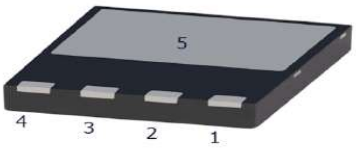
- LED/LCD/PDP TV and monitor Lighting
- Solar/Renewable/UPS-Micro Inverter System
- Charger
- Power Supply

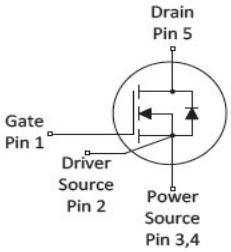
100% DVDS Tested

100% Avalanche Tested

RoHS

H F





Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRJL99N65G2BF	CRJL99N65G2BF	DFN8*8	Tape&Reel	N/A	N/A	3000pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Continuous drain current [1)] T_C = 25°C T_C = 100°C	I_D	 26 16	A
Pulsed drain current [2)](T_C = 25°C, t_p limited by $T_{j,max}$)	$I_{D,pulse}$	77	A
Avalanche energy, single pulse (L=50mH)	E_{AS}	480	mJ
MOSFET dv/dt ruggedness	dv/dt	50	V/ns
Gate-Source voltage	V_{GS}	±30	V
Power dissipation (T_C = 25°C)	P_{tot}	149	W
Continuous diode forward current(T_C = 25°C)	I_S	26	A
Diode pulse current [2)] (T_C = 25°C)	$I_{S,pulse}$	77	A
Recovery diode dv/dt [3)]	dv/dt	50	V/ns
Maximum diode commutation speed	di_F/dt	800	A/μs
Operating junction and storage temperature	T_j , T_{stg}	-55...+150	°C

1) Limited by $T_{j,max}$. Maximum Duty Cycle D = 0.50
2) Pulse width t_p limited by $T_{j,max}$
3) Identical low side and high side switch with identical R_g

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Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – case	R_{thJC}	-	0.6	0.84	°C/W	
Thermal resistance, junction – ambient	R_{thJA}	-	-	56	°C/W	

Electrical Characteristic (at T_j = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	650	-	-	V	V_{GS}=0V, I_D=250μA
Gate threshold voltage	$V_{GS(th)}$	3.2	-	4.6	V	V_{DS}=V_{GS},I_D=250μA
Zero gate voltage drain current	I_{DSS}	-	-	5	μA	V_{DS}=650V,V_{GS}=0V T_j=25°C
		-	800	-		T_j=150°C
Gate-source leakage current	I_{GSS}	-	-	±100	nA	V_{GS}=±30V,V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	-	91	105	mΩ	V_{GS}=10V, I_D=17A, T_j=25°C
		-	205	-		T_j=150°C
Transconductance	g_{fs}	-	19	-	S	V_{DS}=20V,I_D=17A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	1900	-	pF	V_{GS}=0V, V_{DS}=100V, f=1MHz
Output Capacitance	C_{oss}	-	117	-		
Reverse Transfer Capacitance	C_{rss}	-	2.2	-		
Gate Total Charge	Q_g	-	70	-	nC	V_{GS}=10V, V_{DS}=480V, I_D=17A
Gate-Source charge	Q_{gs}	-	17	-		
Gate-Drain charge	Q_{gd}	-	45	-		
Gate plateau voltage	$V_{plateau}$	-	7.7	-	V	
Turn-on delay time	$t_{d(on)}$	-	50	-	ns	V_{GS}=10V, I_D=17A, V_{DS}=400V, R_g=27Ω
Rise time	t_r	-	100	-		
Turn-off delay time	$t_{d(off)}$	-	180	-		
Fall time	t_f	-	50	-		
Gate resistance	$R_{g,int}$	-	0.9	-	Ω	f=1MHz

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Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	0.7	0.9	1.1	V	V_{GS}=0V,I_{SD}=17A
Body Diode Reverse Recovery Time	t_{rr}	-	140	-	ns	I_{SD}=17A di_F/dt=100A/μs V_{DS}=400V
Body Diode Reverse Recovery Charge	Q_{rr}	-	0.89	-	μC	

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Typical Performance Characteristics

Fig 1. Output Characteristics (T_j=25℃)

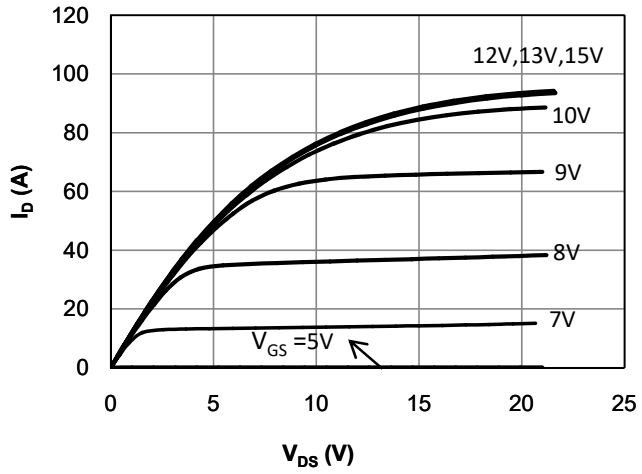


Fig 2. Output Characteristics (T_j=150℃)

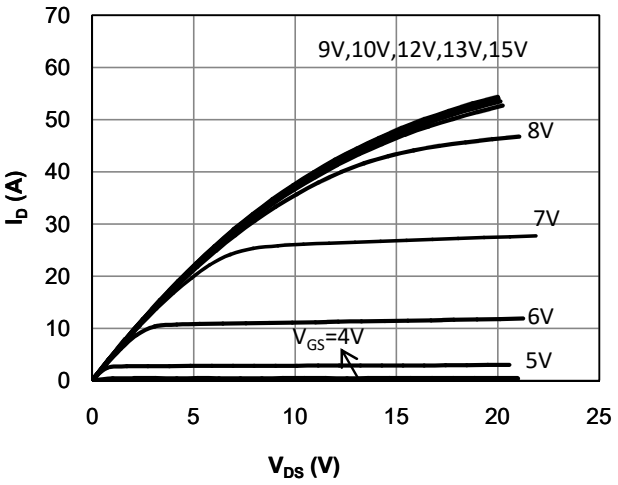


Fig 3: Transfer Characteristics

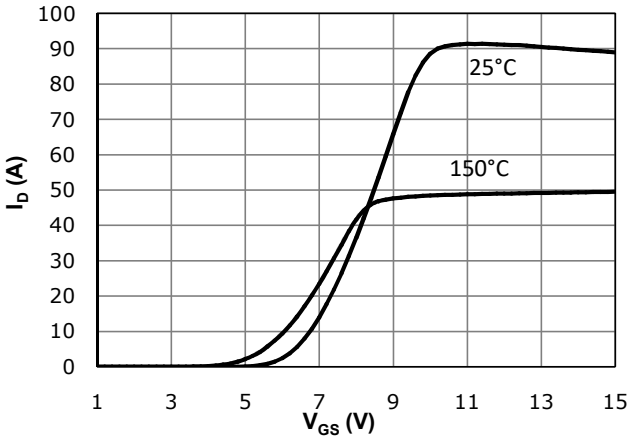


Fig 4: V_{TH} Vs. T_j Temperature Characteristics

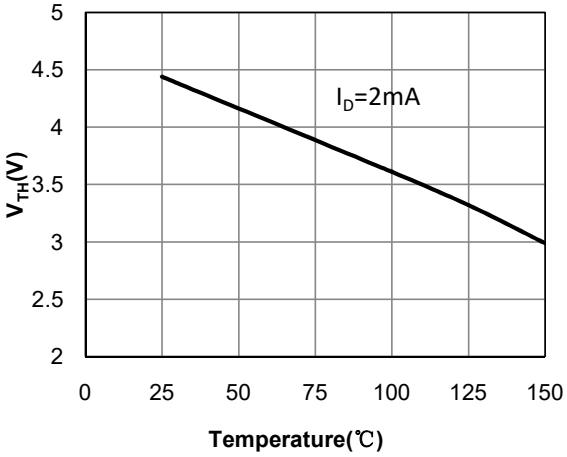


Fig 5: $R_{DS(on)}$ vs. I_{DS} Characteristics(T_j=25℃)

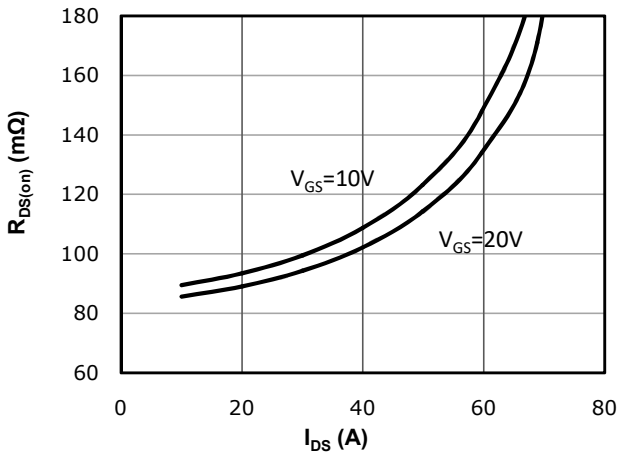
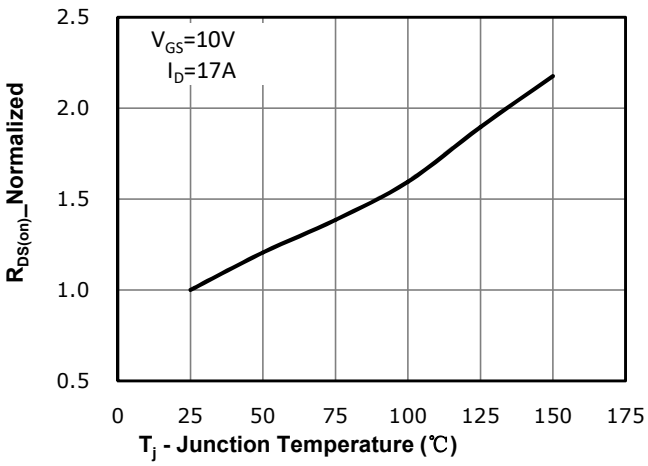


Fig 6: $R_{DS(on)}$ vs. Temperature



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Fig 7: BV_{DSS} vs. Temperature

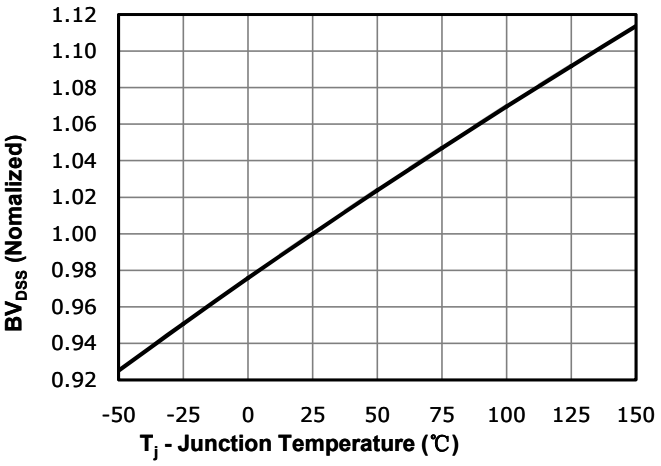


Fig 8: $R_{DS(on)}$ vs. Gate Voltage

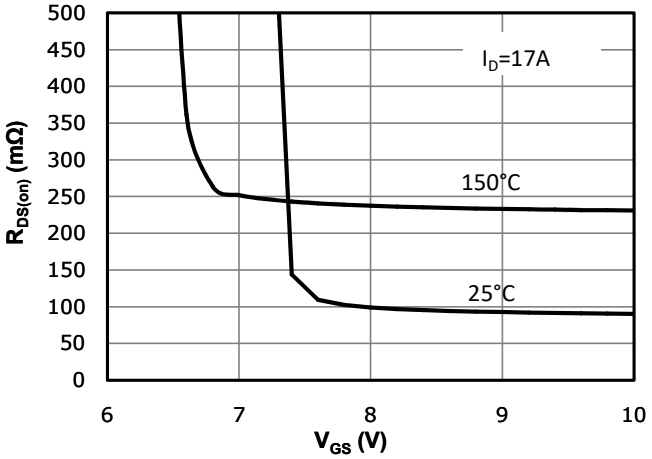


Fig 9: Body-diode Forward Characteristics

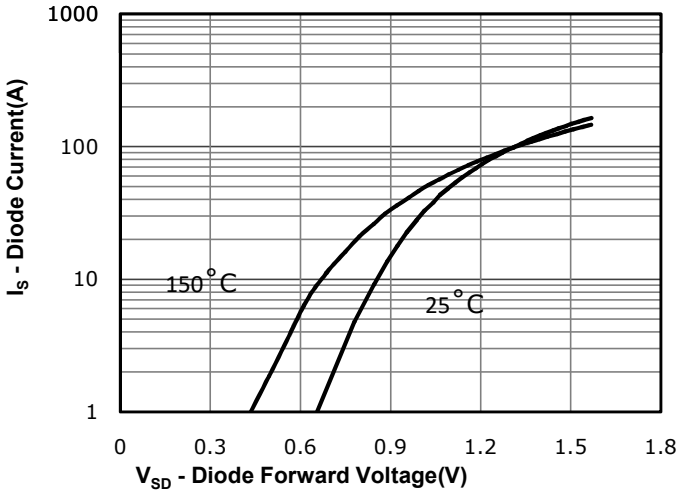


Fig 10: Gate Charge Characteristics

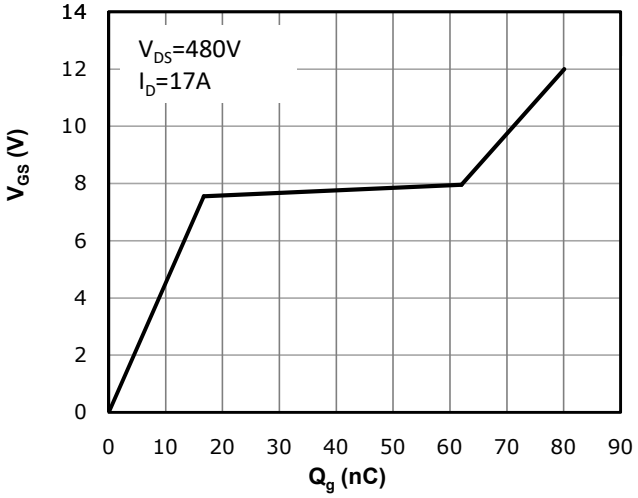


Fig 11: Capacitance Characteristics

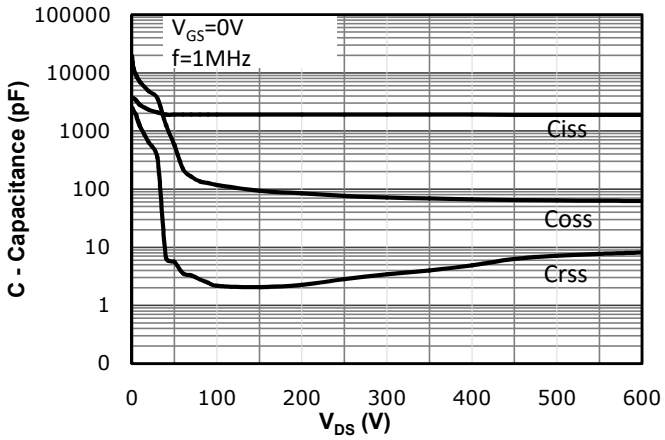
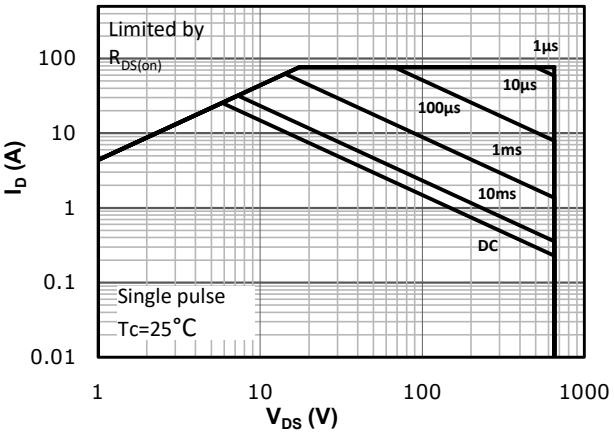
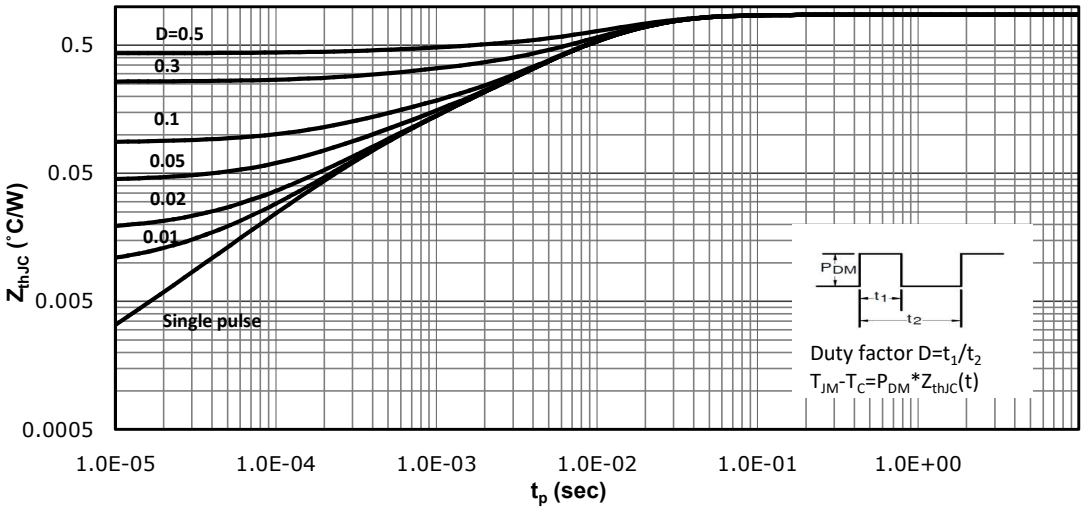


Fig 12: Safe Operating Area



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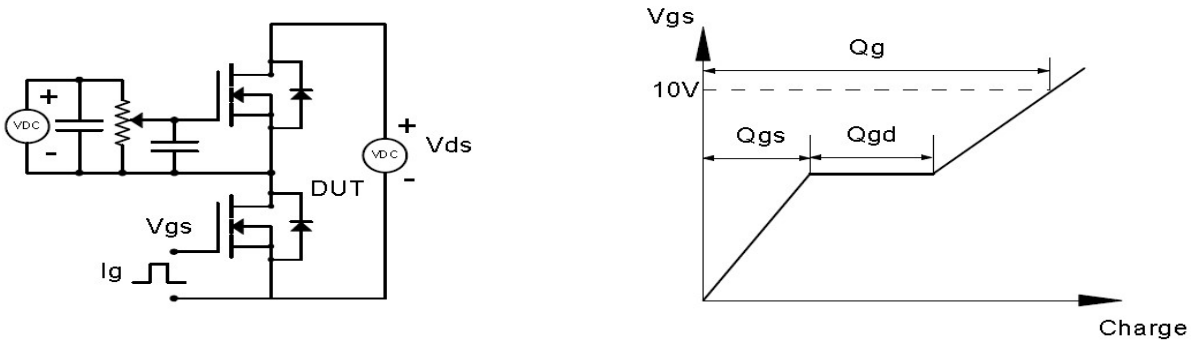
Fig 13: Max. Transient Thermal Impedance



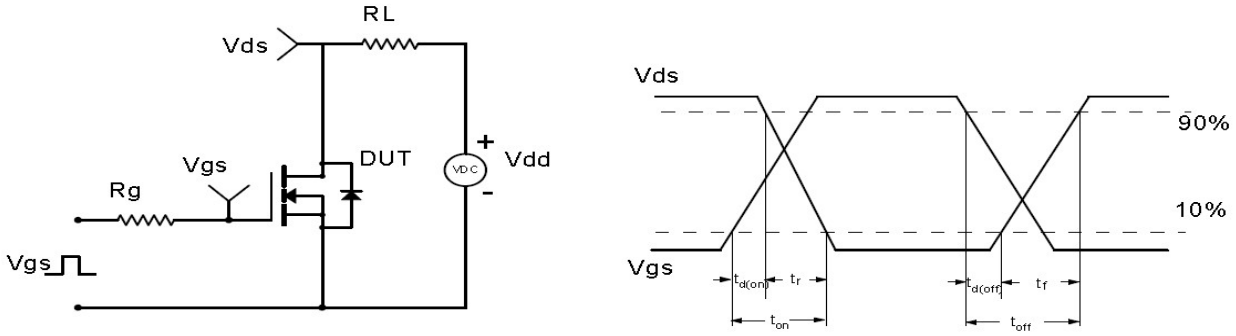
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Test Circuit & Waveform

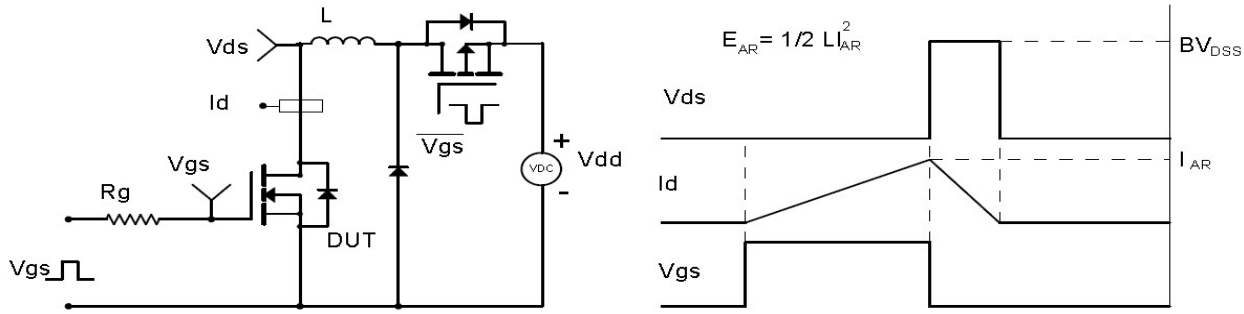
Gate Charge Test Circuit & Waveform



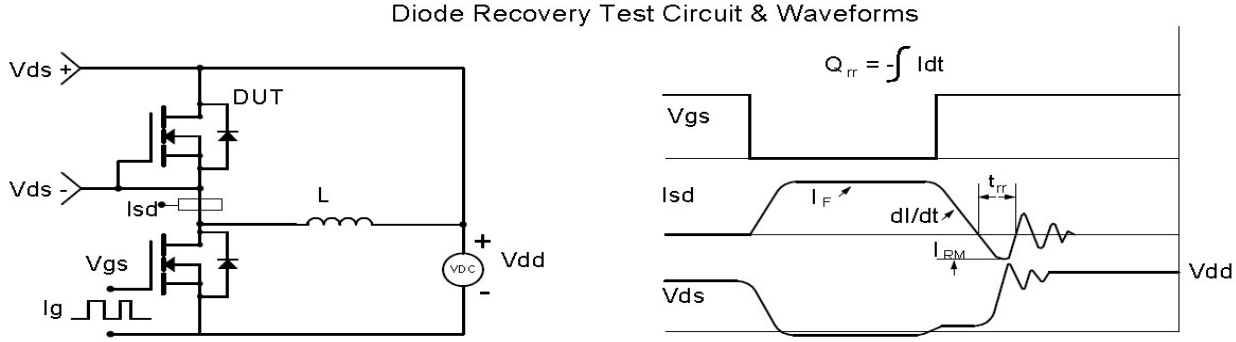
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

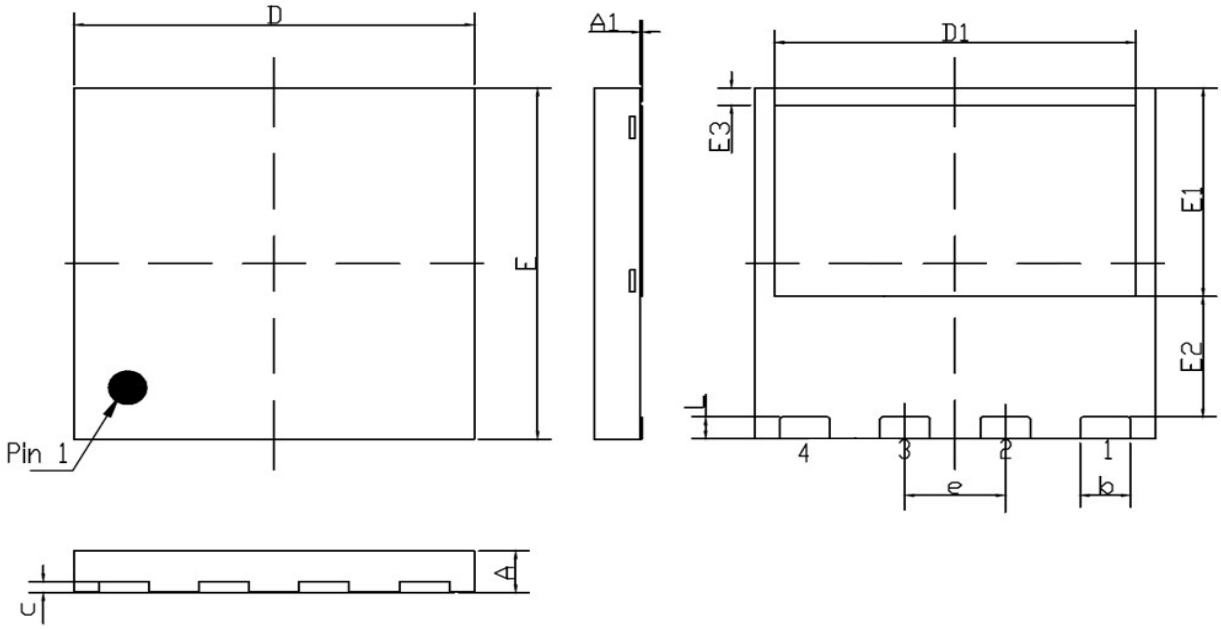


Diode Recovery Test Circuit & Waveforms



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Package Outline: DFN8*8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.75	1.10	0.030	0.043
A1	0.00	0.05	0.000	0.002
b	0.90	1.10	0.035	0.043
c	0.10	0.30	0.004	0.012
D	7.90	8.10	0.311	0.319
D1	7.10	7.30	0.280	0.287
E	7.90	8.10	0.311	0.319
E1	4.65	4.85	0.183	0.191
E2	2.65	2.85	0.104	0.112
E3	0.30	0.50	0.012	0.020
e	2.00 BSC		0.079 BSC	
L	0.40	0.60	0.016	0.024

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Marking



NOTE:
NXBBAAAA

N	—WB code (Usually omitted)
X	—Assembly location code
BB	—Fab code
AAAA	—Lot code

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Revision History

Revison	Date	Major changes
1.0	2023/8/10	First version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qulified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semicondutor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM(CQ) reserves the right to improve product design, function and reliability without notice.

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