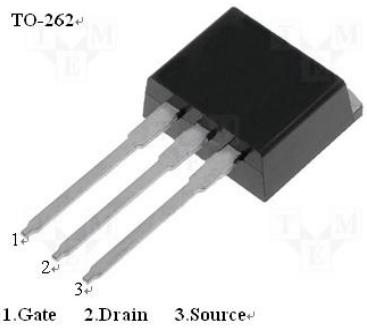
# Silicon N-Channel Power MOSFET

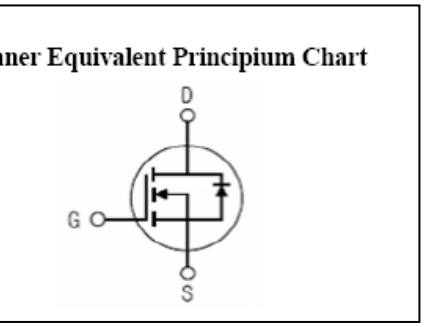
## CS10N65 ARR-G

## General Description：

CS10N65 ARR-G, the silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-262, which accords with the RoHS standard.

| | | |
|---|---|---|
| $V_{DSS}$ | 650 | V |
| $I_D$ | 10 | A |
| $P_D$($T_C$=25℃) | 130 | W |
| $R_{DS(ON)Typ}$ | 0.86 | Ω |




## Features：

- **Fast Switching**
- **Low ON Resistance(Rdson≤1. 0Ω)**
- **Low Gate Charge (Typical Data:32nC)**
- **Low Reverse transfer capacitances(Typical:7pF)**
- **100% Single Pulse avalanche energy Test**

## Applications：

Power switch circuit of adaptor and charger.

**Absolute**（Tc= 25℃ unless otherwise specified）：

| Symbol | Parameter | Rating | Units |
|---|---|---|---|
| $V_{DSS}$ | Drain-to-Source Voltage | 650 | V |
| $I_D$ | Continuous Drain Current | 10 | A |
| | Continuous Drain Current $T_C$ = 100 ℃ | 6.3 | A |
| $I_{DM}$ [a1] | Pulsed Drain Current | 40 | A |
| $V_{GS}$ | Gate-to-Source Voltage | ±30 | V |
| $E_{AS}$ [a2] | Single Pulse Avalanche Energy | 500 | mJ |
| dv/dt [a3] | Peak Diode Recovery dv/dt | 5.0 | V/ns |
| $P_D$ | Power Dissipation | 130 | W |
| | Derating Factor above 25 ℃ | 1.04 | W/℃ |
| $T_J$， $T_{stg}$ | Operating Junction and Storage Temperature Range | 150，–55 to 150 | ℃ |
| $T_L$ | Maximum Temperature for Soldering | 300 | ℃ |

## Electrical Characteristics（Tc= 25℃ unless otherwise specified）:

| OFF Characteristics | | | | | | |
|---|---|---|---|---|---|---|
| Symbol | Parameter | Test Conditions | Rating | | | Unit |
| | | | Min. | Typ. | Max. | s |
| $V_{DSS}$ | Drain to Source Breakdown Voltage | $V_{GS}$=0V, $I_D$=250μA | 650 | -- | -- | V |
| $\Delta BV_{DSS}/\Delta T_J$ | Bvdss Temperature Coefficient | ID=250uA,Reference25℃ | -- | 0.7 | -- | V/℃ |
| $I_{DSS}$ | Drain to Source Leakage Current | $V_{DS}$ =650V, $V_{GS}$= 0V, $T_a$ = 25℃ | -- | -- | 1 | μA |
| | | $V_{DS}$ =520V, $V_{GS}$= 0V, $T_a$ = 125℃ | -- | -- | 100 | μA |
| $I_{GSS(F)}$ | Gate to Source Forward Leakage | $V_{GS}$ =+30V | -- | -- | 100 | nA |
| $I_{GSS(R)}$ | Gate to Source Reverse Leakage | $V_{GS}$ =-30V | -- | -- | -100 | nA |

| ON Characteristics | | | | | | |
|---|---|---|---|---|---|---|
| Symbol | Parameter | Test Conditions | Rating | | | Units |
| | | | Min. | Typ. | Max. | |
| $R_{DS(ON)}$ | Drain-to-Source On-Resistance | $V_{GS}$=10V,$I_D$=5A | -- | 0.86 | 1.0 | Ω |
| $V_{GS(TH)}$ | Gate Threshold Voltage | $V_{DS}$ = $V_{GS}$, $I_D$ = 250μA | 2.0 | -- | 4.0 | V |
| Pulse width tp≤300μs, δ ≤2% | | | | | | |

| Dynamic Characteristics | | | | | | |
|---|---|---|---|---|---|---|
| Symbol | Parameter | Test Conditions | Rating | | | Units |
| | | | Min. | Typ. | Max. | |
| $g_{fs}$ | Forward Transconductance | $V_{DS}$=15V, $I_D$ =5A | -- | 9.5 | -- | S |
| $C_{iss}$ | Input Capacitance | $V_{GS}$ = 0V $V_{DS}$ = 25V f = 1.0MHz | -- | 1642 | -- | pF |
| $C_{oss}$ | Output Capacitance | | -- | 128 | -- | |
| $C_{rss}$ | Reverse Transfer Capacitance | | -- | 7 | -- | |

| Resistive Switching Characteristics | | | | | | |
|---|---|---|---|---|---|---|
| Symbol | Parameter | Test Conditions | Rating | | | Units |
| | | | Min. | Typ. | Max. | |
| $t_{d(ON)}$ | Turn-on Delay Time | $I_D$ =10A $V_{DD}$ = 325V $R_G$ =10Ω | -- | 27 | -- | ns |
| tr | Rise Time | | -- | 22 | -- | |
| $t_{d(OFF)}$ | Turn-Off Delay Time | | -- | 53 | -- | |
| $t_f$ | Fall Time | | -- | 24 | -- | |
| $Q_g$ | Total Gate Charge | $I_D$ =10A $V_{DD}$ =520V $V_{GS}$ = 10V | -- | 32 | -- | nC |
| $Q_{gs}$ | Gate to Source Charge | | -- | 8 | -- | |
| $Q_{gd}$ | Gate to Drain (“Miller”)Charge | | -- | 12 | -- | |

| Source-Drain Diode Characteristics | | | | | | |
|---|---|---|---|---|---|---|
| Symbol | Parameter | Test Conditions | Rating | | | Units |
| | | | Min. | Typ. | Max. | |
| $I_S$ | Continuous Source Current (Body Diode) | | -- | -- | 10 | A |
| $I_{SM}$ | Maximum Pulsed Current (Body Diode) | | -- | -- | 40 | A |
| $V_{SD}$ | Diode Forward Voltage | $I_S$=10A,$V_{GS}$=0V | -- | -- | 1.5 | V |
| trr | Reverse Recovery Time | $I_S$=10A,$T_j$ = 25℃ dI$_F$/dt=100A/us, $V_{GS}$=0V | -- | 528 | -- | ns |
| Qrr | Reverse Recovery Charge | | -- | 3220 | -- | nC |
| $I_{RRM}$ | Reverse Recovery Current | | -- | 12.2 | -- | A |
| Pulse width tp≤300μs, δ ≤2% | | | | | | |

| Symbol | Parameter | Typ. | Units |
|---|---|---|---|
| $R_{\theta JC}$ | Junction-to-Case | 0.96 | ℃/W |
| $R_{\theta JA}$ | Junction-to-Ambient | 62.5 | ℃/W |

[a1]: Repetitive rating; pulse width limited by maximum junction temperature
[a2]: L=10mH, $I_D$=10A, Start $T_J$=25℃
[a3]: $I_{SD}$ =10A,di/dt ≤100A/us,$V_{DD}$≤$BV_{DS,}$ Start $T_J$=25℃

## Characteristics Curve:

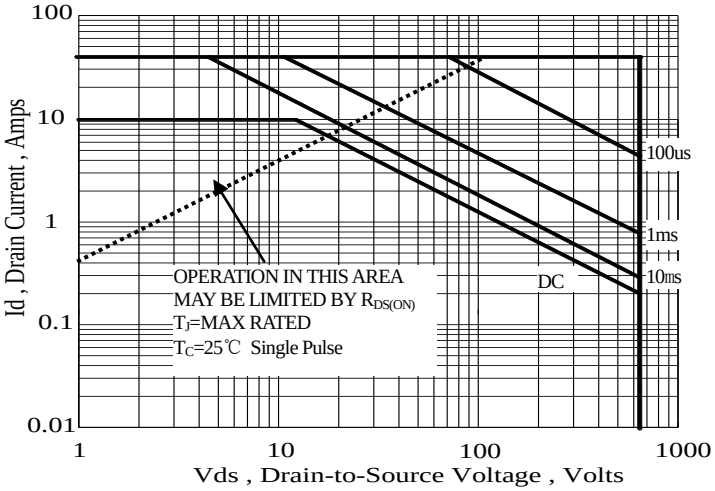



Figure 1 Maximum Forward Bias Safe Operating Area

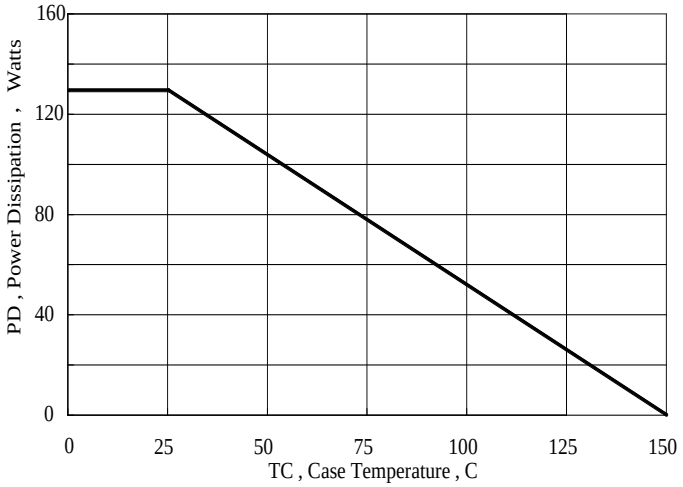



Figure 2 Maximum Power Dissipation vs Case Temperature

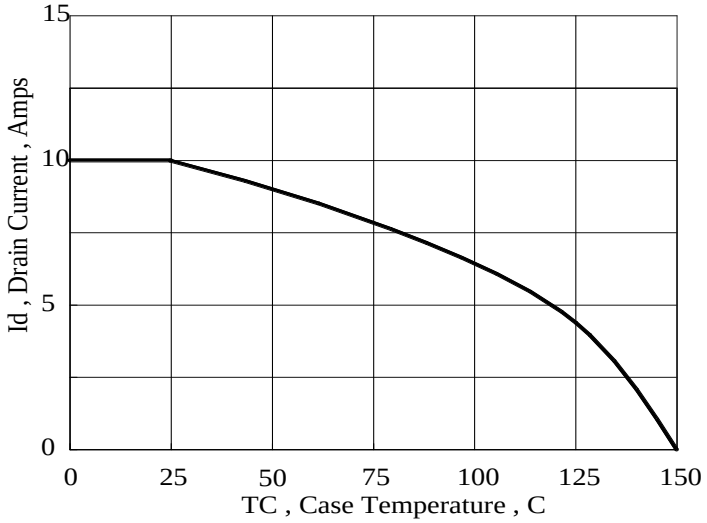



Figure 3 Maximum Continuous Drain Current vs Case Temperature

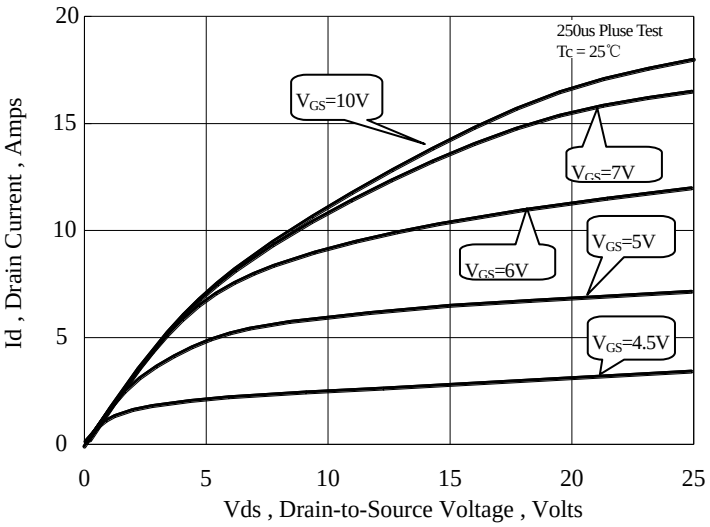



Figure 4 Typical Output Characteristics

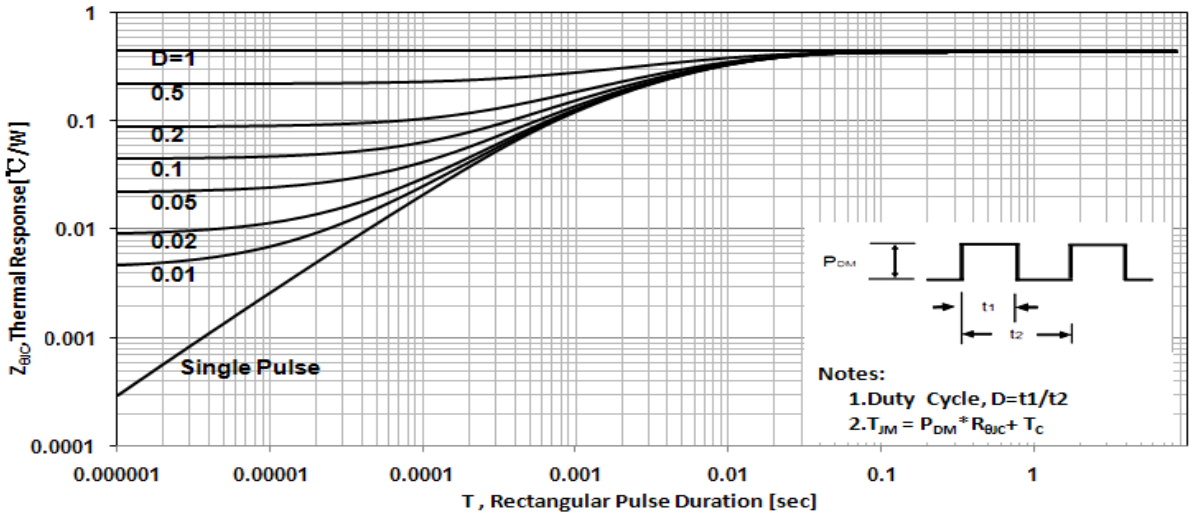



Figure 5 Maximum Effective Thermal Impendance , Junction to Case

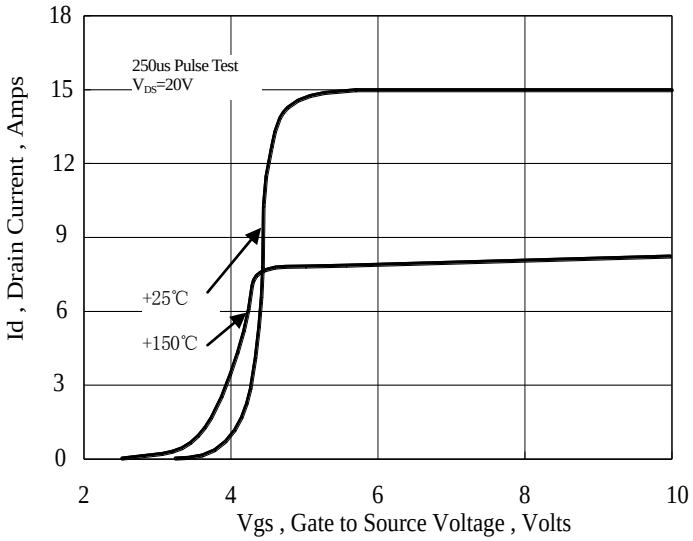



Figure 6 Typical Transfer Characteristics

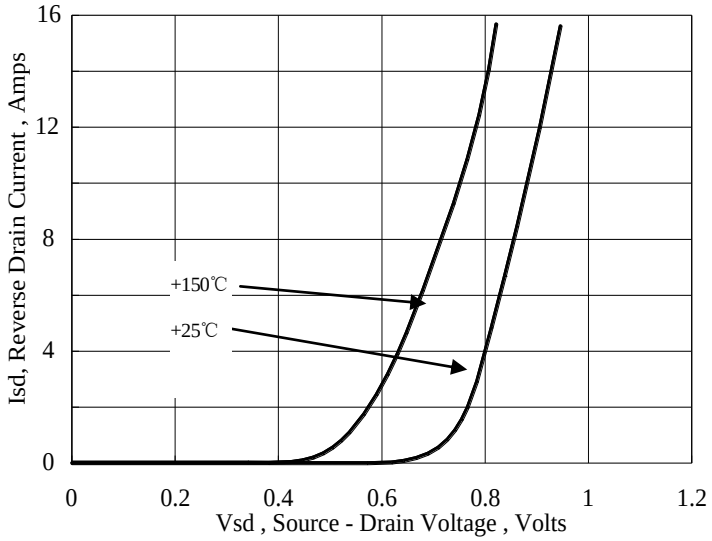



Figure 7 Typical Body Diode Transfer Characteristics

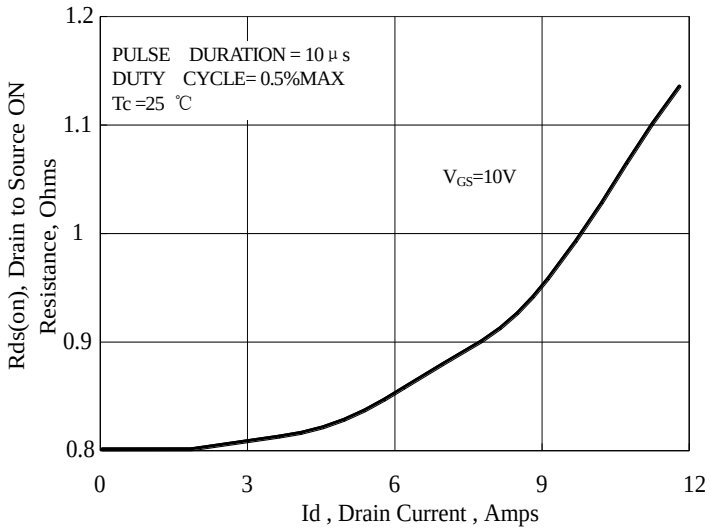



Figure 8 Typical Drain to Source ON Resistance vs Drain Current

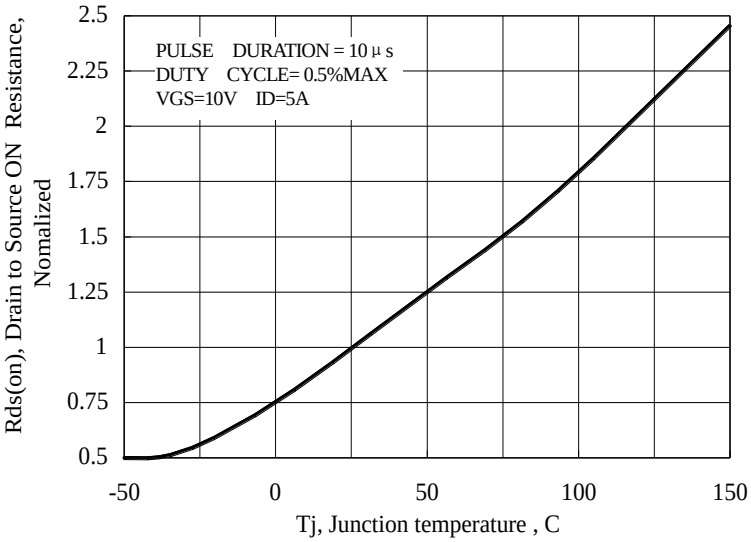



Figure 9 Typical Drian to Source on Resistance vs Junction Temperature

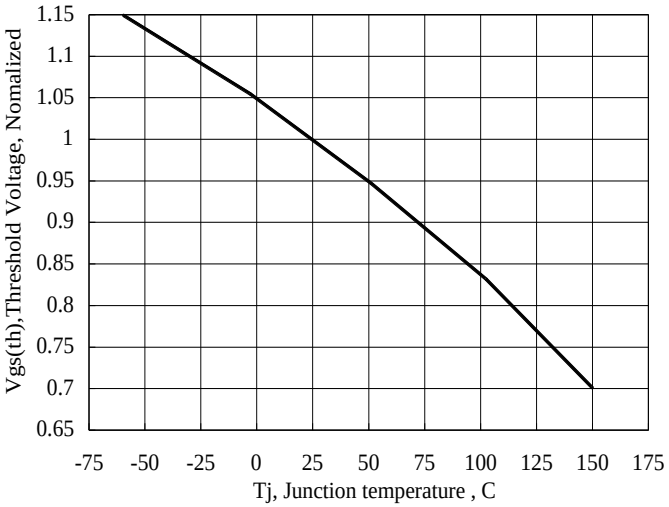



Figure 10 Typical Theshold Voltage vs Junction Temperature

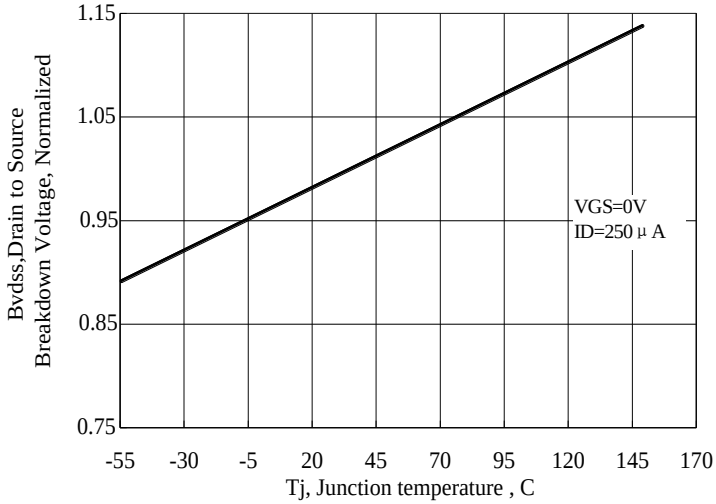



Figure 11 Typical Breakdown Voltage vs Junction Temperature

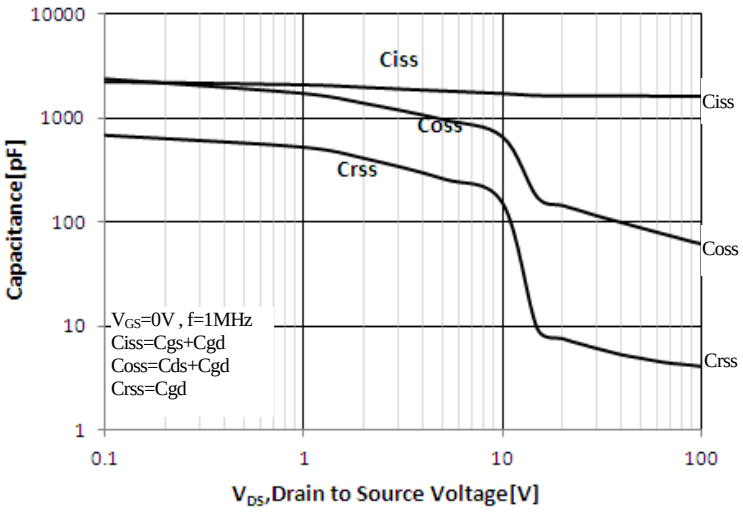



Figure 12 Typical Capacitance vs Drain to Source Voltage

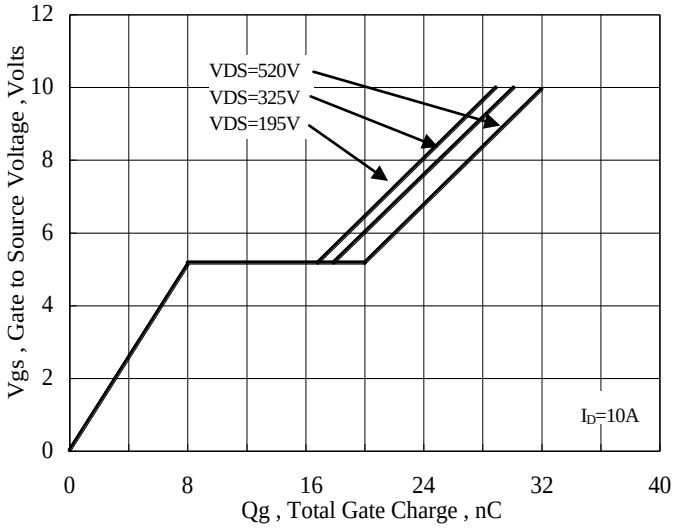



Figure 13 Typical Gate Charge vs Gate to Source Voltage

## Test Circuit and Waveform

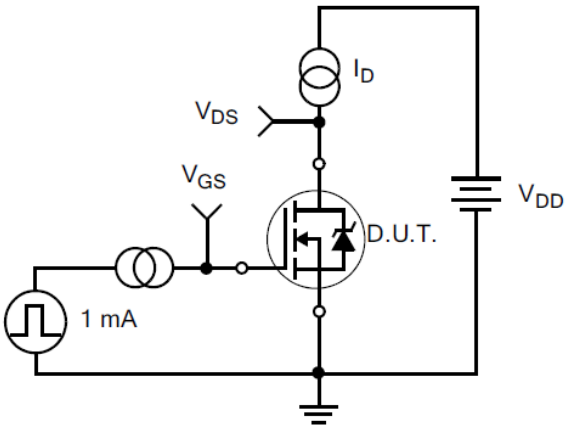



Figure 17. Gate Charge Test Circuit

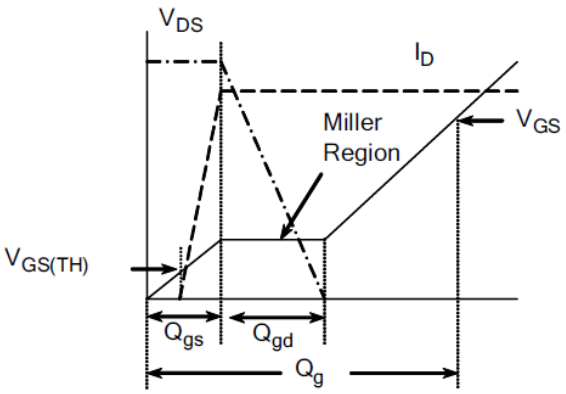



Figure 18. Gate Charge Waveform

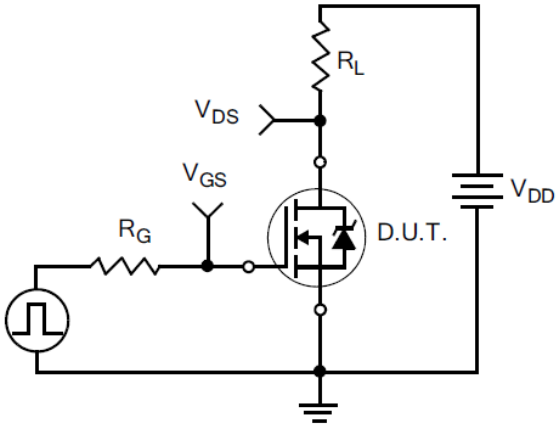



Figure 19. Resistive Switching Test Circuit

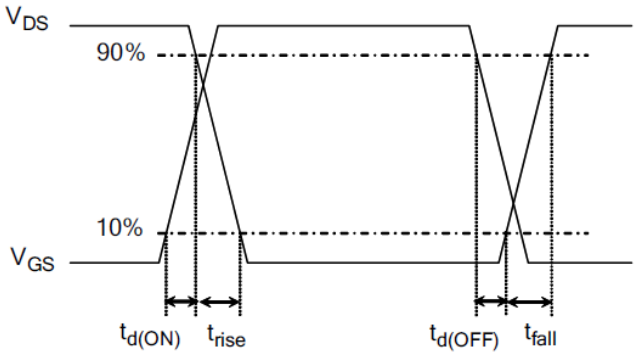



Figure 20. Resistive Switching Waveforms

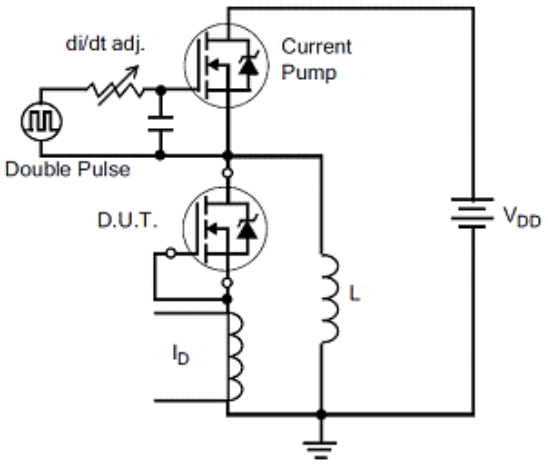



Figure 21. Diode Reverse Recovery Test Circuit

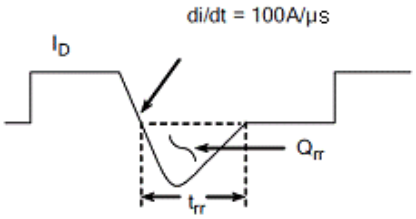



Figure 22. Diode Reverse Recovery Waveform

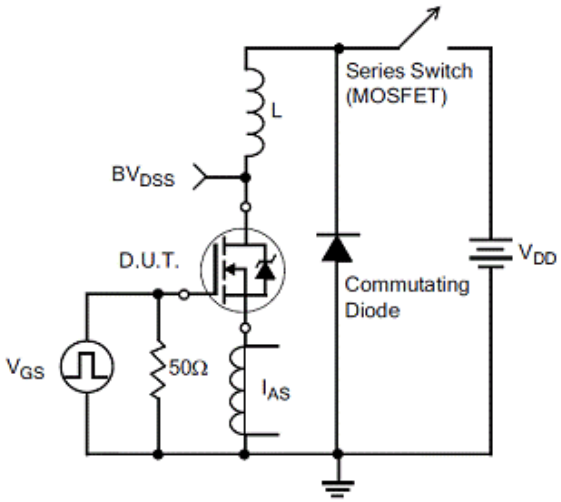



Figure 23. Unclamped Inductive Switching Test Circuit

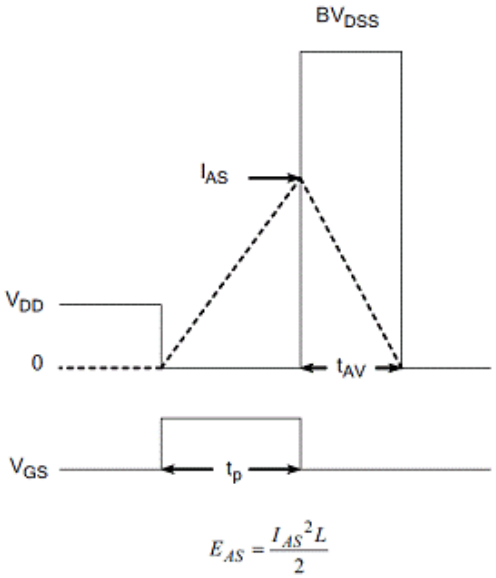
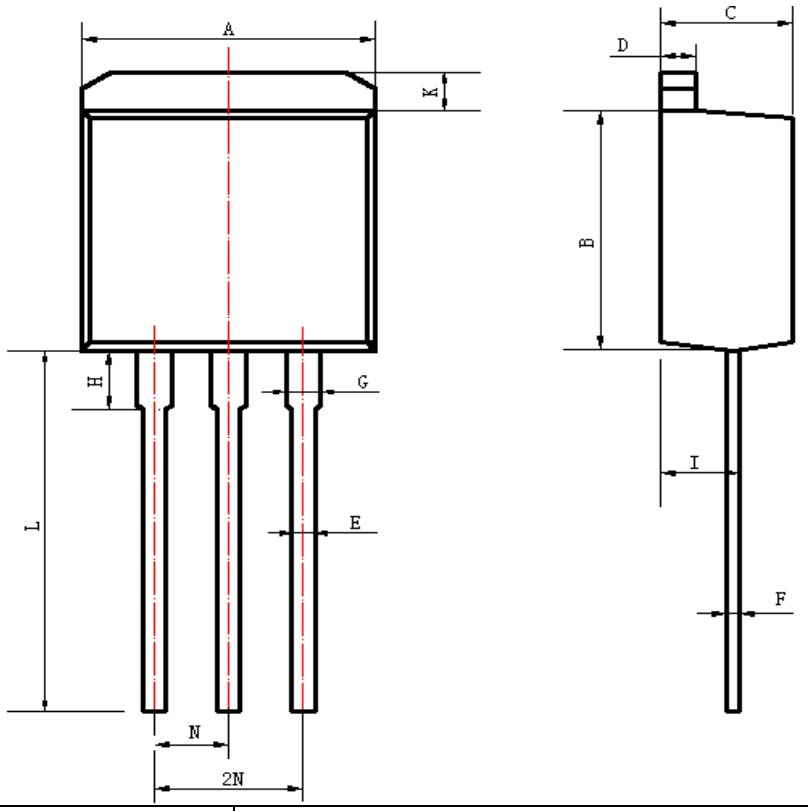



Figure 24. Unclamped Inductive Switching Waveforms

## Package Information




| Items | Values(mm) | |
|---|---|---|
| | MIN | MAX |
| A | 9.80 | 10.40 |
| B | 8.90 | 9.50 |
| C | 4.30 | 4.80 |
| D | 1.15 | 1.40 |
| E | 0.70 | 0.91 |
| F | 0.28 | 0.55 |
| G | 1.07 | 1.47 |
| H | 3.37 | 3.77 |
| I | 2.50 | 2.90 |
| K | 0.90 | 1.40 |
| L | 12.7 | 14.7 |
| N | 2.35 | 2.70 |

TO-262 Package

**The name and content of poisonous and harmful material in products**

| Part's Name | Hazardous Substance | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|
| | Pb | Hg | Cd | Cr(VI) | PBB | PBDE | DIBP | DEHP | DBP | BBP |
| Limit | ≤0.1% | ≤0.1% | ≤ 0.01% | ≤0.1% | ≤0.1% | ≤0.1% | ≤0.1% | ≤0.1% | ≤0.1% | ≤0.1% |
| Lead Frame | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ |
| Molding | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ |
| Chip | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ |
| Wire Bonding | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ |
| Solder | × | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ | ○ |
| Note | ○: Means the hazardous material is under the criterion of 2011/65/EU.<br>×: Means the hazardous material exceeds the criterion of 2011/65/EU.<br>The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS. | | | | | | | | | |

## Warnings

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

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