Features

- Uses CRM(CQ) advanced SkyMOS4 technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent Q_gx$R_{DS(on)}$ product(FOM)
- Qualified according to JEDEC criteria

Applications

- Synchronous Rectification for AC/DC Quick Charger
- UPS (Uninterrupible Power Supplies)

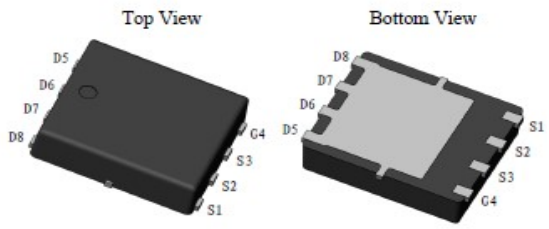
Product Summary

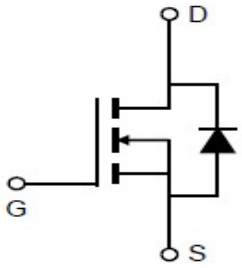
V_{DS}	100V
$R_{DS(on)@10V\ typ}$	7mΩ
I_D	80A

100% Avalanche Tested

100% DVDS Tested

RoHS HF





Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRSM080N10N4	080N10N4	DFN5X6	Tape&Reel	N/A	N/A	5000pcs

Absolute Maximum Ratings

Parameter	**Symbol**	**Value**	**Unit**
Drain-source voltage	V_{DS}	100	V
Continuous drain current T_C = 25°C(Silicon limit) T_C = 25°C(Package limit) T_C = 100°C(Silicon limit))	I_D	 81 80 51	A
Pulsed drain current (T_C = 25°C, t_p limited by T_{jmax})	$I_{D\ pulse}$	320	A
Avalanche energy, single pulse (I_L=0.3mH, Rg=25Ω)	E_{AS}	65	mJ
Gate-Source voltage	V_{GS}	±20	V
Power dissipation (T_C = 25°C)	P_{tot}	114	W
Operating junction and storage temperature	T_j , T_{stg}	-55...+150	°C
Soldering temperature, wave soldering only allowed at leads (1.6mm from case for 10s)	Tsold	260	°C

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Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – case.	RthJC	-	0.80	1.10	°C/W	-
Thermal resistance, junction - ambient(min. footprint)	RthJA	-	-	50	°C/W	-

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	**Symbol**	**Value**			**Unit**	**Test Condition**
		min.	**typ.**	**max.**		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	100	-	-	V	V_{GS}=0V, I_D=250uA
Gate threshold voltage	$V_{GS(th)}$	2.2	3	3.8	V	V_{DS}=V_{GS},I_D=250uA
Zero gate voltage drain current	I_{DSS}	- -	- -	1 100	μA	V_{DS}=100V,V_{GS}=0V T_j=25°C T_j=150°C
Gate-source leakage current	I_{GSS}		-	±100	nA	V_{GS}=±20V,V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	4	7.0	8.4	mΩ	VGS=10V,I_D=50A
Transconductance	g_{fs}	20	92	100	S	V_{DS}=5V,I_D=50A
Dynamic Characteristic						
Input Capacitance	C_{iss}	1200	2303	3455	pF	V_{GS}=0V, V_{DS}=50V, f=1MHz
Output Capacitance	C_{oss}	200	350	525		
Reverse Transfer Capacitance	C_{rss}	5	30	60		
Gate Total Charge	Q_G	5	34	50	nC	V_{GS}=10V, V_{DS}=50V, I_D=50A, f=1MHz
Gate-Source charge	Q_{gs}	2	15	23		
Gate-Drain charge	Q_{gd}	0	4	8		
Turn-on delay time	$t_{d(on)}$	10	18	40	ns	V_{GS}=10V, V_{DD}=50V, R_{G_ext}=3.0Ω
Rise time	t_r	20	77	150		
Turn-off delay time	$t_{d(off)}$	10	24	48		
Fall time	t_f	20	51	110		
Gate resistance	R_G	0.3	1.1	2.2	Ω	V_{GS}=0V, V_{DS}=0V, f=1MHz

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Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	0.5	0.91	1.5	V	V_{GS}=0V,I_{SD}=50A
Body Diode Continuous Forward Current	Is	-	-	80	A	TC = 25°C
Body Diode Pulsed Current	Is pulse	-	-	320	A	TC = 25°C
Body Diode Reverse Recovery Time	t_{rr}	20	49	97	ns	I_F=50A, dI/dt=100A/μs
Body Diode Reverse Recovery Charge	Q_{rr}	40	88	176	nC	

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Typical Performance Characteristics

Fig 1: Typ. Output Characteristics

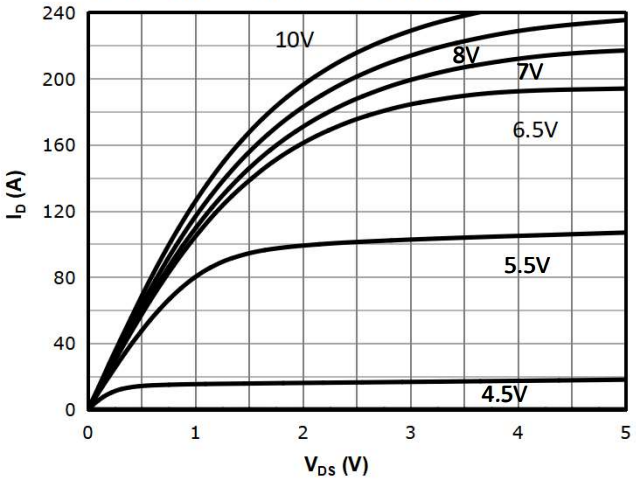
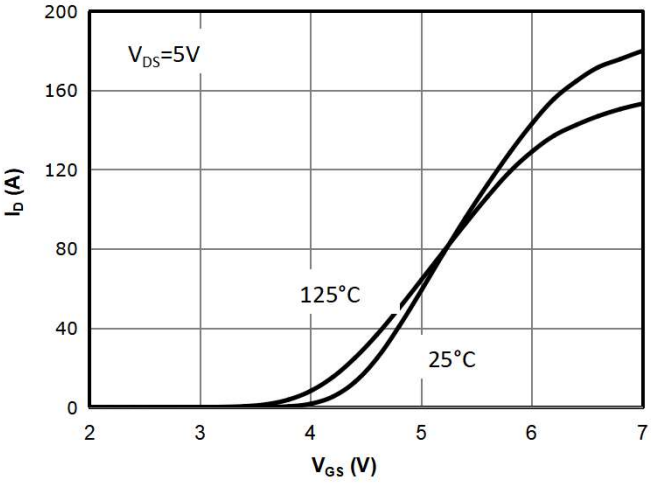


Fig 2: Typ. Transfer Characteristics



ig 3: Typ. Rds(on) vs Drain Current and Gate Voltage

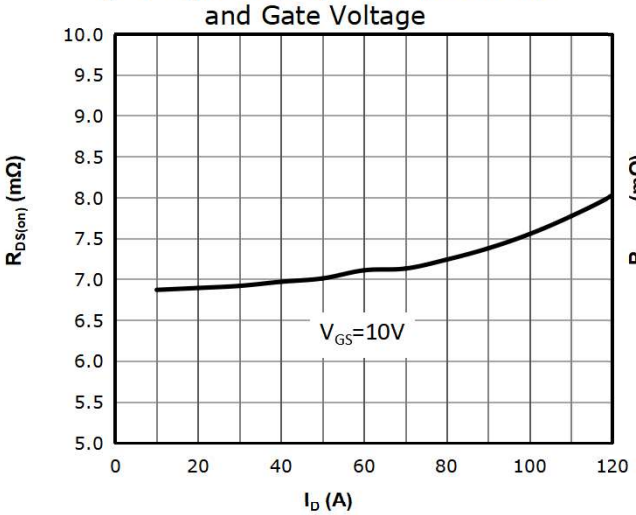


Fig 4: Typ. Rds(on) vs Gate Voltage

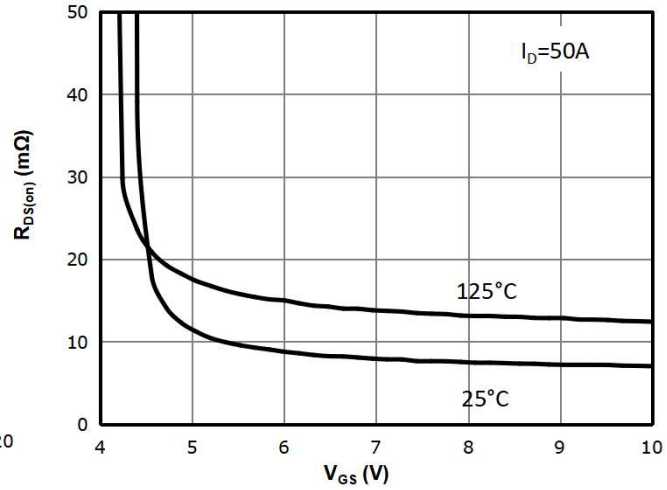


Fig 5: Rds(on) vs. Temperature

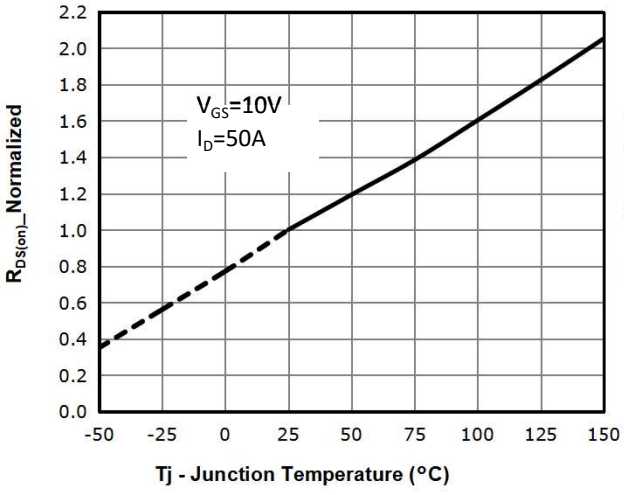
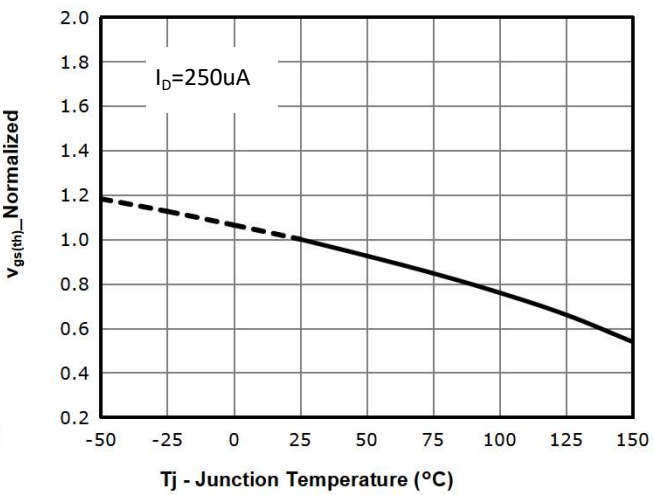


Fig 6: Vgs(th) vs. Temperature



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Fig 7: BVds vs. Temperature

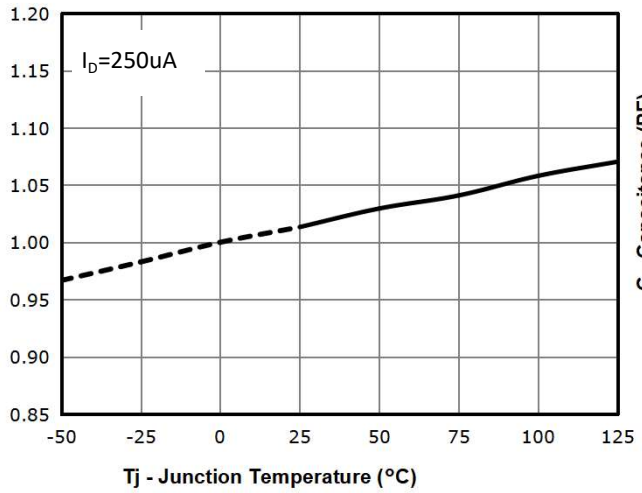


Fig 8: Typ. Cap Characteristics

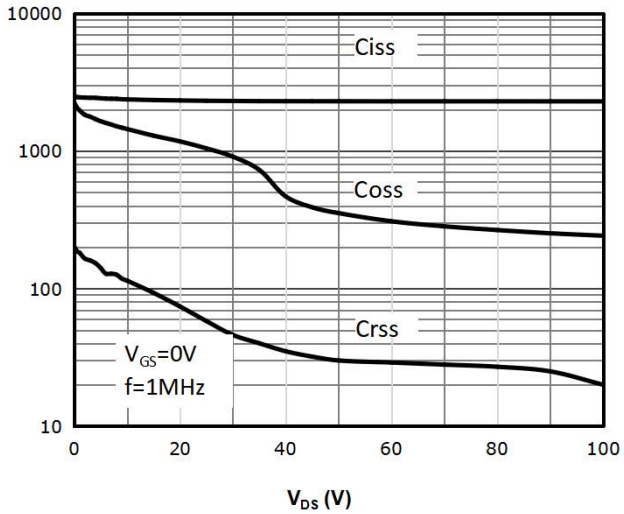


Fig 9: Typ. Gate Charge Characteristics

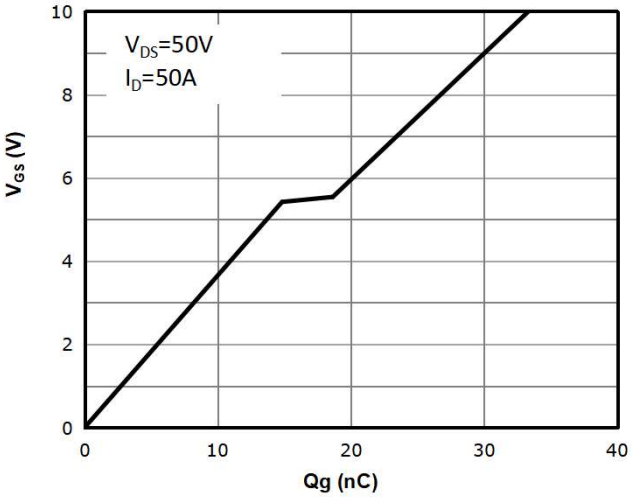


Fig 10: Typ. Body-diode Forward Characteristics

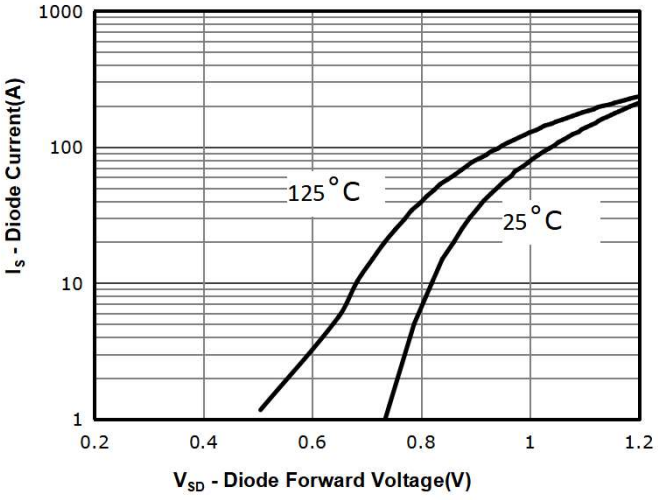


Fig 11: Power Dissipation

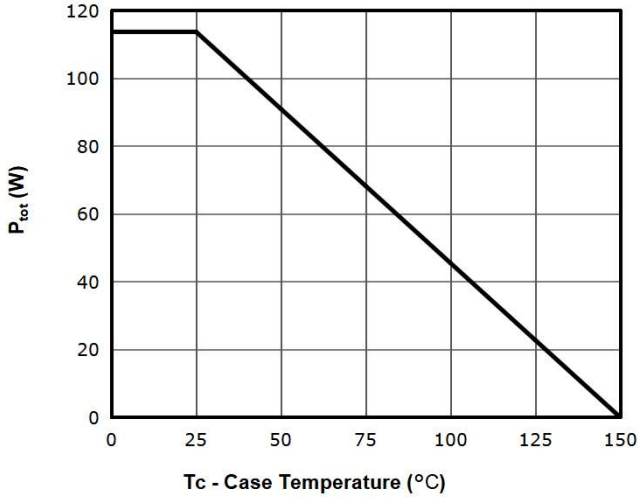
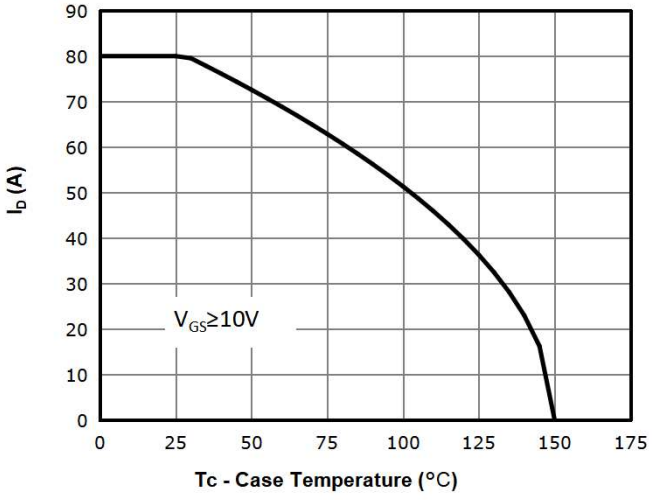


Fig 12: Drain Current Derating



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Fig 13: Safe Operating Area

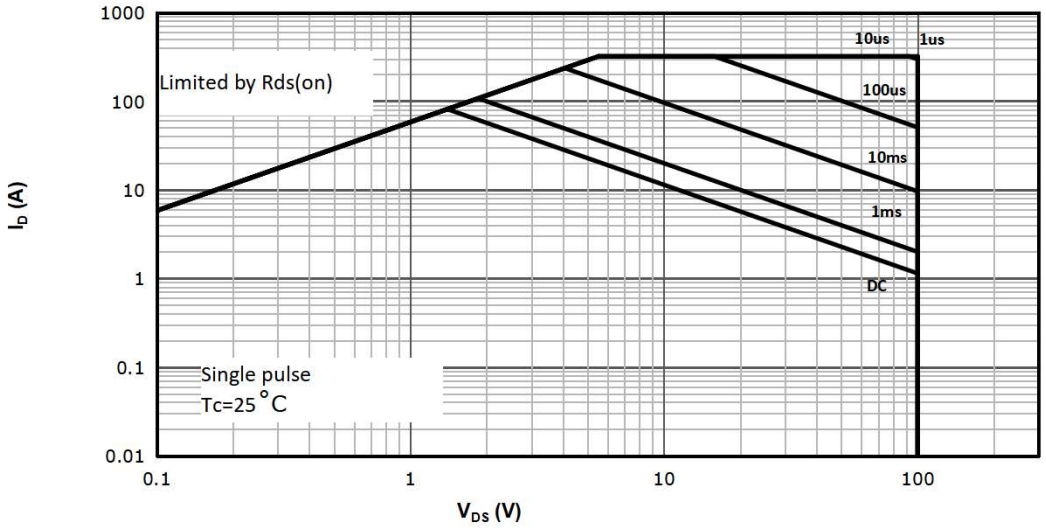
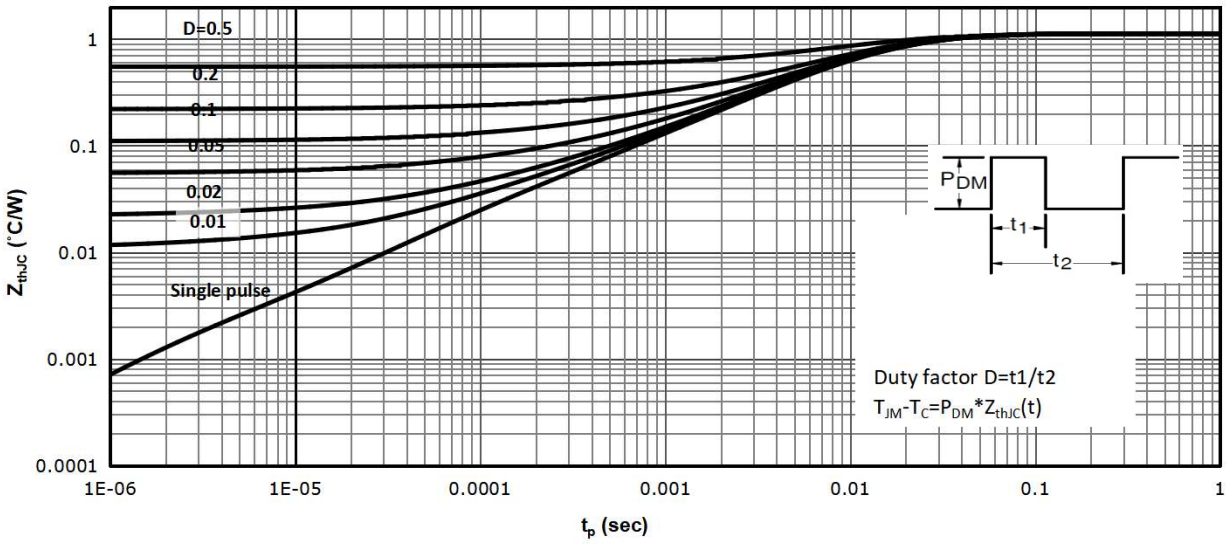


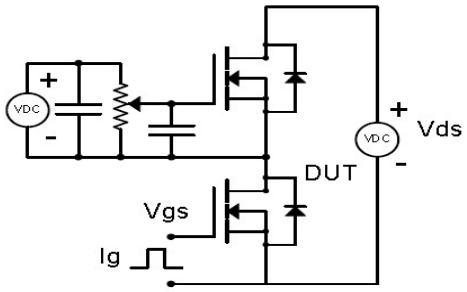
Fig 14: Max. Transient Thermal Impedance

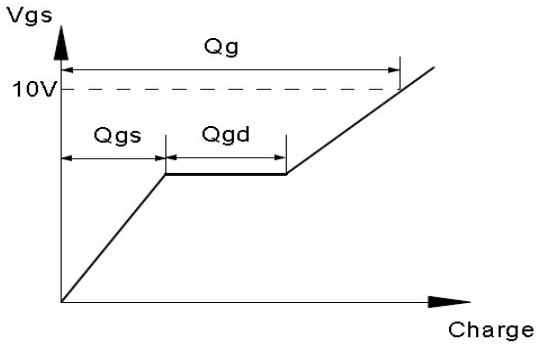


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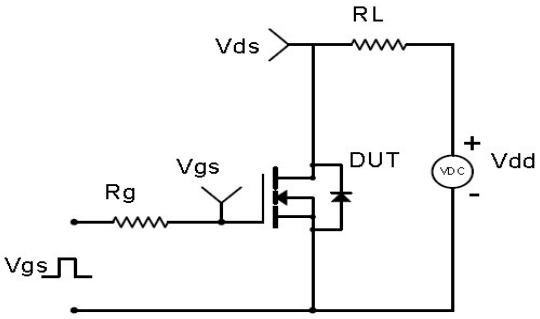
Test Circuit & Waveform

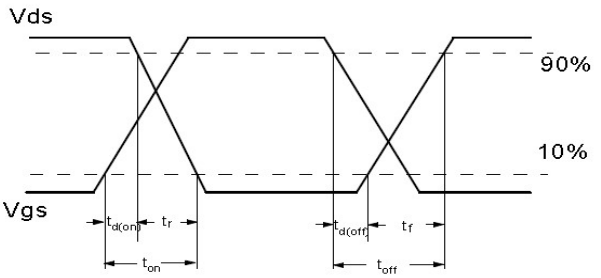
Gate Charge Test Circuit & Waveform



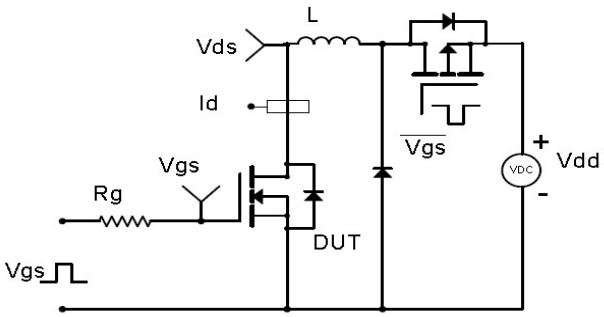


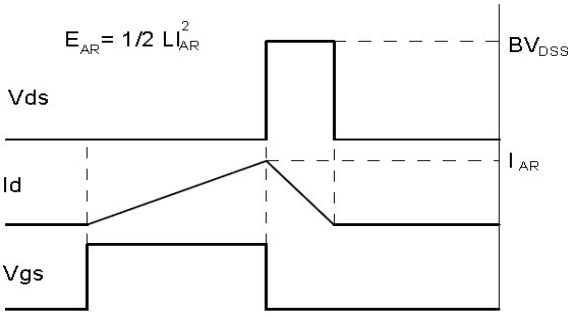
Resistive Switching Test Circuit & Waveforms



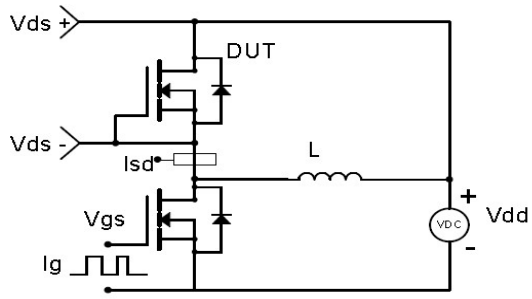


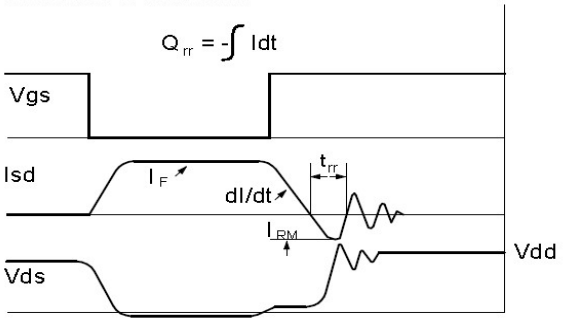
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms





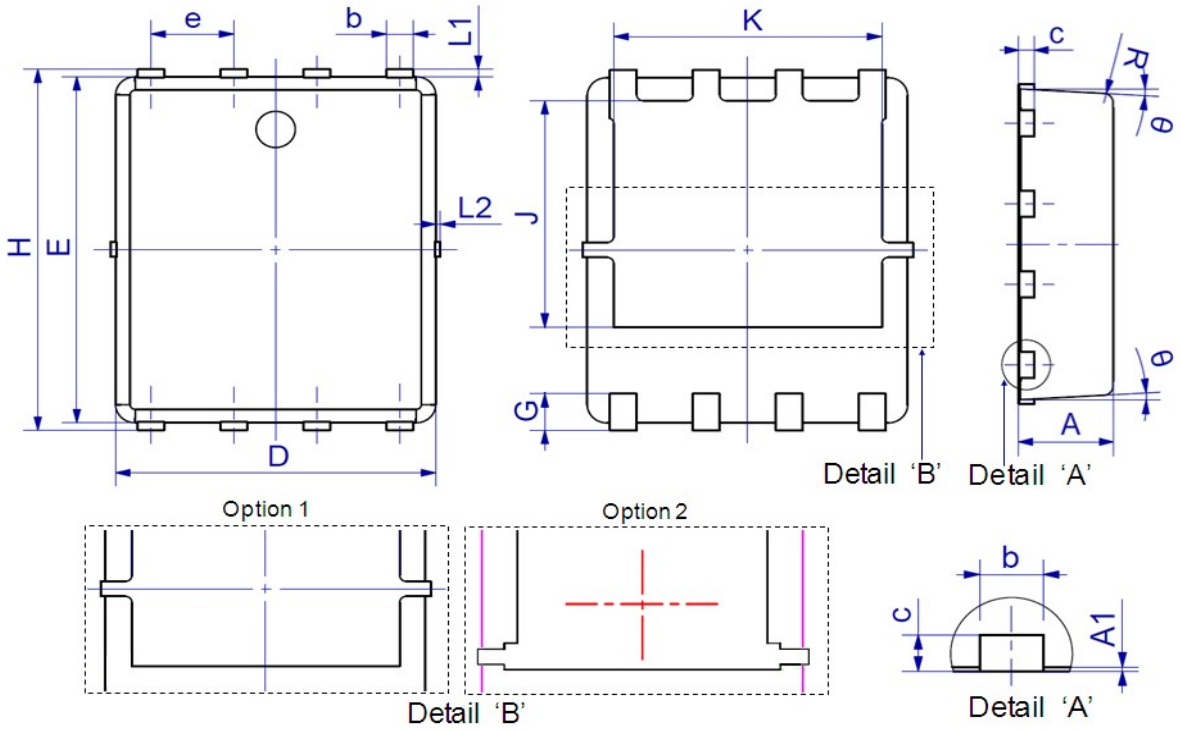
Diode Recovery Test Circuit & Waveforms





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Package Outline: DFN5X6



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.80	1.20	0.031	0.047
A1	0.00	0.05	0.000	0.002
b	0.30	0.51	0.012	0.020
c	0.15	0.35	0.006	0.014
D	4.80	5.40	0.189	0.213
e	1.27 BSC		0.050 BSC	
E	5.66	6.06	0.223	0.239
G	0.30	0.71	0.012	0.028
H	5.90	6.35	0.232	0.250
J	3.32	3.92	0.131	0.154
K	3.61	4.25	0.142	0.167
L1	0.05	0.25	0.002	0.010
L2	0.00	0.15	0.000	0.006
R	0.25 REF		0.010 REF	
θ	0°	12°	0°	12°

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Marking



NOTE:
NXBBAAAAY

N	—Wire Bond code
X	—Assembly location code
BB	—Fab code
AAAA	—Lot code
Y	—Bin code

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Revision History

Revison	Date	Major changes
1.1	2025/1/3	Change SOA

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qulified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semicondutor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM(CQ) reserves the right to improve product design, function and reliability without notice.

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