CRMM4902D

60V Dual Power MOSFET

Features

- Uses CRM(CQ) advanced Trench technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent $Q_g x R_{DS(on)}$ product(FOM)

Product Summary

Symbol	N-Ch1	N-Ch2
V_{DS}	60V	60V
$R_{DS(on)\ typ.}$	42mΩ	42mΩ
I_D	6A	6A

Applications

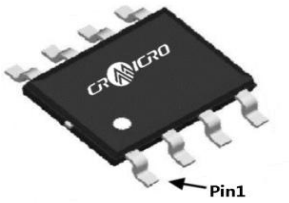
- Motor drive

100% DVDS Tested

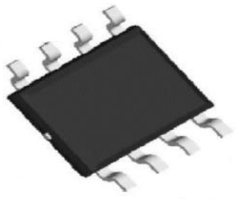
100% Avalanche Tested

RoHS HF

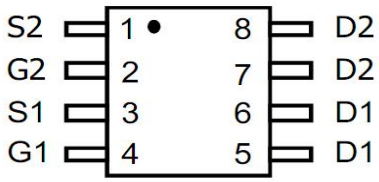
SOP-8



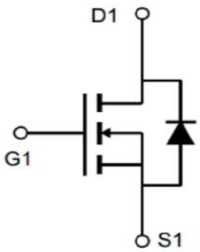
Top View



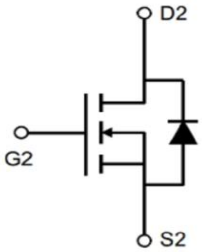
Bottom View



Top View



N-Ch1



N-Ch2

Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRMM4902D	CRMM4902D	SOP-8	Taping	N/A	N/A	4000pcs

Absolute Maximum Ratings

Parameter	Symbol	Maximum		Unit
		N-Ch1	N-Ch2	
Drain-source voltage	V_{DS}	60	60	V
Continuous drain current T_C = 25°C (Silicon limit)	I_D	6	6	A
Continuous drain current T_C = 25°C (Package limit)	I_D	6	6	A
Continuous drain current T_C = 100°C (Silicon limit)	I_D	4	4	A
Pulsed drain current (T_C = 25°C, t_p limited by T_{jmax})	$I_{D\ pulse}$	23	23	A
Avalanche energy, single pulse (L=0.5mH, Rg=25Ω)	E_{AS}	42	42	mJ
Gate-Source voltage	V_{GS}	±20	±20	V
Power dissipation (T_C = 25°C)	P_{tot}	3.2	3.2	W
Operating junction and storage temperature	T_j , T_{stg}	-55...+150		°C

Thermal Resistance

Parameter	Symbol	Typ	Max	Unit
Thermal resistance, junction – to-Lead.	R_{thJL}	27.5	38.5	°C/W
SMD version, device on PCB [1] Thermal resistance, junction – ambient(min. footprint)	R_{thJA}	79.0	102.7	°C/W

NOTE:
1.The value of RθJA is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C. The value in any given application depends on the user's specific board design.

N-Ch1&N-Ch2 Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	60	-	-	V	V_{GS}=0V, I_D=250uA
Gate threshold voltage	$V_{GS(th)}$	1.6	2.1	2.6	V	V_{DS}=V_{GS}, I_D=250uA
Zero gate voltage drain current	I_{DSS}	- -	0.08 -	1 5	μA	V_{DS}=60V, V_{GS}=0V T_j=25°C T_j=125°C
Gate-source leakage current	I_{GSS}	-	±10	±100	nA	V_{GS}=±20V, V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	- -	52.0 42.0	62.4 50.4	mΩ	V_{GS}=4.5V, I_D=5A V_{GS}=10V, I_D=5A
Transconductance	g_{fs}	-	12.5	-	S	V_{DS}=5V, I_D=5A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	493	-	pF	V_{GS}=0V, V_{DS}=60V, f=1MHz
Output Capacitance	C_{oss}	-	45	-		
Reverse Transfer Capacitance	C_{rss}	-	35	-		
Gate Total Charge	Q_G	-	13.4	-	nC	V_{GS}=10V, V_{DS}=60V, I_D=5A, f=1MHz
Gate-Source charge	Q_{gs}	-	2.6	-		
Gate-Drain charge	Q_{gd}	-	4.5	-		
Turn-on delay time	$t_{d(on)}$	-	5.8	-	ns	V_{GS}=10V, V_{DD}=30V, R_{G_ext}=2.7Ω, ID=5A
Rise time	t_r	-	11.4	-		
Turn-off delay time	$t_{d(off)}$	-	14.8	-		
Fall time	t_f	-	14.2	-		
Gate resistance	R_G	-	2.0	-	Ω	V_{GS}=0V, V_{DS}=0V, f=1MHz

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	0.84	1.20	V	V_{GS}=0V, I_{SD}=5A
Body Diode Reverse Recovery Time	t_{rr}	-	21.0	-	ns	I_F=5A, dI/dt=100A/μs
Body Diode Reverse Recovery Charge	Q_{rr}	-	15.1	-	nC	

N-Ch1&N-Ch2 Typical Performance Characteristics

Fig 1: Output Characteristics

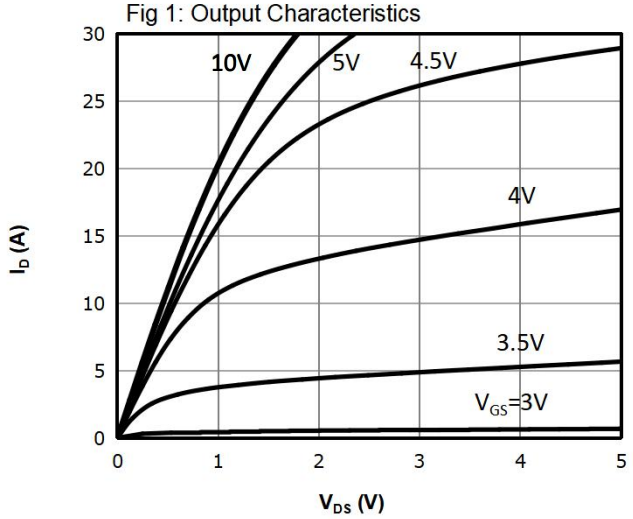


Fig 2: Transfer Characteristics

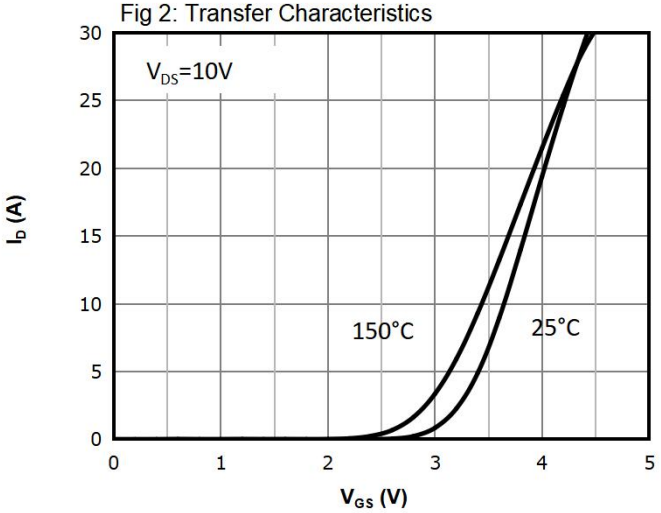


Fig 3: Rds(on) vs Drain Current and Gate Voltage

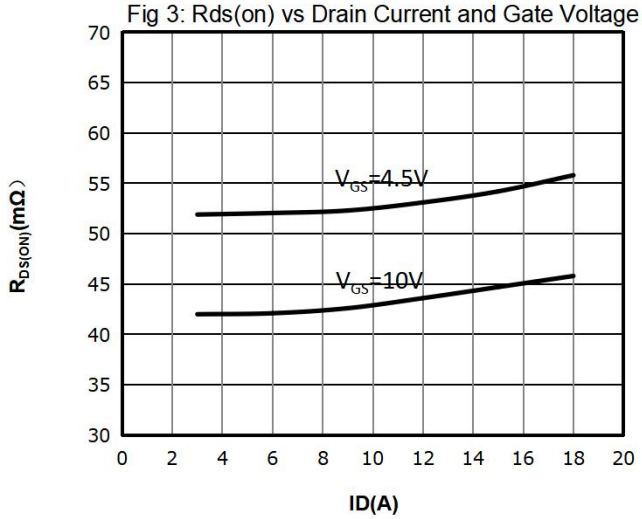


Fig 4: Rds(on) vs Gate Voltage

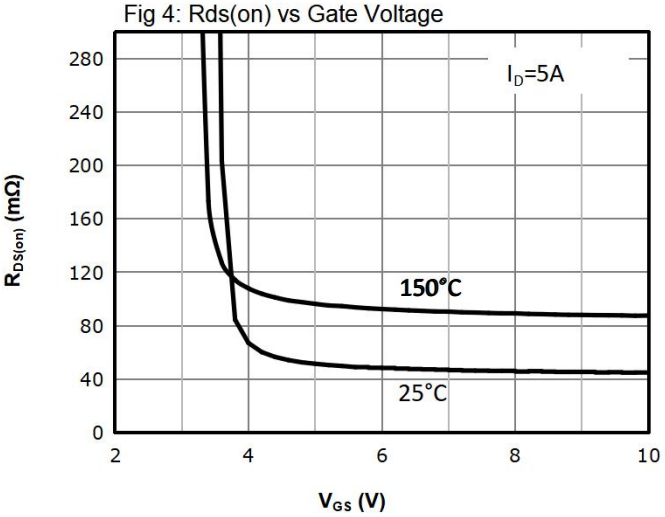


Fig 5: Rds(on) vs. Temperature

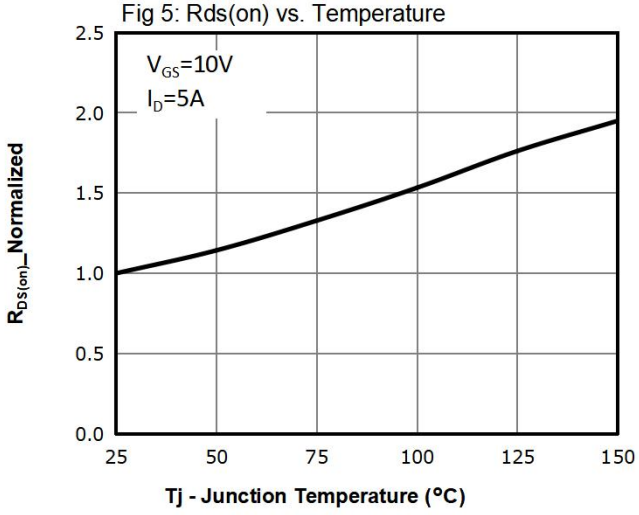


Fig 6: Capacitance Characteristics

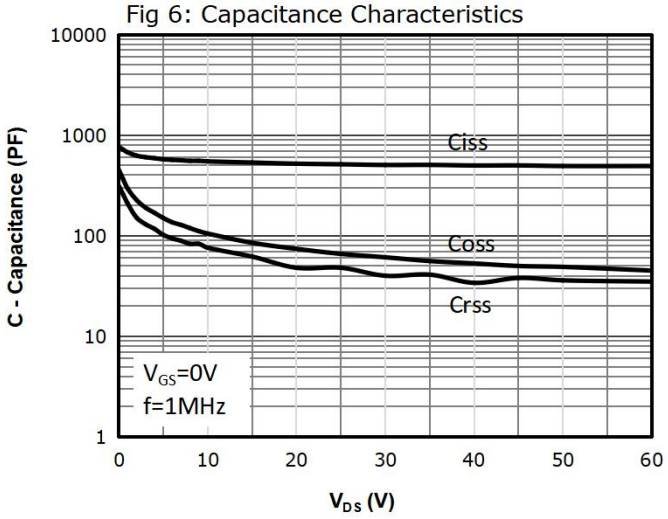


Fig 7: Gate Charge Characteristics

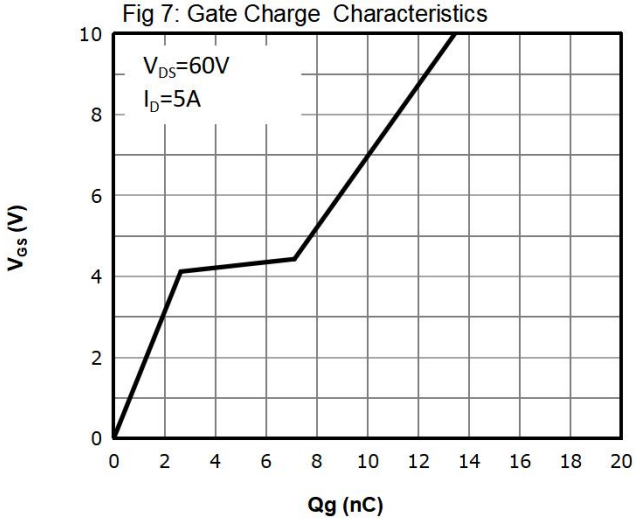


Fig 8: Body-diode Forward Characteristics

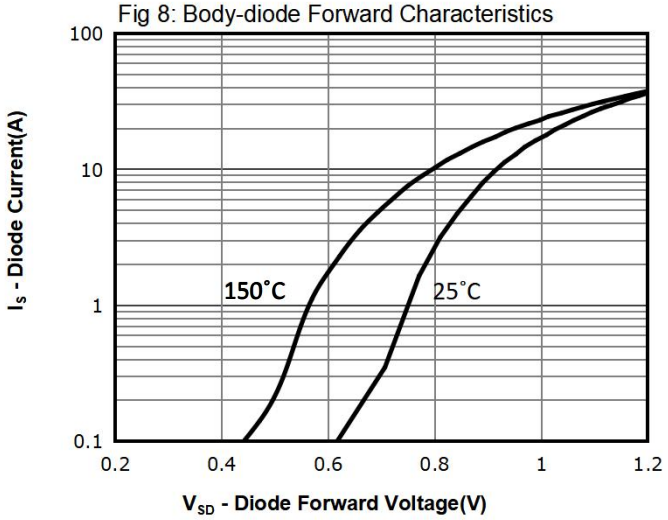


Fig 9: Safe Operating Area

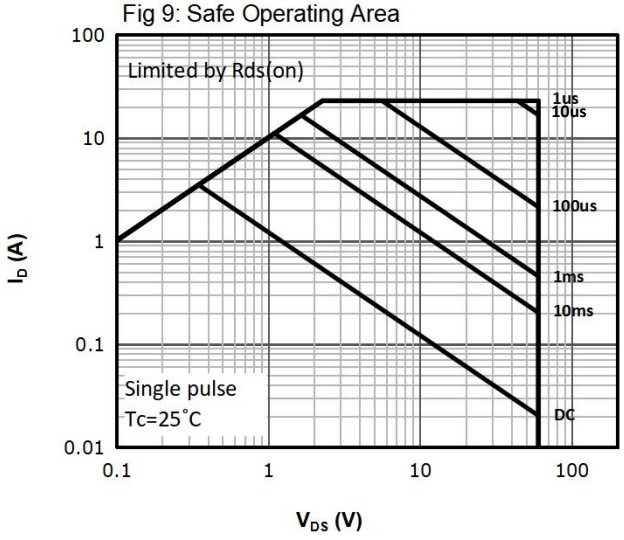
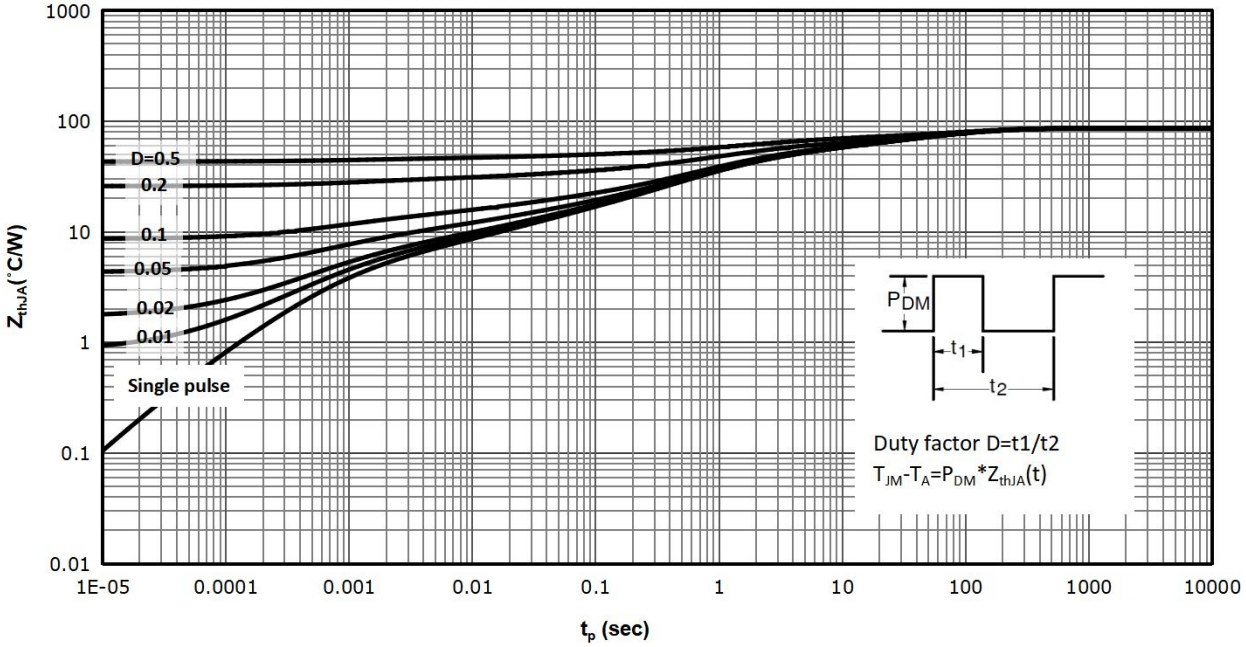
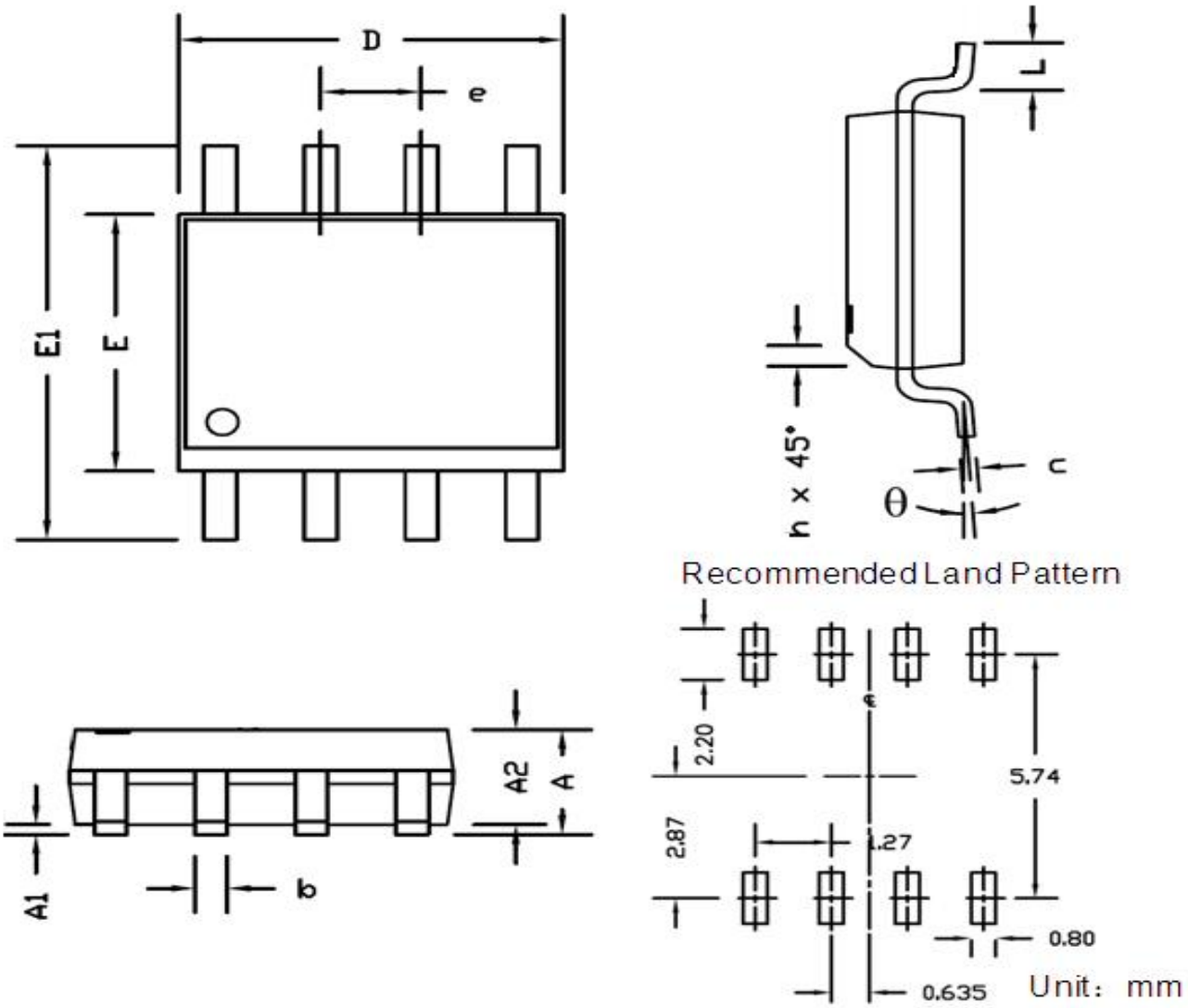


Fig 10: Max. Transient Thermal Impedance



Package Outline: SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
A2	1.25	1.65	0.049	0.065
b	0.33	0.51	0.013	0.020
c	0.17	0.25	0.007	0.010
D	4.80	5.00	0.189	0.197
e	1.27 BSC.		0.050 BSC.	
E	3.80	4.00	0.150	0.157
E1	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.40	1.27	0.016	0.050
θ	0°	8°	0°	8°

Revision History

Revison	Date	Major changes
1.1	2022/12/20	Release of formal version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qulified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semicondutor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM(CQ) reserves the right to improve product design, function and reliability without notice.