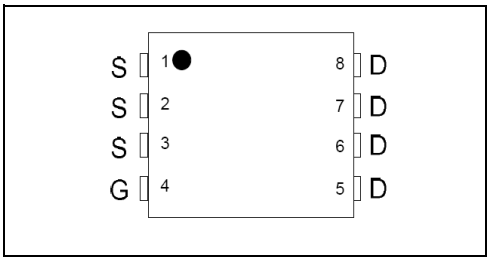
Silicon N-Channel Power MOSFET

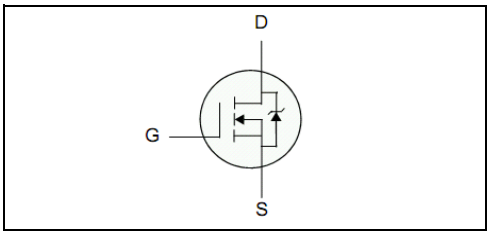
HGR065NE4A

V_{DSS}	45	V
I_D	65	A
P_D	35.2	W
$R_{DS(ON)Typ}$	5	mΩ

General Description:

HGR065NE4A, the silicon N-channel Enhanced VDMOSFETs, is obtained by the high density Trench technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. This device is suitable for use as A load switch and PWM applications. the package form is PDFN3.3×3.3, which accords with the RoHS standard.





Features:

- Fast Switching
- Low ON Resistance
- Low Gate Charge
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test

Applications:

Power switch circuit of adaptor and charger.

Absolute（T_j= 25℃ unless otherwise specified）

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-to-Source Voltage	45	V
I_D	Continuous Drain Current T_C = 25 °C	65	A
	Continuous Drain Current T_C = 100 °C	43.5	A
I_{DM} [a1]	Pulsed Drain Current T_C = 25 °C	260	A
V_{GS}	Gate-to-Source Voltage	±18	V
E_{AS} [a2]	Avalanche Energy	115.5	mJ
P_D	Power Dissipation T_C = 25 °C	35.2	W
	Derating Factor above 25°C	0.28	W/°C
T_J, T_{stg}	Operating Junction and Storage Temperature Range	150，–55 to 150	℃

Electrical Characteristics（T_j= 25℃ unless otherwise specified）：

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	45	--	--	V
I_{DSS}	Drain to Source Leakage Current	VDS =45V, VGS= 0V, Tj = 25℃	--	--	1	μA
		VDS =36V, VGS= 0V, Tj = 125℃	--	--	100	
$I_{GSS(F)}$	Gate to Source Forward Leakage	VGS=18V	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	VGS=-18V	--	--	-100	nA

ON Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	VGS=10V,ID=19A	--	5	6.5	mΩ
		VGS=4.5V,ID=19A	--	7.3	9.5	mΩ
$V_{GS(TH)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	1	1.8	2.5	V
g_{fs}	Transconductance	VDS=5V,Id=19A	--	58.2	--	S
Pulse width tp≤300μs,δ≤2%						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1MHz	--	2.6	--	Ω
C_{iss}	Input Capacitance	VGS = 0V VDS = 25V f = 1.0MHz	--	1162	--	pF
C_{oss}	Output Capacitance		--	622	--	
C_{rss}	Reverse Transfer Capacitance		--	56.9	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	VGS=10V Rg=3Ω VDD=25V Id=19A	--	9.5	--	ns
tr	Rise Time		--	7.4	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	31.2	--	
t_f	Fall Time		--	8.6	--	
Q_g	Total Gate Charge	VGS = 10V VDD=25V Id=19A	--	23.2	--	nC
Q_{gs}	Gate to Source Charge		--	3	--	
Q_{gd}	Gate to Drain (“Miller”)Charge		--	6.5	--	

Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I_S	Continuous Source Current (Body Diode)	T_C = 25 °C	--	--	65	A
I_{SM}	Maximum Pulsed Current (Body Diode)		--	--	260	A
V_{SD}	Diode Forward Voltage	IS=19A,VGS=0V	--	--	1.2	V
trr	Reverse Recovery Time	IS=19A di/dt=100A/us, VGS=0V	--	37.3	--	ns
Qrr	Reverse Recovery Charge		--	29.9	--	nC

Pulse width tp≤300μs, δ ≤2%

Symbol	Parameter	Max.	Units
$R_{\theta JC}$	Junction-to-Case	3.55	℃/W
$R_{\theta JA}$	Junction-to-Ambient	62.5	℃/W

a1：Repetitive rating; pulse width limited by maximum junction temperature

a2：L=0.5mH,Ias=21.5A Start TJ=25℃

a3：Recommend soldering temperature defined by IPC/JEDEC J-STD 020

Characteristics Curve:

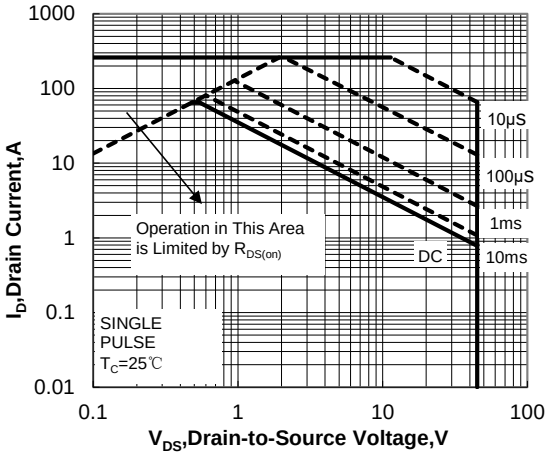


Figure 1 . Maximum Safe Operating Area

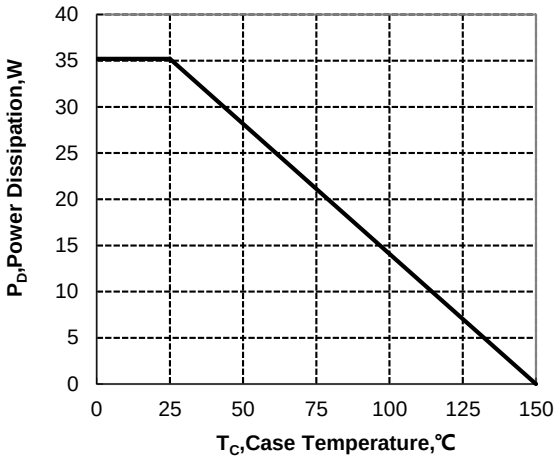


Figure 2. Maximum Power Dissipation vs Case Temperature

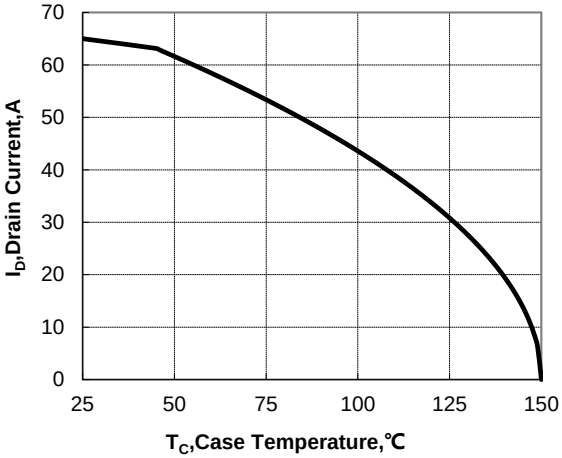


Figure 3. Maximum Continuous Drain Current vs Case Temperature

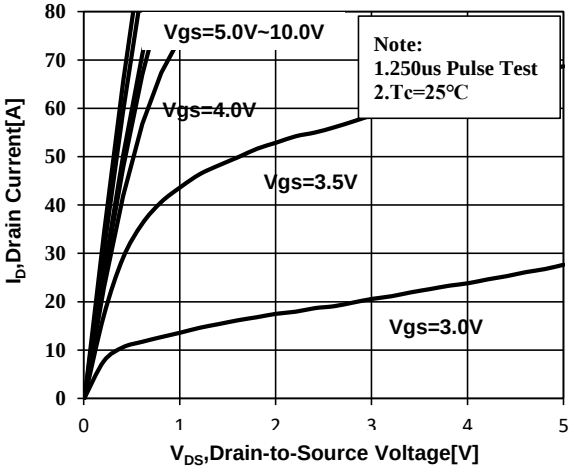


Figure 4. Typical output Characteristics

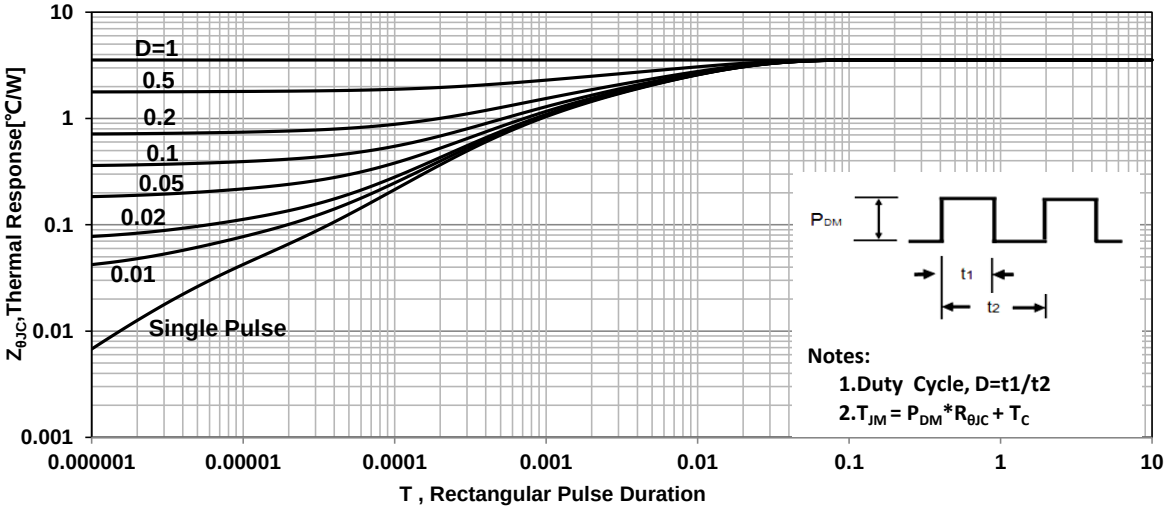


Figure 5 Maximum Effective Thermal Impedance , Junction to Case

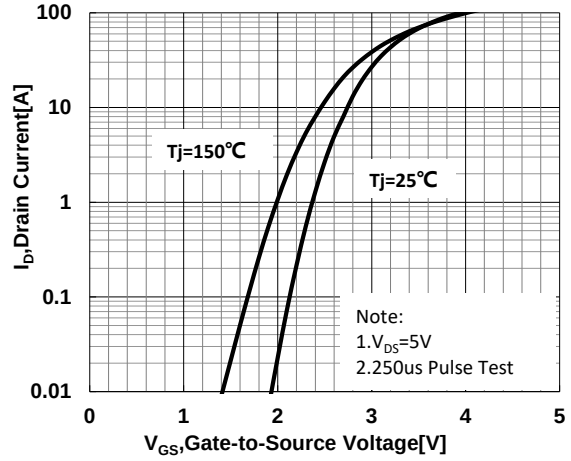


Figure 6 Typical Transfer Characteristics

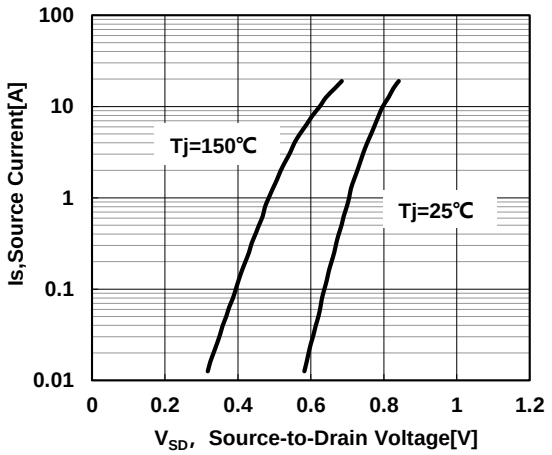


Figure 7 Typical Body Diode Transfer Characteristics

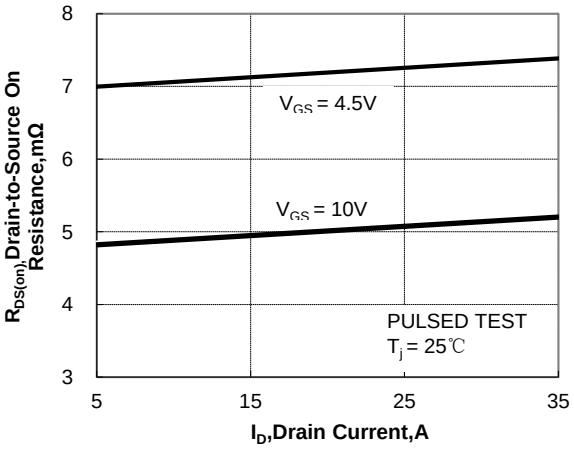


Figure 8. Drain-to-Source On Resistance vs Drain Current

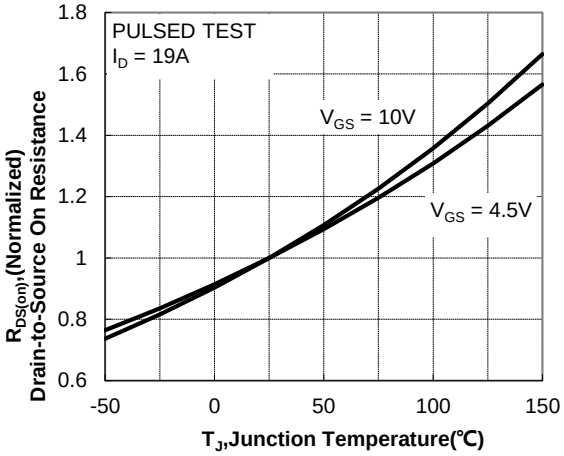


Figure 9. Normalized On Resistance vs Junction Temperature

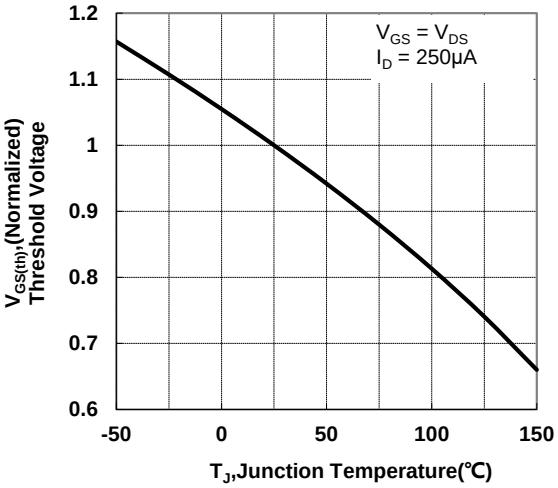


Figure10. Normalized Threshold Voltage vs Junction Temperature

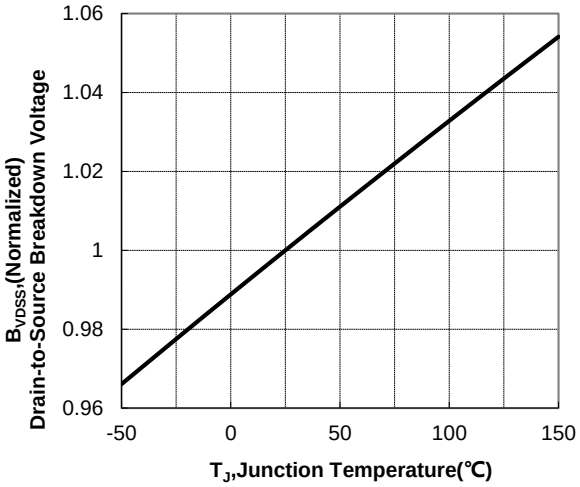


Figure 11. Normalized Breakdown Voltage vs Junction Temperature

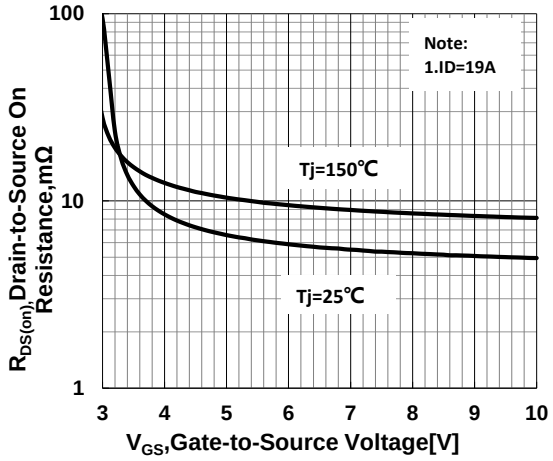


Figure 12. Rds(on) vs Gate Voltage

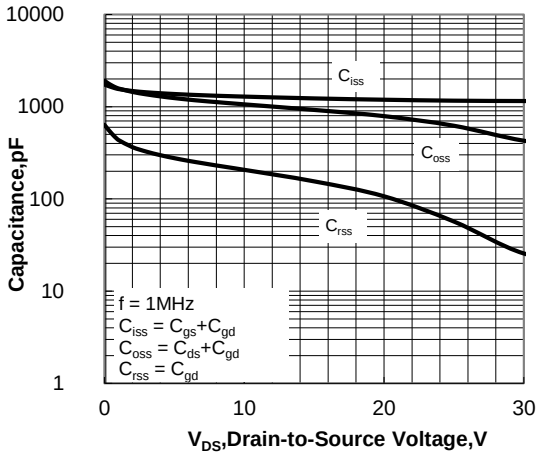


Figure 13. Capacitance Characteristics

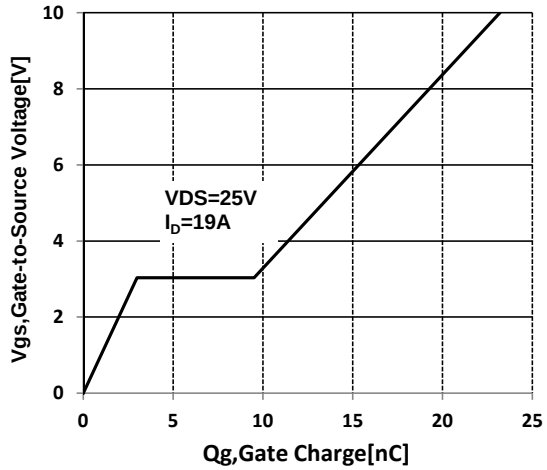


Figure 14 Typical Gate Charge vs Gate to Source Voltage

Test Circuit and Waveform

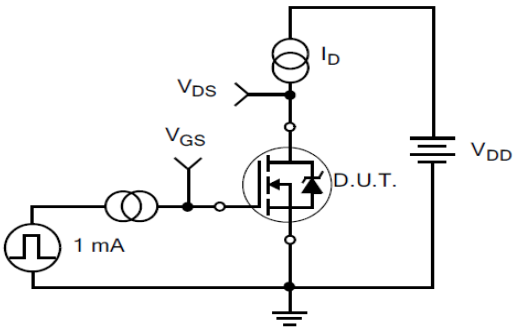


Figure 15. Gate Charge Test Circuit

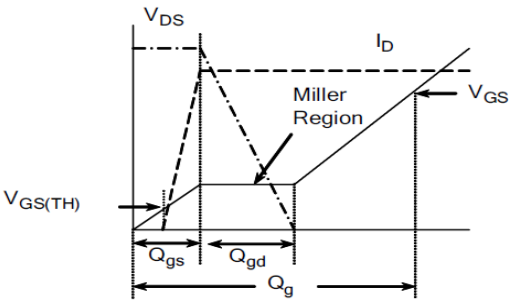


Figure 16. Gate Charge Waveforms

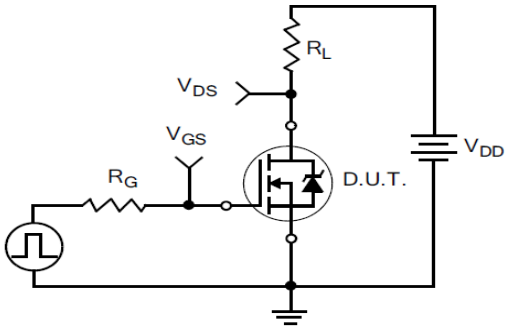


Figure 17. Resistive Switching Test Circuit Waveforms

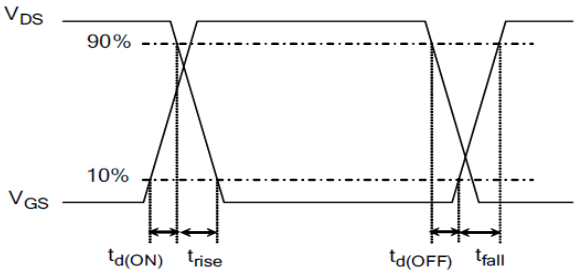


Figure 18. Resistive Switching

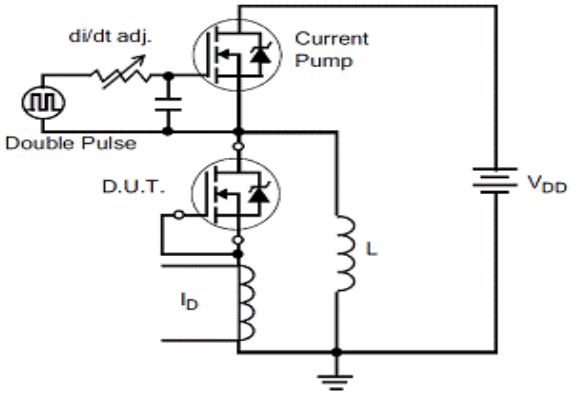


Figure 19. Diode Reverse Recovery Test Circuit Waveform

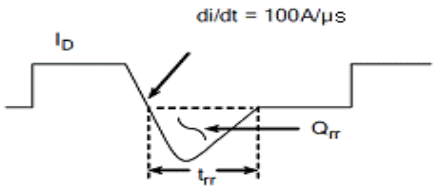


Figure 20. Diode Reverse Recovery

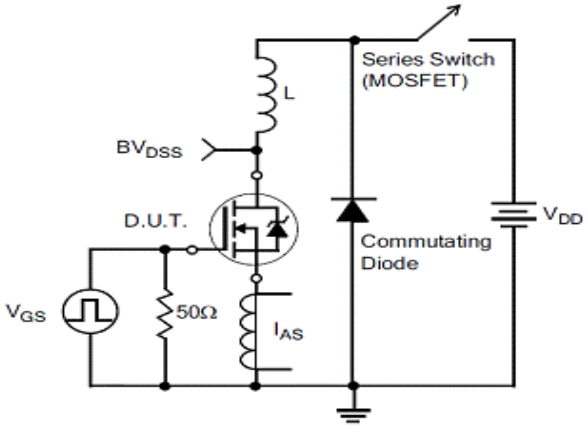


Figure21.Unclamped Inductive Switching Test Circuit Waveform

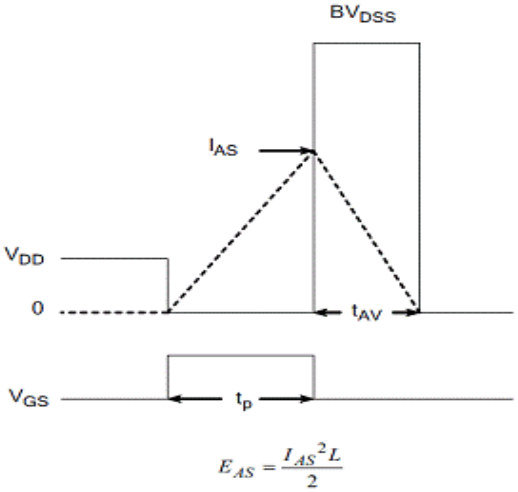
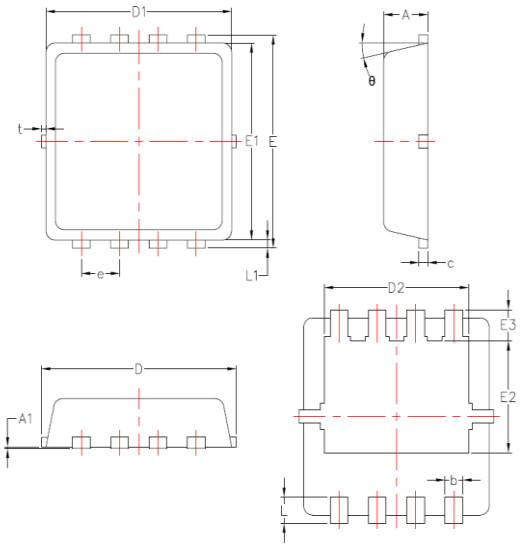


Figure22.Unclamped Inductive Switching

Package Information：



Symbol	Values(mm)	
	MIN	MAX
A	0.7	0.9
A1	/	0.05
b	0.2	0.4
c	0.1	0.25
D	3.05	3.55
D1	2.9	3.35
D2	2.15	2.65
E	3.05	3.55
E1	2.8	3.3
E2	1.54	1.94
E3	0.18	0.65
e	0.55	0.75
L	0.2	0.6
L1	0.05	0.25
t	/	0.15
θ	8°	14°

PDFN3.3×3.3 Package

The name and content of poisonous and harmful material in products

Part's Name	Hazardous Substance									
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE	DIBP	DEHP	DBP	BBP
Limit	≤0.1%	≤0.1%	≤ 0.01%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○	○	○	○	○
Molding	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○	○	○	○	○
Electric glue	○	○	○	○	○	○	○	○	○	○
Note	○：Means the hazardous material is under the criterion of 2011/65/EU. ×：Means the hazardous material exceeds the criterion of 2011/65/EU. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.									

Warnings

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

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