Features

- Uses CRM(CQ) advanced SkyMOS1 technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent Q_gx$R_{DS(on)}$ product(FOM)
- Qualified according to JEDEC criteria

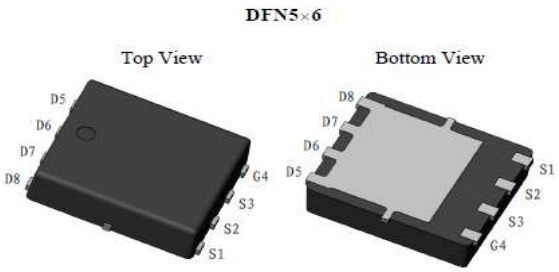
Product Summary

V_{DS}	85V
$R_{DS(on)@10V\ typ}$	5.2mΩ
I_D	80A

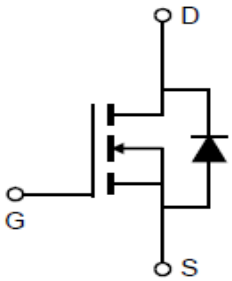
Applications

- Synchronous Rectification for AC/DC Quick Charger
- Battery management
- UPS (Uninterrupible Power Supplies)

100% Avalanche Tested



CRSM062N08N



Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRSM062N08N	-	DFN5x6	Tape&Reel	N/A	N/A	5000pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	85	V
Continuous drain current T_c = 25°C (Silicon Limit) Tc = 25°C (Package Limit) T_c = 100°C (Slicon Limit)	I_D	 86 80 55	A
Pulsed drain current (T_A = 25°C, t_p limited by T_{jmax})	$I_{D\ pulse}$	344	A
Avalanche energy, single pulse (L=0.5mH, Rg=25Ω)	E_{AS}	121	mJ
Gate-Source voltage	V_{GS}	±20	V
Power dissipation (T_A = 25°C)	P_{tot}	79.1	W
Operating junction and storage temperature	T_j , T_{stg}	-55...+150	°C

※. Notes:1.EAS is tested at starting Tj = 25℃, L = 0.5mH, IAS = 22A, VGS = 10V. IAS(max)=44A;EAS(max)=484mJ under above Conditions;

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Thermal Resistance

Parameter	Symbol	Max	Unit
Thermal resistance, junction – case.	R_{thJC}	1.58	°C/W
Thermal resistance, junction – ambient(min. footprint)	R_{thJA}	47.0	

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	85	97	-	V	V_{GS}=0V, I_D=250uA
Gate threshold voltage	$V_{GS(th)}$	2.0	3.0	4.0	V	V_{DS}=V_{GS},I_D=250uA
Zero gate voltage drain current	I_{DSS}	- -	0.05 -	1 10	μA	V_{DS}=85V,V_{GS}=0V T_j=25°C T_j=125°C
Gate-source leakage current	I_{GSS}	-	10	±100	nA	V_{GS}=±20V,V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	-	5.2	6.2	mΩ	V_{GS}=10V, I_D=50A
Transconductance	g_{fs}	-	89.7	-	S	V_{DS}=5V,I_D=50A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	2990	-	pF	V_{GS}=0V, V_{DS}=42.5V, f=1MHz
Output Capacitance	C_{oss}	-	790	-		
Reverse Transfer Capacitance	C_{rss}	-	19	-		
Gate Total Charge	Q_G	-	55.5	-	nC	V_{GS}=10V,VDS=42.5V, ID=50A, f=1MHz
Gate-Source charge	Q_{gs}	-	17.6	-		
Gate-Drain charge	Q_{gd}	-	13	-		
Turn-on delay time	$t_{d(on)}$	-	16.0	-	ns	V_{GS}=10V,V_{DD}=42.5V R_{G_ext}=3.5Ω (Note 2,3)
Rise time	t_r	-	31.0	-		
Turn-off delay time	$t_{d(off)}$	-	36.0	-		
Fall time	t_f	-	19.0	-		
Gate resistance	R_G	-	2.2	-	Ω	V_{GS}=0V, V_{DS}=0V, f=1MHz

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	0.9	1	V	V_{GS}=0V,I_{SD}=50A
Body Diode Reverse Recovery Time	t_{rr}	-	78	-	ns	I_F=50A, dI/dt=300A/μs,
Body Diode Reverse Recovery Charge	Q_{rr}	-	99	-	nC	

※. Notes
2.Pulse Test : Pulse Width ≤ 300us, duty cycle ≤ 2%.
3.Essentially independent of operating temperature.

Typical Performance Characteristics

Fig 1: Output Characteristics

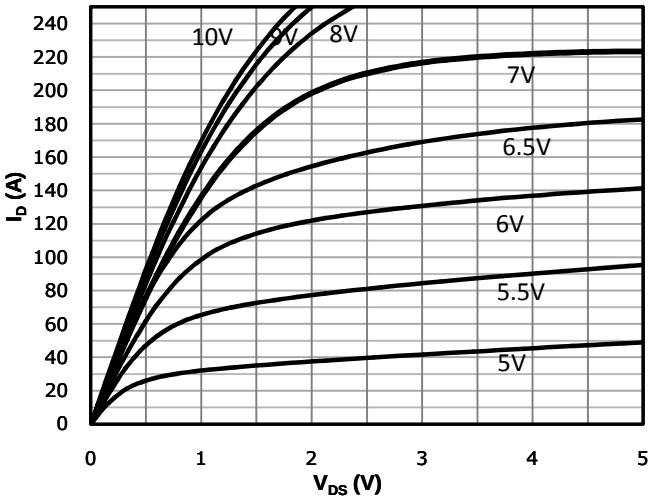


Fig 2: Transfer Characteristics

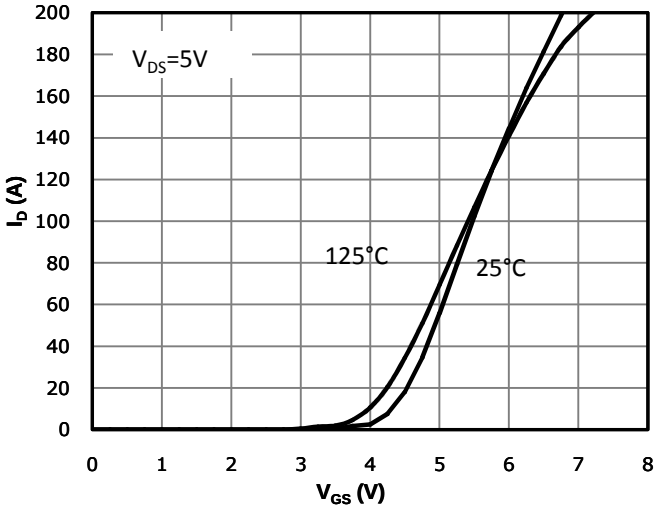


Fig 3: Rds(on) vs Drain Current and Gate Voltage

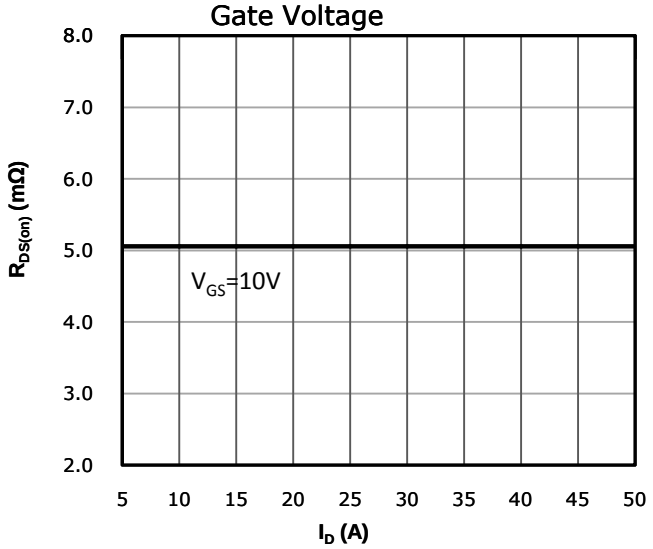


Fig 4: Rds(on) vs Gate Voltage

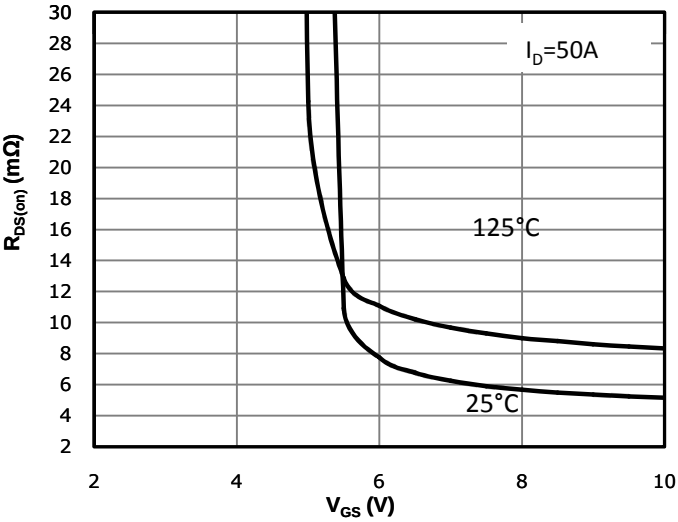


Fig 5: Rds(on) vs. Temperature

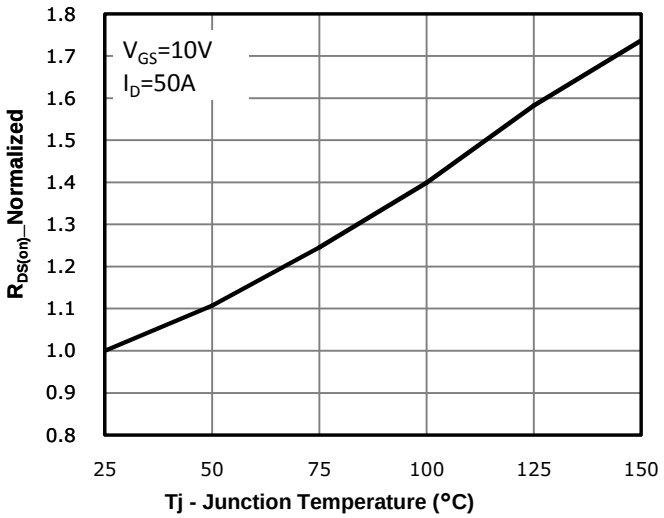


Fig 6: Capacitance Characteristics

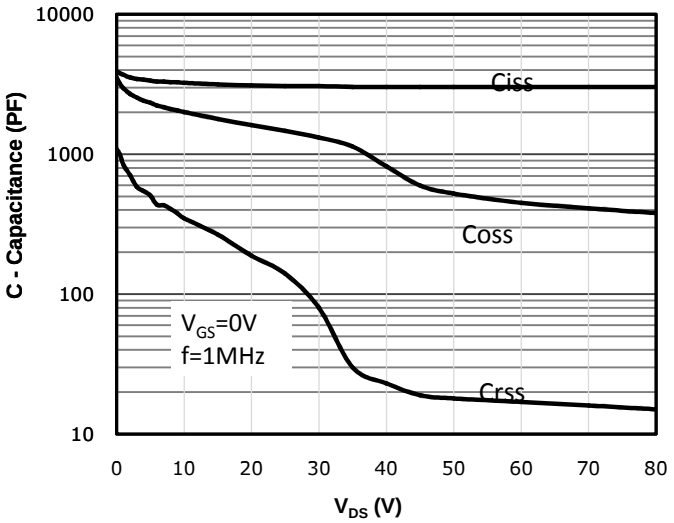


Fig 7: Gate Charge Characteristics

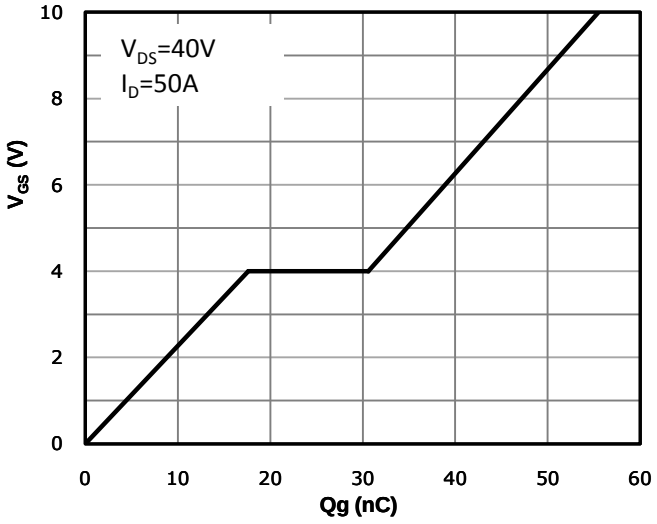


Fig 8: Body-diode Forward Characteristics

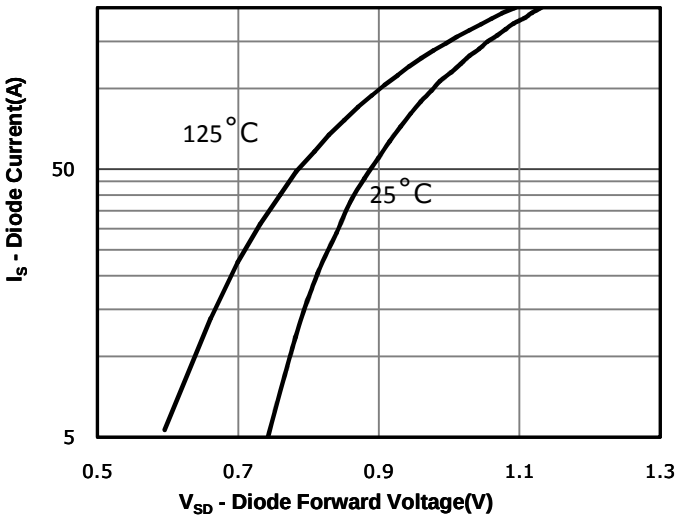


Fig 9: Power Dissipation

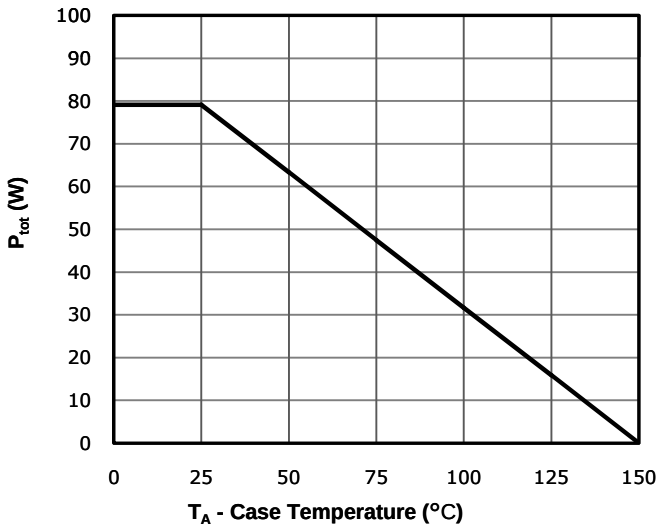


Fig 10: Drain Current Derating

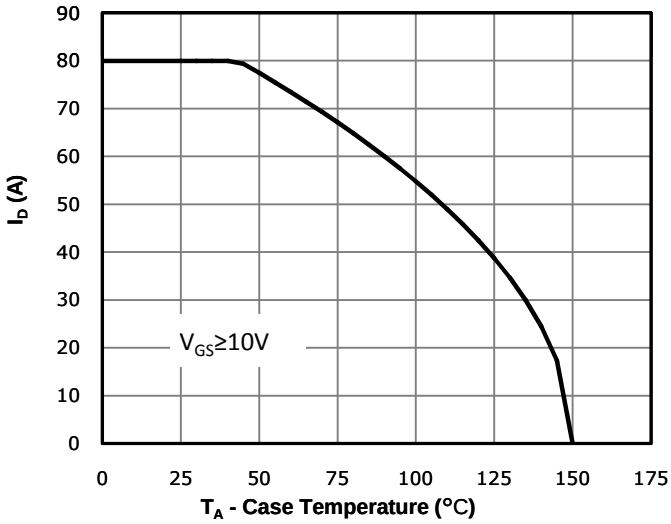


Fig 11: Safe Operating Area

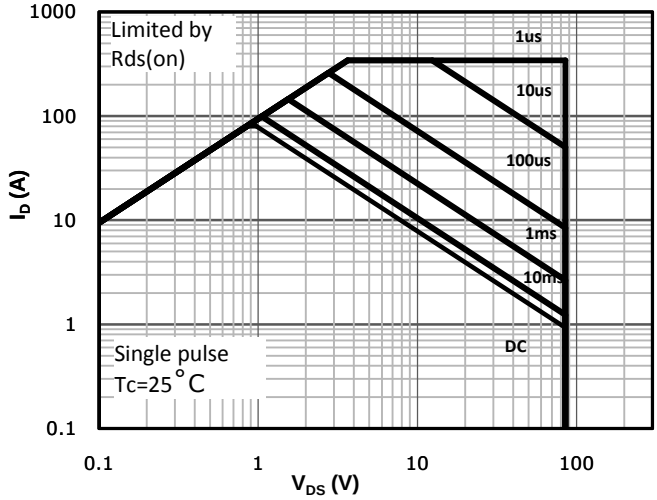
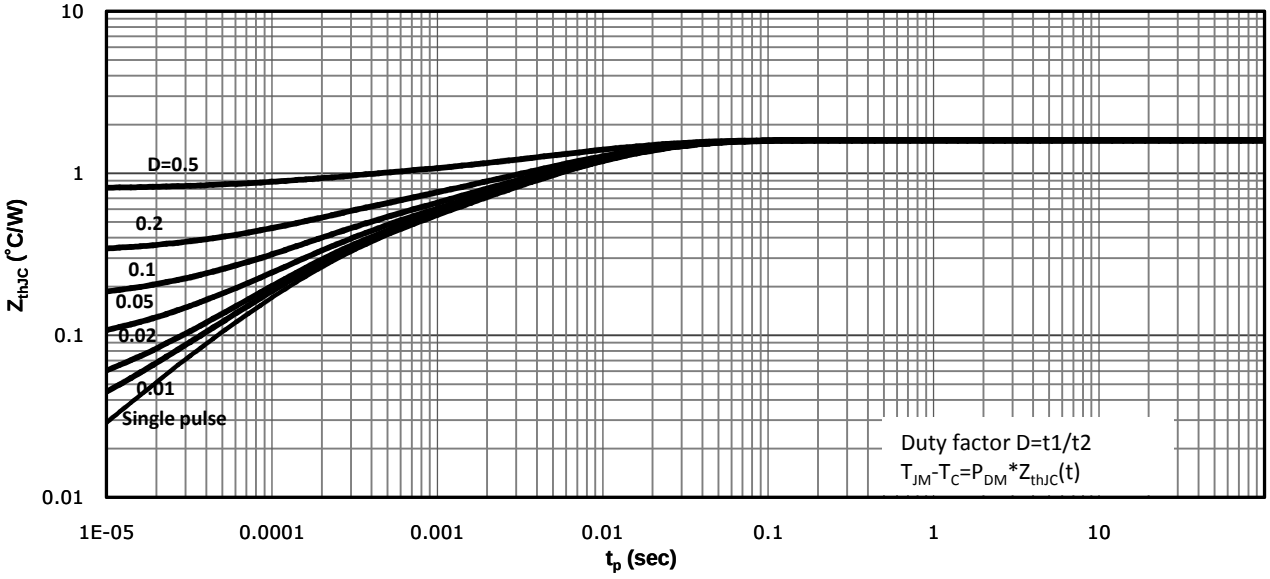
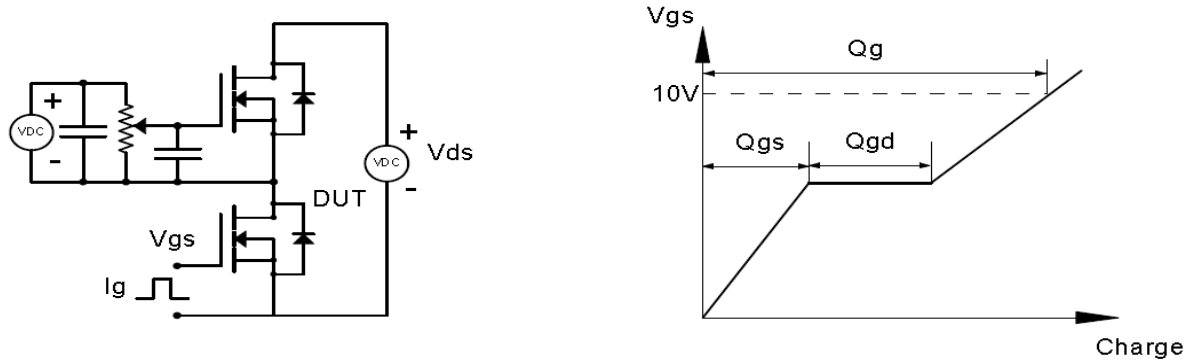


Fig 12: Max. Transient Thermal Impedance

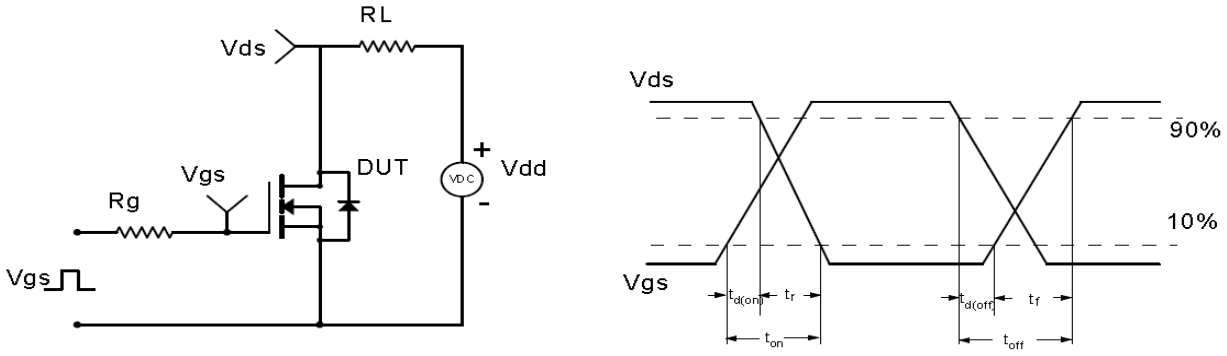


Test Circuit & Waveform

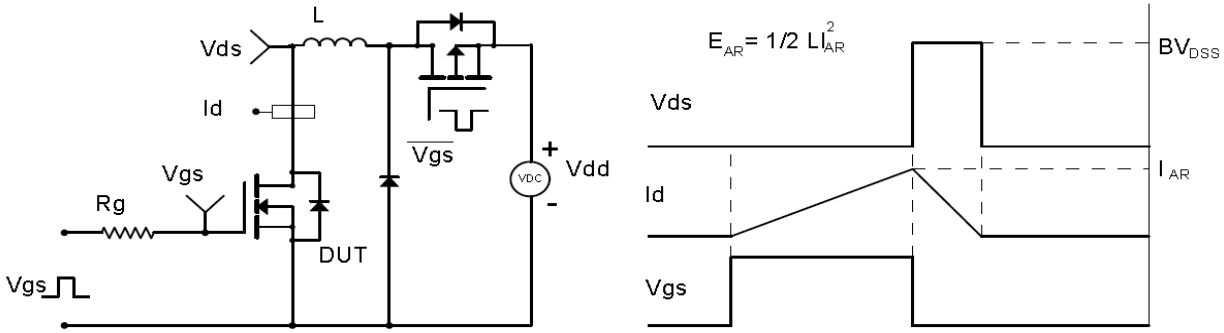
Gate Charge Test Circuit & Waveform



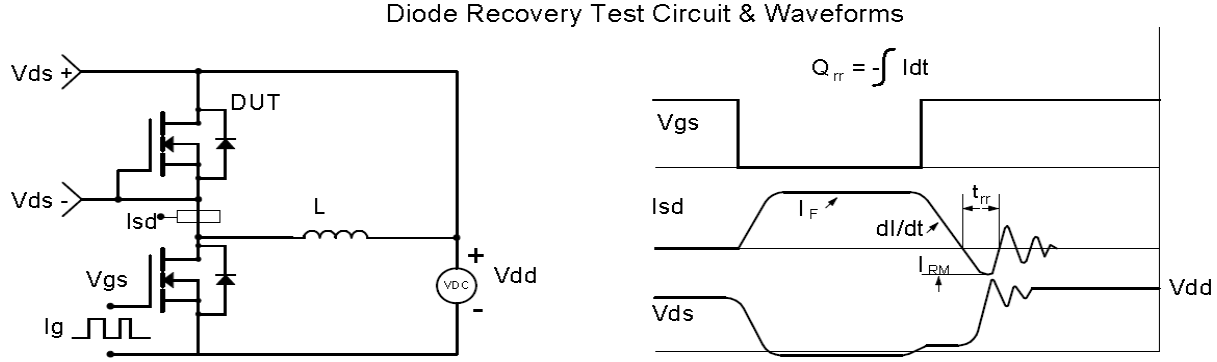
Resistive Switching Test Circuit & Waveforms



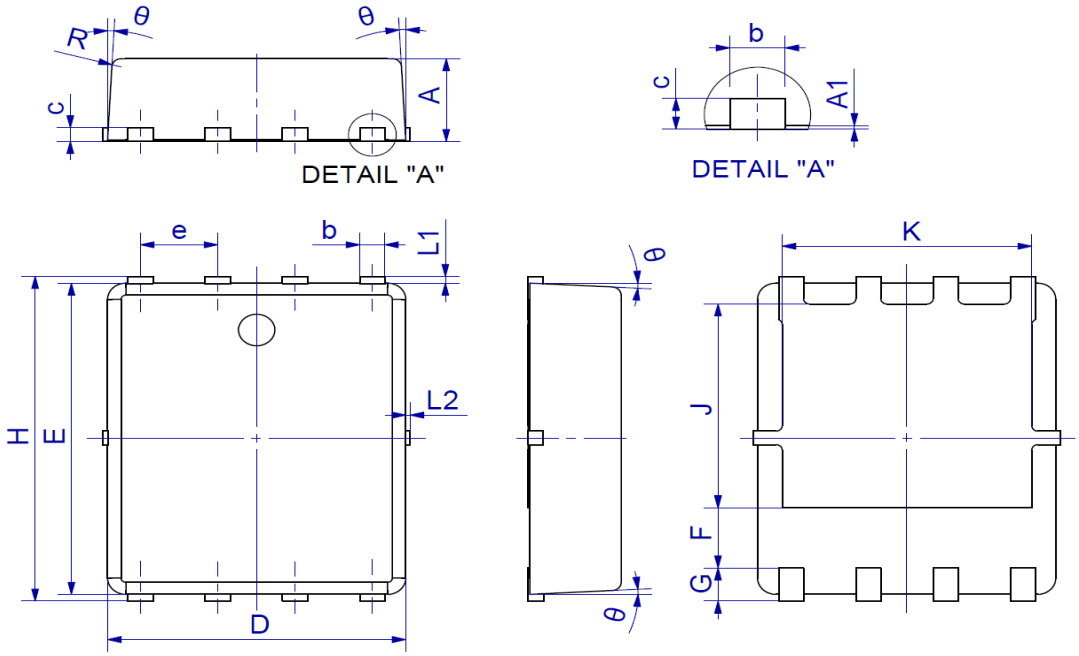
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline: DFN5*6



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.80	1.00	0.31	0.39
A1	0.00	0.05	0.00	0.02
b	0.35	0.49	0.14	0.19
c	0.254 REF.		0.1REF.	
D	4.90	5.10	1.93	2.01
E	5.70	5.90	2.24	2.32
e	1.27 BSC.		0.5 BSC.	
F	1.40 REF.		0.551 REF.	
G	0.60 REF.		0.236 REF.	
H	5.95	6.20	2.34	2.44
J	3.40 REF.		1.339 REF.	
K	4.00 REF.		1.575 REF.	
L1	0.10	0.15	0.04	0.06
L2	----	0.02	----	0.01
θ	10° TYP.		10° TYP.	

Revision History

Revison	Date	Major changes
1.0	2018-11-05	priliminary version.

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qulified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semicondutor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM(CQ) reserves the right to improve product design, function and reliability without notice.