CRTE280P06L2-G

Trench P-MOSFET -60V, 24mΩ, -11A

Features

- Uses CRM advanced Trench technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent $Q_g x R_{DS(on)}$ product(FOM)
- Qualified according to JEDEC criteria

Applications

- Motor control and drive
- Electrical tools
- Lithium battery protection

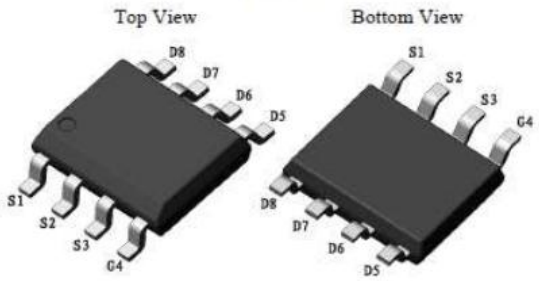
Product Summary

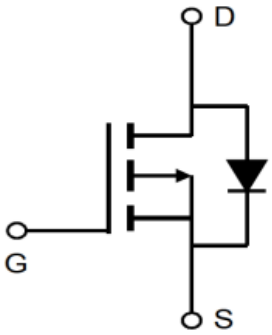
V_{DS}	-60V
$R_{DS(on)\ typ.}$	24mΩ
I_D	-11A

100% DVDS Tested

100% Avalanche Tested

RoHS HF





Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRTE280P06L2-G	T280P06L2	SOP8	Reel	N/A	N/A	2500pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	-60	V
Continuous drain current T_L = 25°C T_L = 100°C	I_D	 -11 -7.0	A
Pulsed drain current (T_L = 25°C, t_p limited by T_{jmax})	$I_{D\ pulse}$	-44	A
Avalanche energy, single pulse (L=0.5mH)	E_{AS}	150	mJ
Gate-Source voltage	V_{GS}	±18	V
Power dissipation (T_L = 25°C)	P_{tot}	5.0	W
Operating junction and storage temperature	T_j , T_{stg}	-55...+150	°C
Soldering temperature, wave soldering only allowed at leads (1.6mm from case for 10s)	T_{sold}	260	°C

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Thermal Resistance

Parameter	Symbol	Typ	Max	Unit
Thermal resistance, junction – ambient(min. footprint)	R_{thJL}	-	25	℃/W

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	-60	-	-	V	V_{GS}=0V, I_D=-250uA
Gate threshold voltage	$V_{GS(th)}$	-1	-1.5	-2.5	V	V_{DS}=V_{GS}, I_D=-250uA
Zero gate voltage drain current	I_{DSS}	-	-	-1	μA	V_{DS}=-60V, V_{GS}=0V T_j=25°C
		-	-	-100		T_j=150°C
Gate-source leakage current	I_{GSS}	-	-	±100	nA	V_{GS}=±18V,V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$				mΩ	T_j=25°C
		-	24	28.0		V_{GS}=-10V, I_D=-1.9A
		-	27	40.0		V_{GS}=-4.5V, I_D=-1.9A
Transconductance	g_{fs}		64.85		S	V_{DS}=-5V, I_D=-8A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	3680	-	pF	VGS=0V, VDS=-30V f=1MHz
Output Capacitance	C_{oss}	-	151	-		
Reverse Transfer Capacitance	C_{rss}	-	117	-		
Gate Total Charge	Q_g	-	62.7	-	nC	VGS=-10V, VDS=-30V ID=-8A
Gate-Source charge	Q_{gs}	-	8.2	-		
Gate-Drain charge	Q_{gd}	-	10.2	-		
Turn-on delay time	$t_{d(on)}$	-	14.2	-	ns	V_{GS}=-10V, VDS=-30V RG=3.0Ω, ID=-8A
Rise time	t_r	-	6.6	-		
Turn-off delay time	$t_{d(off)}$	-	118	-		
Fall time	t_f	-	70	-		
Gate resistance	R_G	-	7.1	-	Ω	V_{GS}=0V, V_{DS}=0V f=1MHz

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Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	-	-1.2	V	V_{GS}=0V, I_{SD}=-8A
Body Diode Continuous Forward Current	I_S	-	-	-11	A	T_L = 25°C
Body Diode Reverse Recovery Time	t_{rr}	-	29.38	-	ns	I_F=-8A, dI/dt=100A/μs
Body Diode Reverse Recovery Charge	Q_{rr}	-	30.19	-	nC	

a1：Repetitive rating; pulse width limited by maximum junction temperature

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Typical Performance Characteristics

Fig 1: Output Characteristics

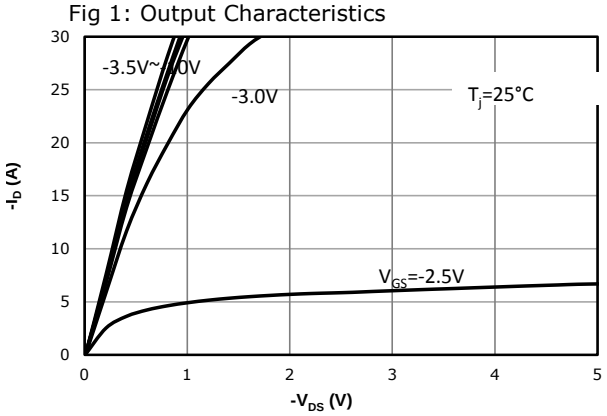


Fig 2: Transfer Characteristics

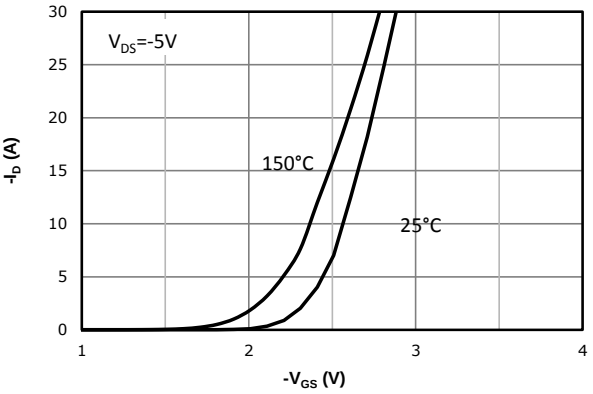


Fig 3: Rds(on) vs Drain Current and Gate Voltage

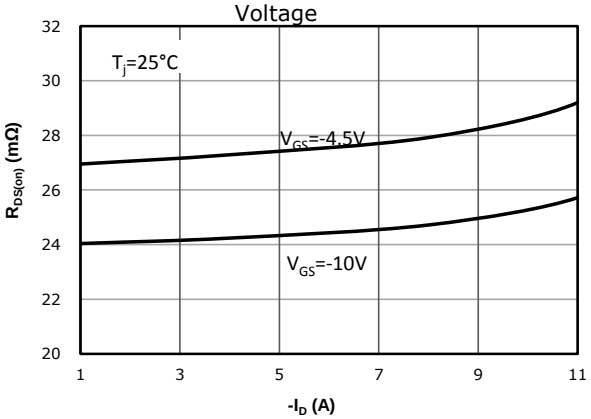


Fig 4: Rds(on) vs Gate Voltage

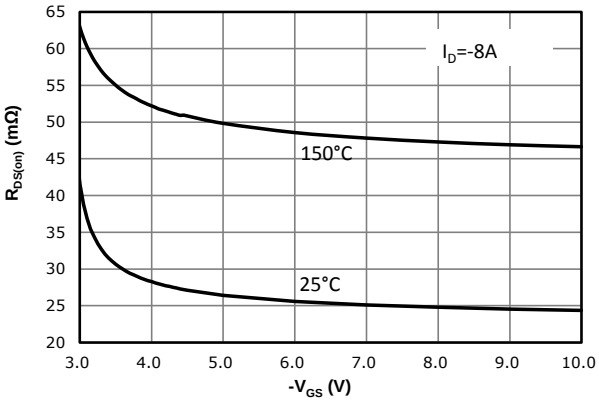


Fig 5: Rds(on) vs. Temperature

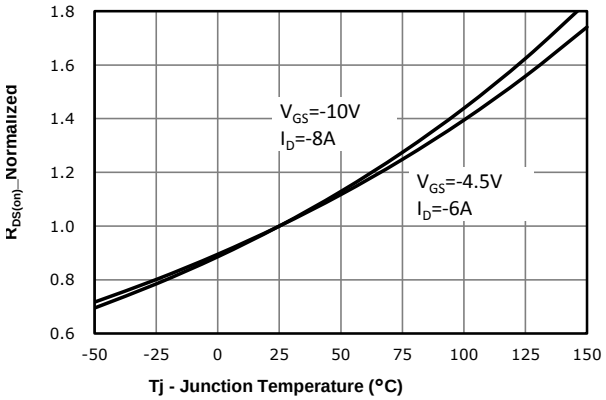
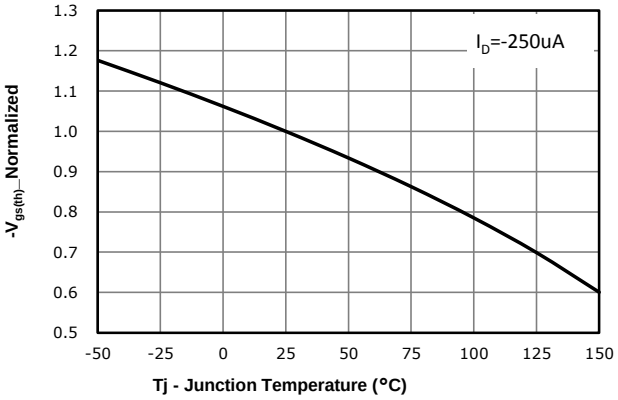


Fig 6: Vgs(th) vs. Temperature



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Fig 7: BVdss vs. Temperature

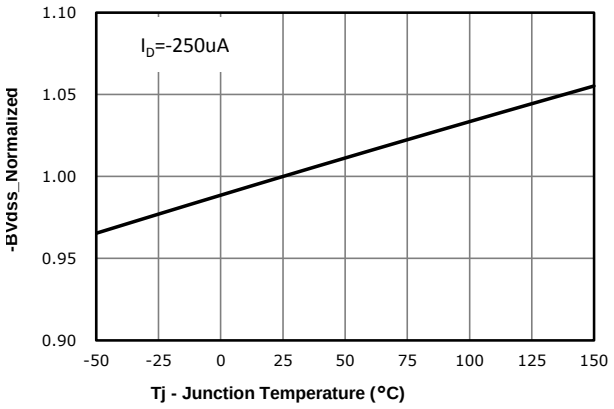


Fig 8: Body-diode Forward Characteristics

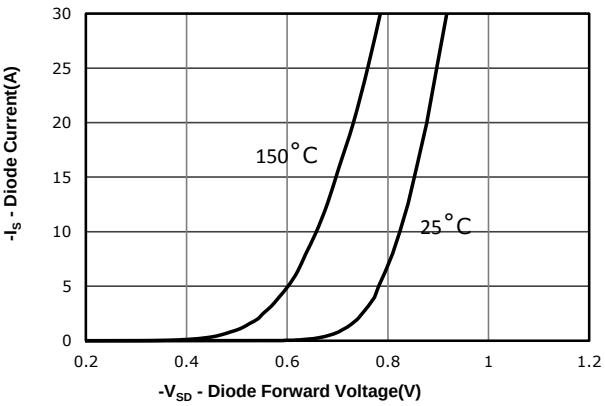


Fig 9: Gate Charge Characteristics

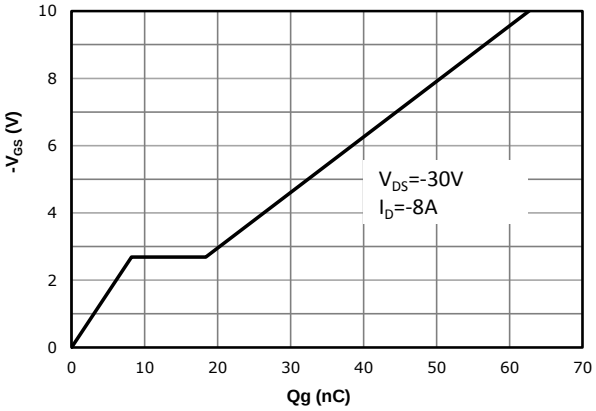


Fig 10: Capacitance Characteristics

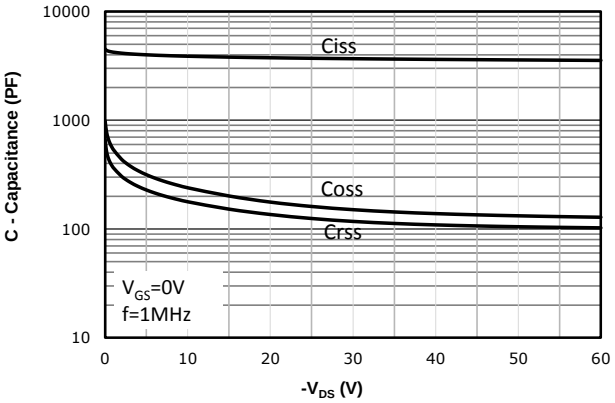


Fig 11: Drain Current Derating

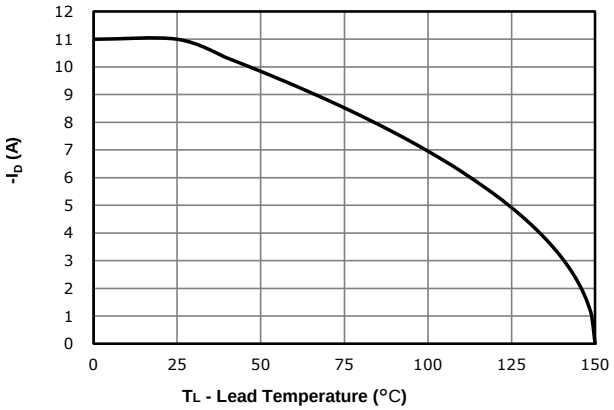
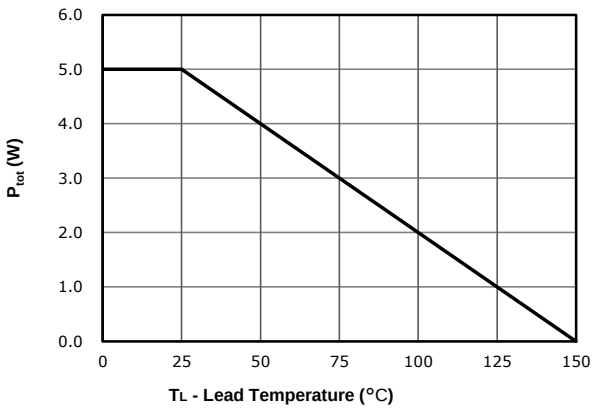


Fig 12: Power Dissipation



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Fig 13: Safe Operating Area

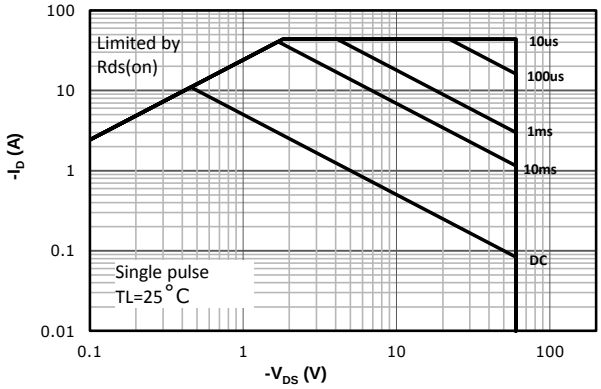
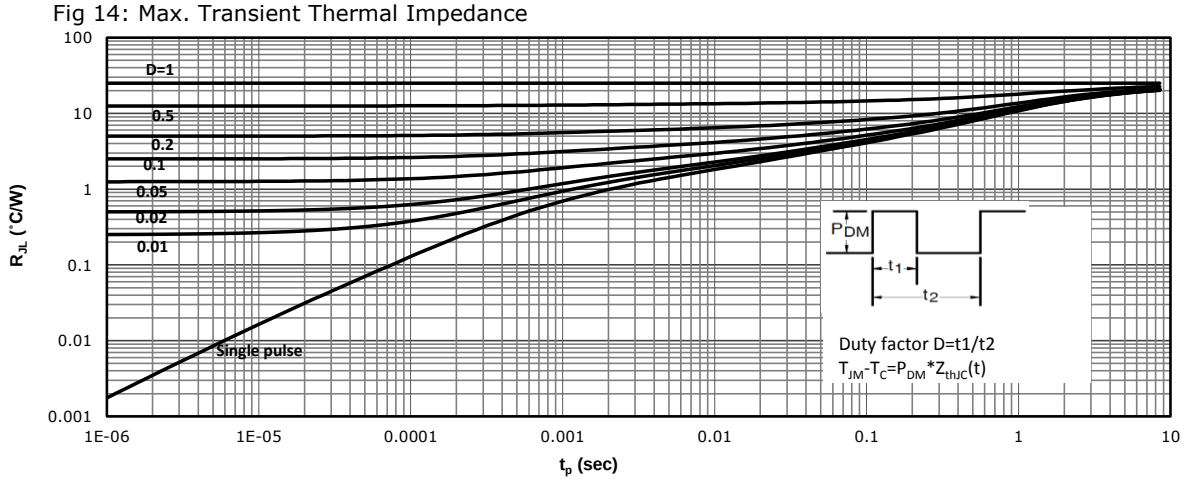


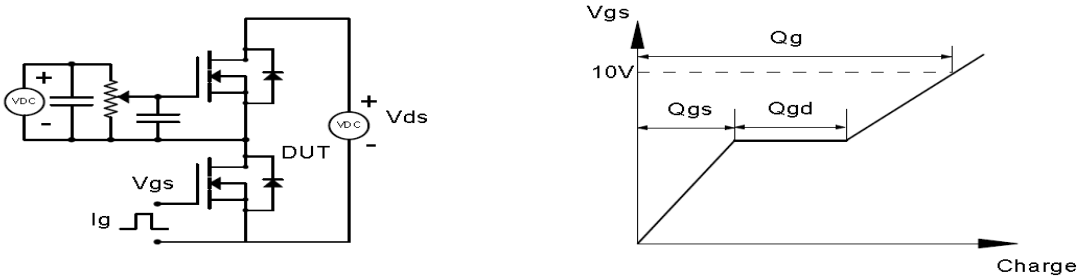
Fig 14: Max. Transient Thermal Impedance



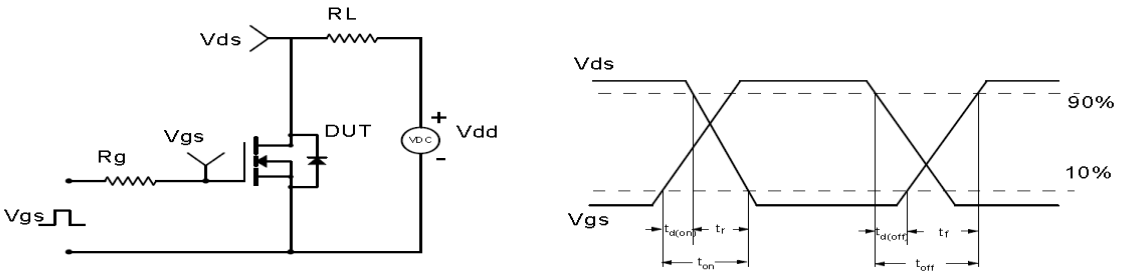
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Test Circuit & Waveform

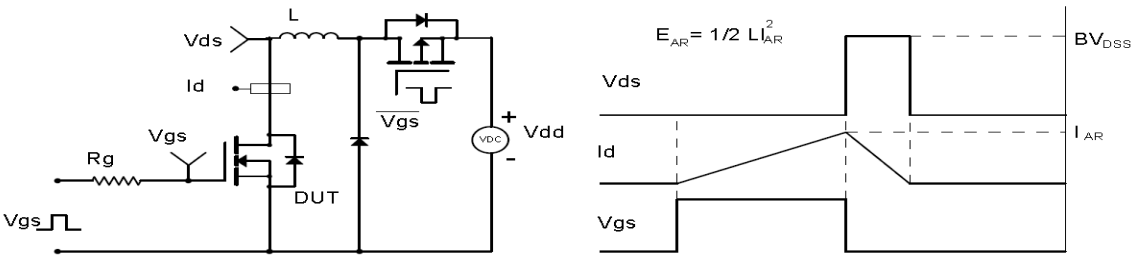
Gate Charge Test Circuit & Waveform



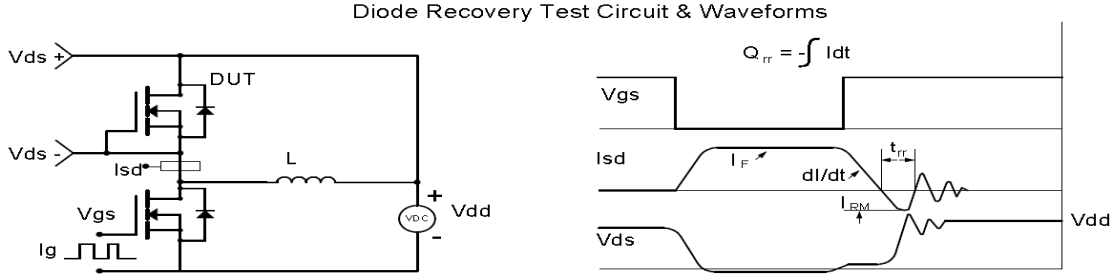
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

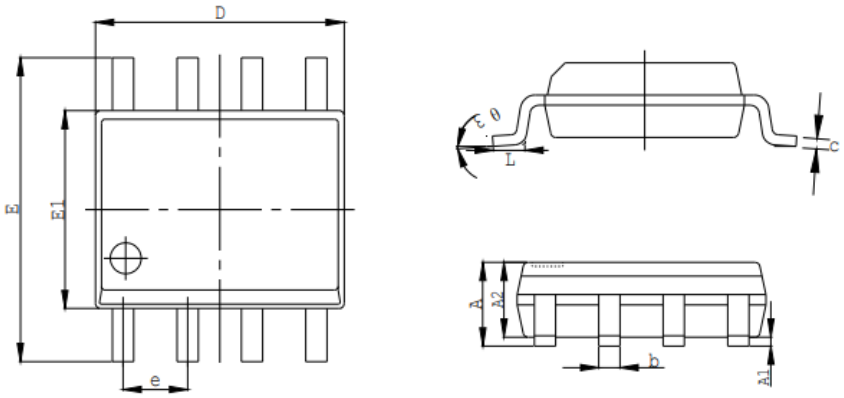


Diode Recovery Test Circuit & Waveforms



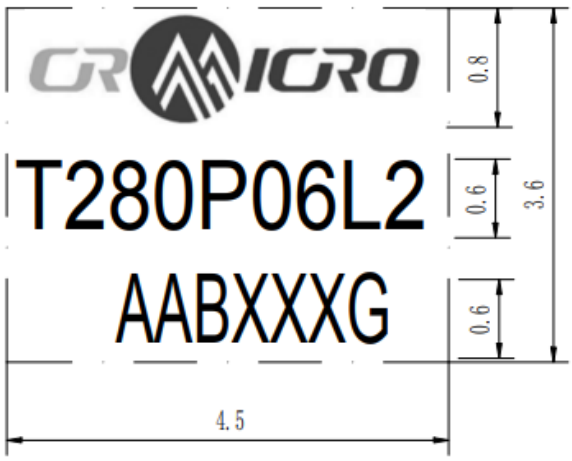
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Package Outline: SOP8



Symbol	values(mm)	
	Min.	Max.
A	1.30	1.80
A1	0.10	0.25
A2	1.30	1.50
E	5.80	6.20
E1	3.80	4.00
D	4.80	5.00
L	0.40	0.90
e	1.27TYP	
b	0.37	0.47
c	0.20TYP	
θ3	0°	8°

Marking



NOTE:
AABXXXG
AA —cycle code
B —Fab code
XXX —Assembly lot code

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Revision History

Revison	Date	Major changes
1.0	2023/10/9	Relaease of formal version
2.0		
3.0		

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qulified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semicondutor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM reserves the right to improve product design, function and reliability without notice.

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