CRXMH97P065NMG2AQ

SiC MOSFET 650V, 25mΩ, 97A

Features

- Qualified according to AEC Q101
- CRM G2 SIC MOSFET Technology
- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Avalanche Ruggedness
- Fast Reverse Recovery

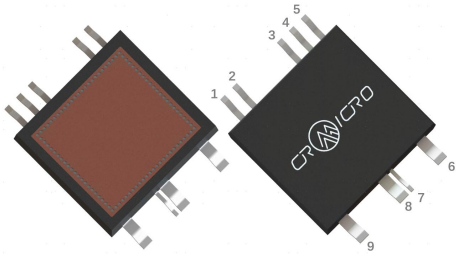
Product Summary

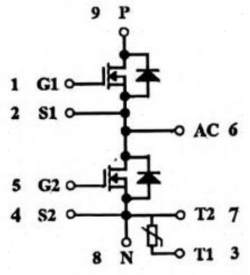
VDS	650V
$R_{DS(on)_typ}$	25mΩ
I_D	97A

Applications

- EV OBC（EV On-board Charge）

100% Avalanche Tested





Package Marking and Ordering Information

Part #	Marking	Package	Packing	Qty
CRXMH97P065NMG2AQ	CRXMH97P065NMG2AQ	MSOP-9	Reel	200pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Continuous drain current V_{GS}=15V，T_C = 25°C V_{GS}=15V，T_C = 100°C	I_D	 97 69	A
Pulsed drain current (T_C = 25°C, t_p limited by T_{jmax})	$I_{D(pulse)}$	243	A
Avalanche energy, single pulse (L=10mH, Rg=25Ω)	E_{AS}	731	mJ
Gate-Source voltage (dynamic)[a1]	V_{GSmax}	-10/+22	V
Gate-Source voltage (static)[a2]	V_{GSop}	-5/+18	V
Power dissipation (T_C=25°C,T_J=175°C)	P_D	300	W
Operating Junction and Storage Temperature	T_j,T_{stg}	-55...175	°C

a1: When using MOSFET Body Diode V_{GSmax} = -5V/+22V

a2: MOSFET can also safely operate at 0/+18V

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R_{thJC}	0.50	°C/W
Thermal resistance, junction – ambient. Max	R_{thJA}	35	

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	$V_{(BR)DSS}$	650	-	-	V	V_{GS}=0V, I_D=250uA
Gate threshold voltage	$V_{GS(th)}$	1.8	-	3.6	V	V_{DS}=V_{GS},I_D=15mA
Zero gate voltage drain current	I_{DSS}	-	1	100	μA	VDS=650V,VGS=0V T_j=25°C
		-	10	-		T_j=175°C
Gate-source leakage current	I_{GSS}	-	-	±100	nA	V_{GS}=-10/+22V,V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	-	25	34	mΩ	VGS=15V, ID=33.5A, T_j=25°C
		-	32.5	-		T_j=175°C
Drain-source on-state resistance	$R_{DS(on)}$	-	20	25	mΩ	VGS=18V, ID=33.5A, T_j=25°C
		-	28	-		T_j=175°C
Transconductance	g_{fs}	-	30	-	S	VDS=20V,IDS=33.5A
Dynamic Characteristic						
Internal Gate resistance	$R_{G(int)}$	-	1.7	-	Ω	f=1MHz
Input Capacitance	C_{iss}	-	2775	-	pF	V_{GS}=0V, V_{DS}=600V, f=1MHz
Output Capacitance	C_{oss}	-	231	-		
Reverse Transfer Capacitance	C_{rss}	-	16.3	-		
Coss Stored Energy	E_{oss}	-	45	-	uJ	
Gate Total Charge	Q_g	-	133	-	nC	VGS=-5/18V VDS=400V ID=33.5A
Gate-Source charge	Q_{gs}	-	41.5	-		
Gate-Drain charge	Q_{gd}	-	26	-		
Turn-on delay time	$t_{d(on)}$	-	49.8	-	ns	VDD=400V,ID=33.5A VGS=-5V/15V， RG=2.5Ω，L=200uH
Rise time	t_r	-	6.4	-		
Turn-off delay time	$t_{d(off)}$	-	34	-		
Fall time	t_f	-	8.8	-		
Turn-On Switching Energy	$E_{(on)}$	-	115	-	uJ	
Turn Off Switching Energy	$E_{(off)}$	-	97	-		

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	3.5	-	V	VGS=0V,IF=16.7A
		-	3	-	V	VGS=0V,IF=16.7A, Tj=175℃
Continuous Diode Forward Current	I_S	-	-	97	A	V_{GS}=0V
Diode pulse Current	$I_{S,pulse}$	-	-	243	A	pulse width t_p limited by Tjmax
Body Diode Reverse Recovery Time	t_{rr}	-	21	-	ns	di/dt=1000A/us IF=33.5A Vdd=400V Tj=25℃
Body Diode Reverse Recovery Charge	Q_{rr}	-	138	-	nC	
Body Diode Peak Reverse Recovery Current	I_{rrm}	-	11	-	A	
Body Diode Reverse Recovery Time	t_{rr}	-	27.7	-	ns	di/dt=1000A/us IF=33.5A Vdd=400V Tj=175℃
Body Diode Reverse Recovery Charge	Q_{rr}	-	242	-	nC	
Body Diode Peak Reverse Recovery Current	I_{rrm}	-	15.2	-	A	

Typical Performance Characteristics

Fig 1. Output Characteristics (Tj=-55℃)

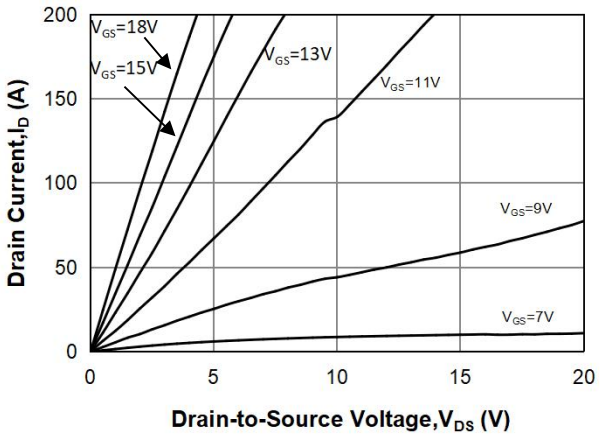


Fig 2. Output Characteristics (Tj=25℃)

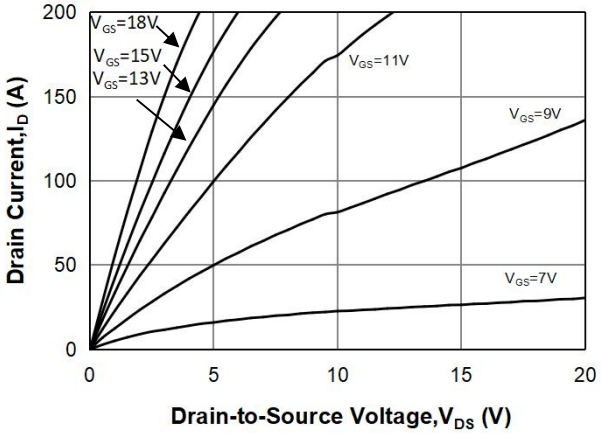


Fig 3. Output Characteristics (Tj=175℃)

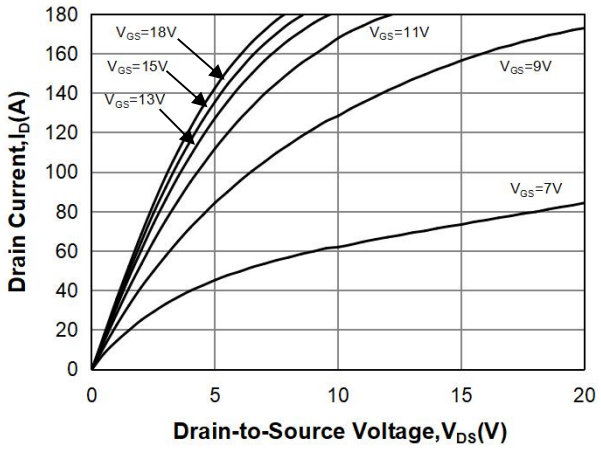


Fig 4: Rds(on) vs. Temperature

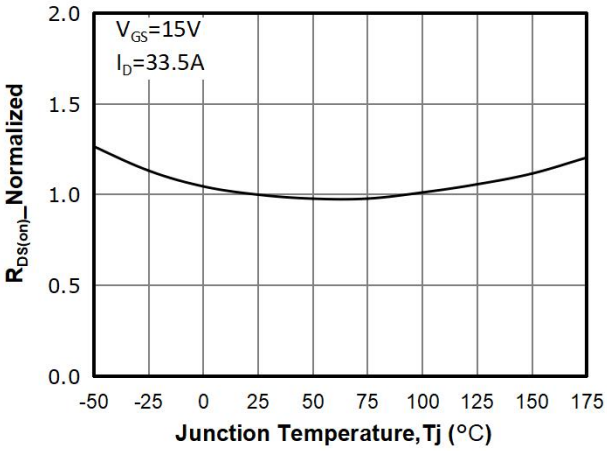


Fig 5: On-Resistance vs. Drain Current For Various Temperatures

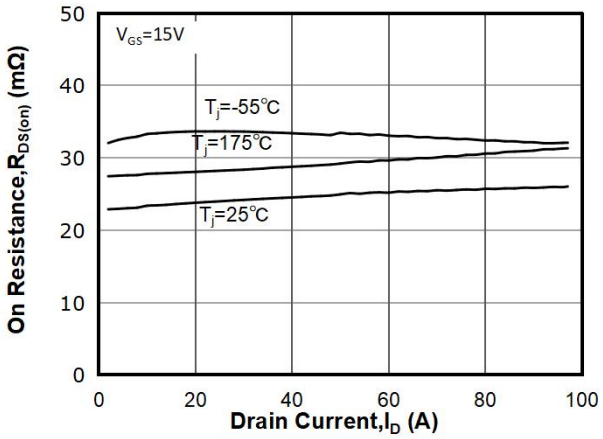


Fig 6: Rds(on) vs. Temperature For Various Gate Voltage

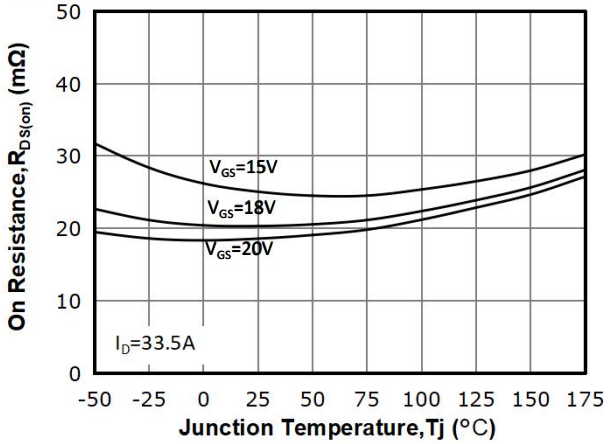


Fig 7: Transfer Characteristics

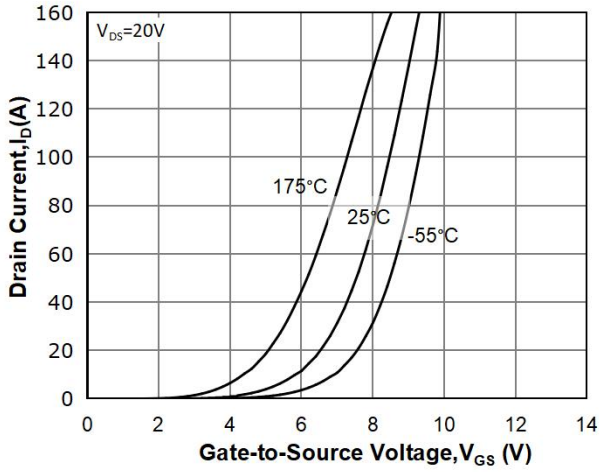


Fig 8: Body-diode Forward Characteristics For Various Temperatures

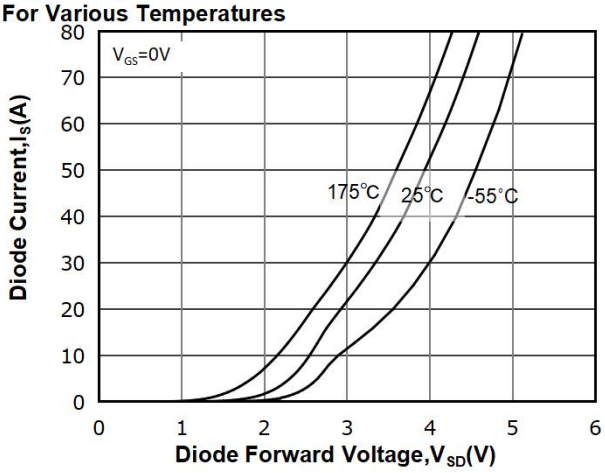


Fig 9: VGS(th) Vs Tj Characteristics

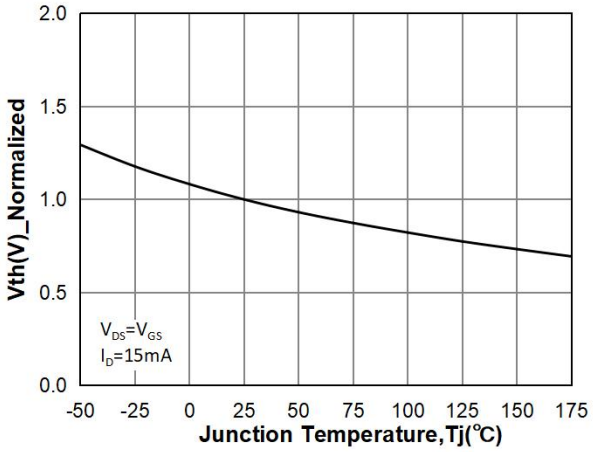


Fig 10: 3rd Quadrant Characteristic at 25℃

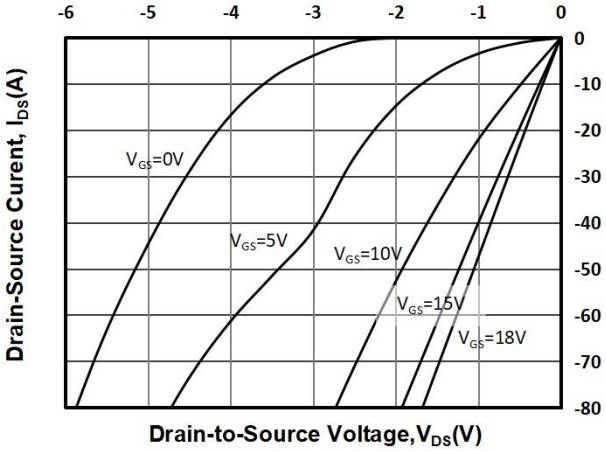


Fig 11：Gtate Charge Characteristics

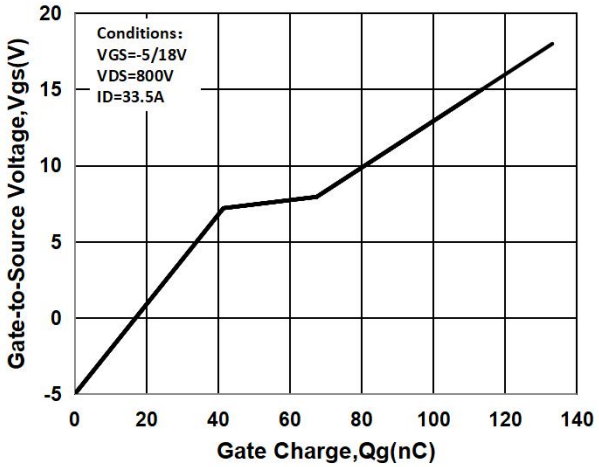


Fig 12: Capacitance Characteristics

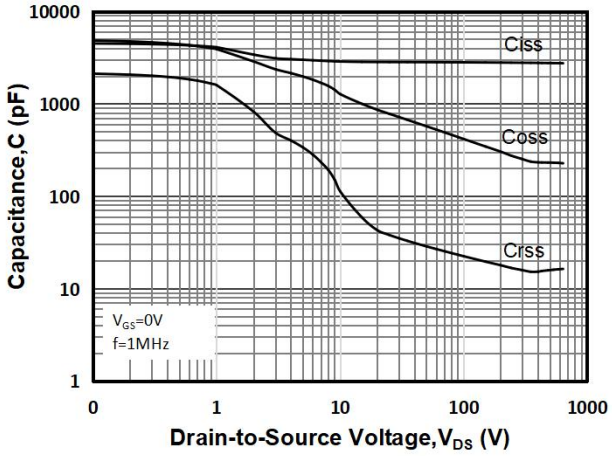


Fig 13: Continuous Drain Current vs. Case Temperature

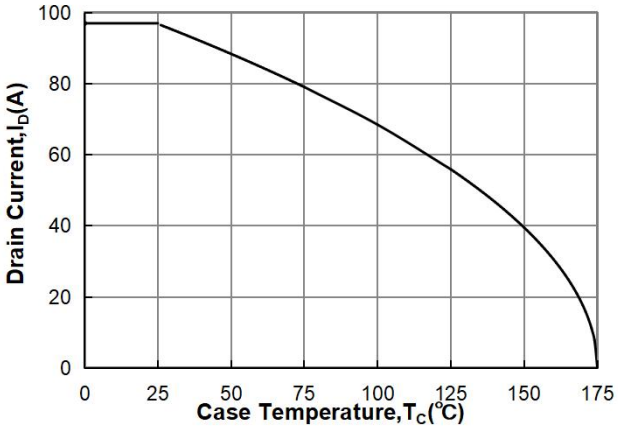


Fig 14: Maximum Power Dissipation vs. Case Temperature

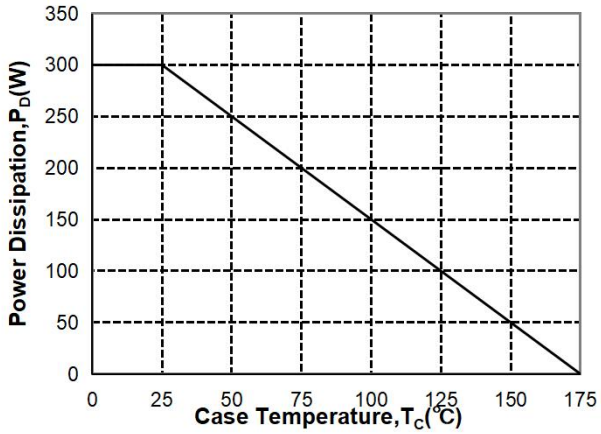


Fig 15: Safe Operating Area

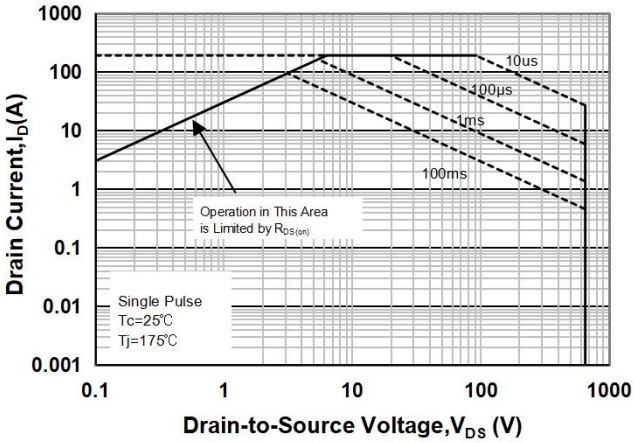


Fig 16:Output Capacitor Stored Energy

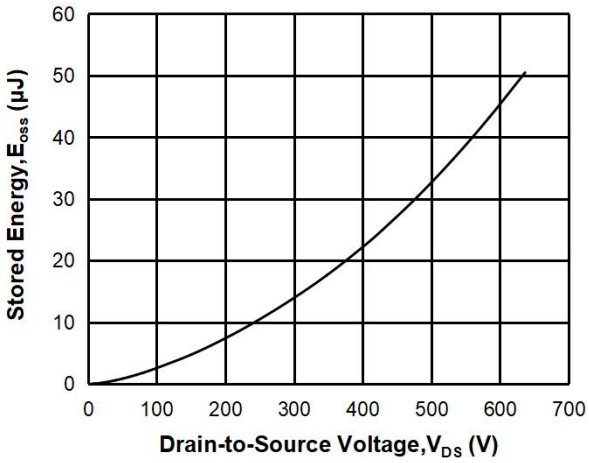


Fig 17: Max. Transient Thermal Impedance

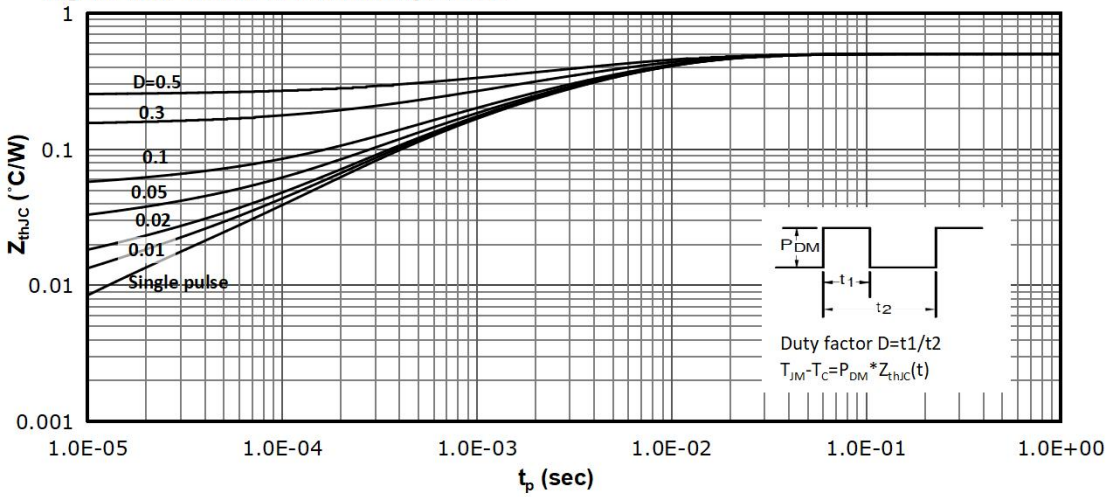


Fig 18: Rg(Ω) vs. Switching Times(us)

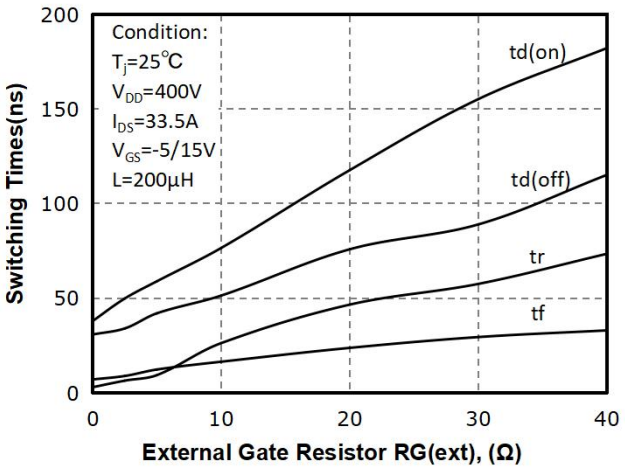


Fig 19: Rg(Ω) vs. Switching Loss(uJ)

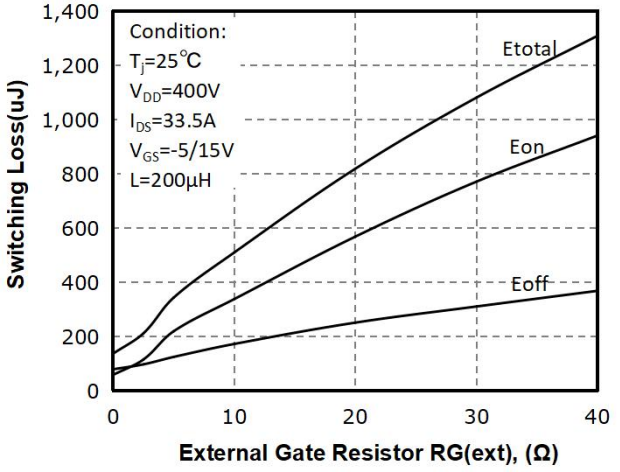


Fig 20: Temperature(°C) vs. Switching Loss(uJ)

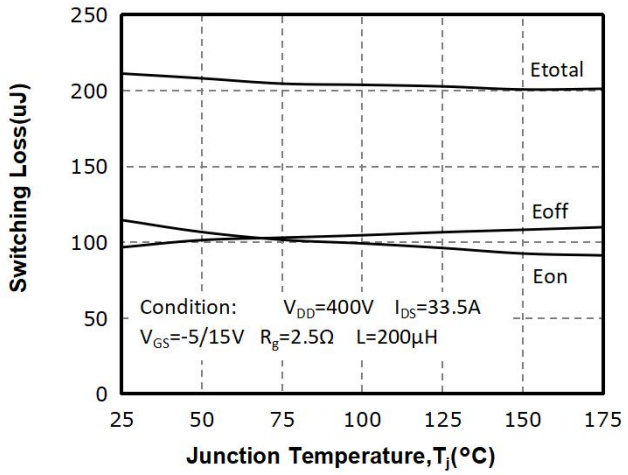
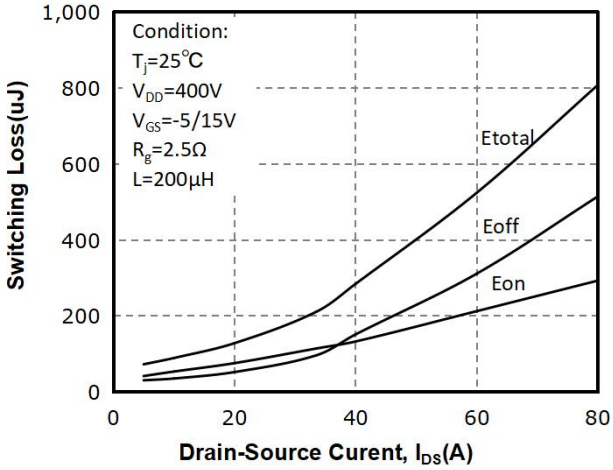
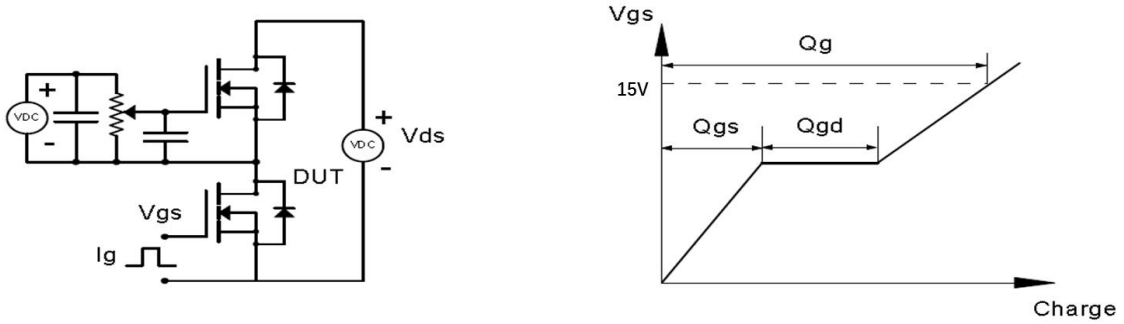


Fig 21: I_{DS}(A) vs. Switching Loss(uJ)

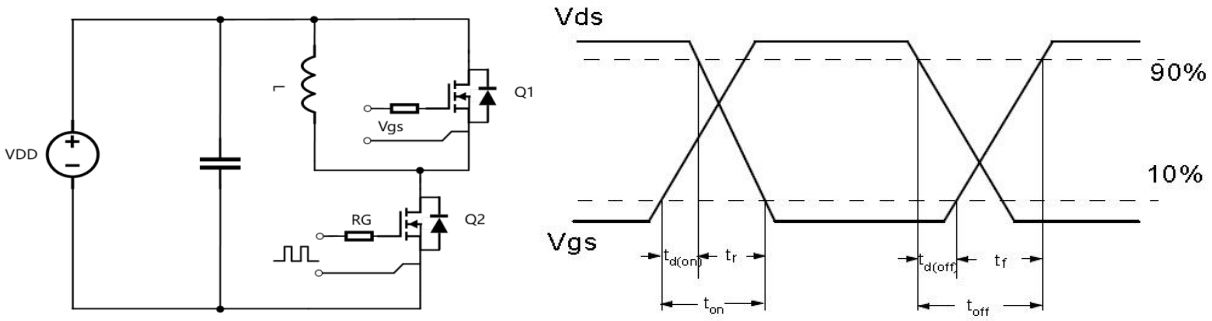


Test Circuit & Waveform

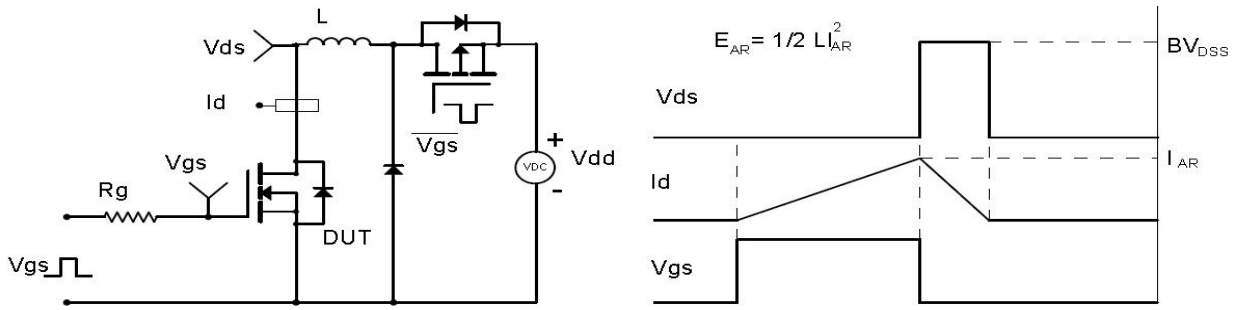
Gate Charge Test Circuit & Waveform



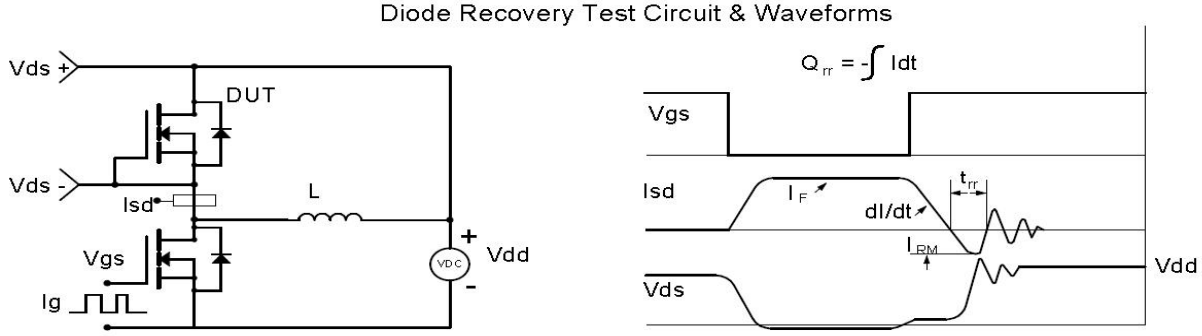
Resistive Switching Test Circuit & Waveforms



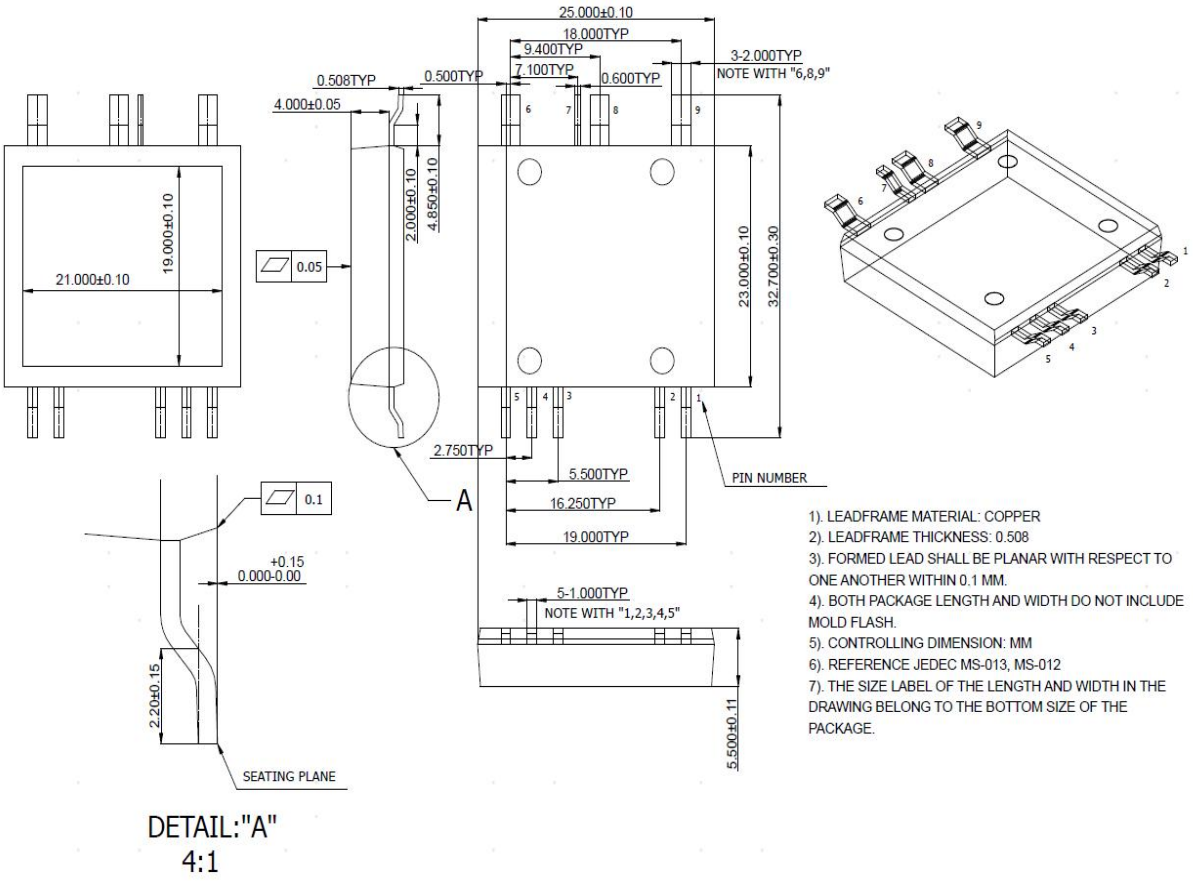
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline: MSOP-9



1). LEADFRAME MATERIAL: COPPER
2). LEADFRAME THICKNESS: 0.508
3). FORMED LEAD SHALL BE PLANAR WITH RESPECT TO ONE ANOTHER WITHIN 0.1 MM.
4). BOTH PACKAGE LENGTH AND WIDTH DO NOT INCLUDE MOLD FLASH.
5). CONTROLLING DIMENSION: MM
6). REFERENCE JEDEC MS-013, MS-012
7). THE SIZE LABEL OF THE LENGTH AND WIDTH IN THE DRAWING BELONG TO THE BOTTOM SIZE OF THE PACKAGE.

Revision History

Revison	Date	Major changes
1.0	2025/3/3	Release of formal version

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