Features

- Uses CRM(CQ) advanced Trench technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent Q_gx$R_{DS(on)}$ product(FOM)
- Complementary N-ch and P-ch MOSFET

Product Summary

Symbol	N-Ch	P-Ch
V_{DS}	60V	-60V
$R_{DS(on)\ typ.}$	12mΩ	53mΩ
I_D	16A	-16A

Applications

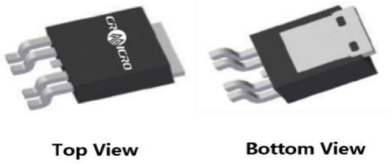
- Motor drive

100% DVDS Tested

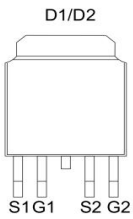
100% Avalanche Tested

RoHS HF

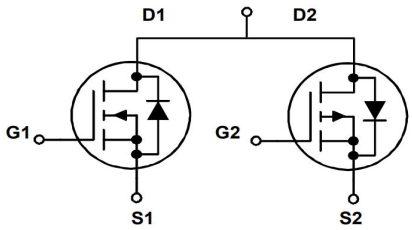
TO-252-5



Top View Bottom View



Top View



N-Channel P-Channel

Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRMM6905C	CRMM6905C	TO-252-5	Taping	N/A	N/A	2500pcs

Absolute Maximum Ratings

Parameter	Symbol	Maximum		Unit
		N-Ch	P-Ch	
Drain-source voltage	V_{DS}	60	-60	V
Continuous drain current T_C = 25°C (Silicon limit)	I_D	40	-27	A
Continuous drain current T_C = 25°C (Package limit)	I_D	16	-16	A
Pulsed drain current (T_C = 25°C, t_p limited by T_{jmax})	$I_{D\ pulse}$	64	-64	A
Avalanche energy, single pulse (L=0.5mH, Rg=25Ω)	E_{AS}	156	72	mJ
Gate-Source voltage	V_{GS}	±20	±20	V
Power dissipation (T_C = 25°C)	P_{tot}	38.8	38.8	W
Operating junction and storage temperature	T_j , T_{stg}	-55...+150		°C

Thermal Resistance

Parameter	Symbol	Typ	Max	Unit
Thermal resistance, junction – case.	R_{thJC}	2.3	3.2	°C/W
Thermal resistance, junction – ambient(min.footprint)	R_{thJA}	79.0	94.8	°C/W

N-Channel Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	60	-	-	V	V_{GS}=0V, I_D=250uA
Gate threshold voltage	$V_{GS(th)}$	1.3	1.8	2.2	V	V_{DS}=V_{GS}, I_D=250uA
Zero gate voltage drain current	I_{DSS}	- -	0.08 -	1 5	μA	V_{DS}=60V, V_{GS}=0V T_j=25°C T_j=125°C
Gate-source leakage current	I_{GSS}	-	±10	±100	nA	V_{GS}=±20V, V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	- -	15.0 12.0	18.0 15.0	mΩ	V_{GS}=4.5V, I_D=10A V_{GS}=10V, I_D=10A
Transconductance	g_{fs}	-	39.4	-	S	V_{DS}=10V, I_D=10A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	1080	-	pF	V_{GS}=0V, V_{DS}=60V, f=1MHz
Output Capacitance	C_{oss}	-	204	-		
Reverse Transfer Capacitance	C_{rss}	-	16	-		
Gate Total Charge	Q_G	-	19.5	-	nC	V_{GS}=10V, V_{DS}=60V, I_D=10A, f=1MHz
Gate-Source charge	Q_{gs}	-	4.9	-		
Gate-Drain charge	Q_{gd}	-	3.1	-		
Turn-on delay time	$t_{d(on)}$	-	8.0	-	ns	V_{GS}=10V, V_{DD}=30V, R_{G_ext}=2.7Ω, ID=10A
Rise time	t_r	-	22.7	-		
Turn-off delay time	$t_{d(off)}$	-	19.7	-		
Fall time	t_f	-	25.3	-		
Gate resistance	R_G	-	1.5	-	Ω	V_{GS}=0V, V_{DS}=0V, f=1MHz

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	0.85	1.20	V	V_{GS}=0V, I_{SD}=10A
Body Diode Reverse Recovery Time	t_{rr}	-	28.1	-	ns	I_F=10A, dI/dt=100A/μs
Body Diode Reverse Recovery Charge	Q_{rr}	-	15.6	-	nC	

N-Channel Typical Performance Characteristics

Fig 1: Output Characteristics

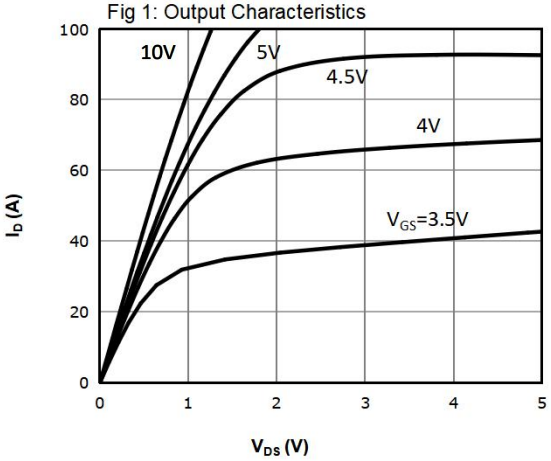


Fig 2: Transfer Characteristics

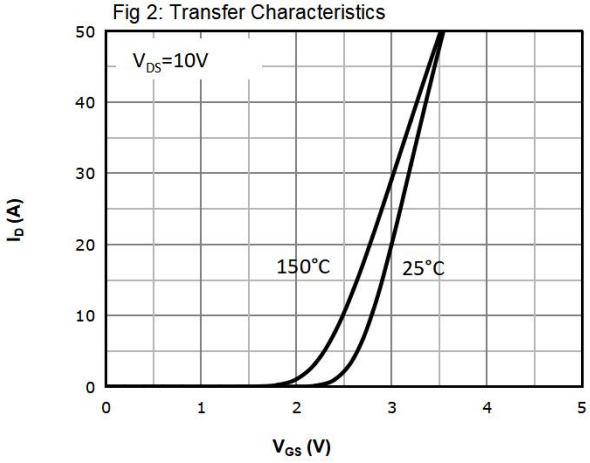


Fig 3: Rds(on) vs Drain Current and Gate Voltage

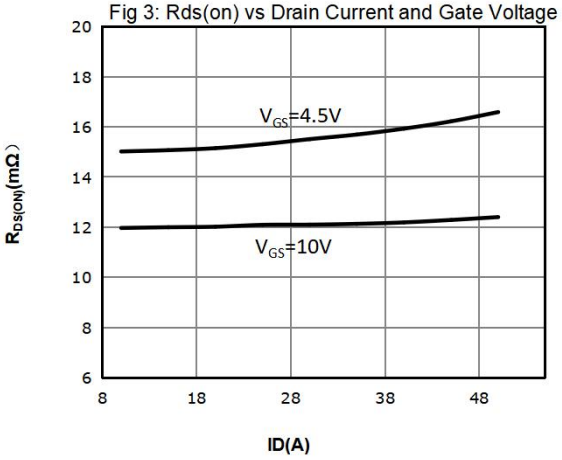


Fig 4: Rds(on) vs Gate Voltage

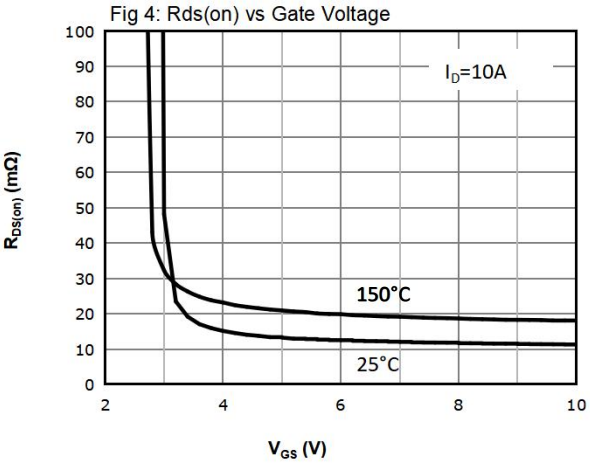


Fig 5: Rds(on) vs. Temperature

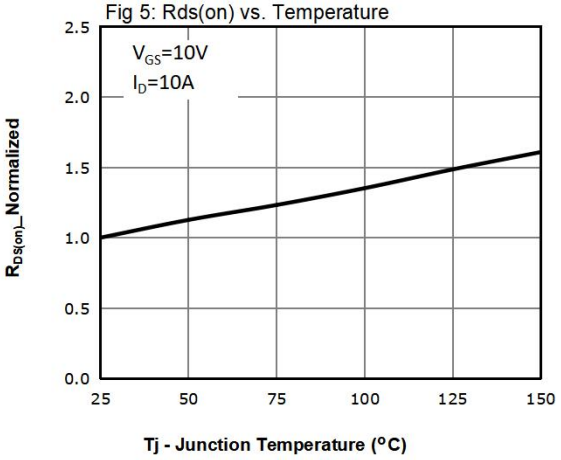


Fig 6: Capacitance Characteristics

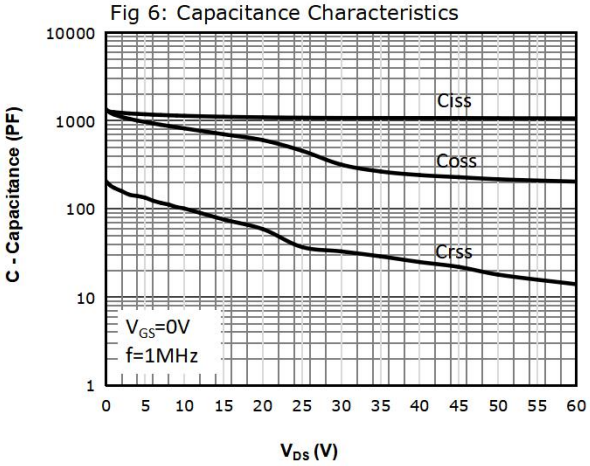


Fig 7: Gate Charge Characteristics

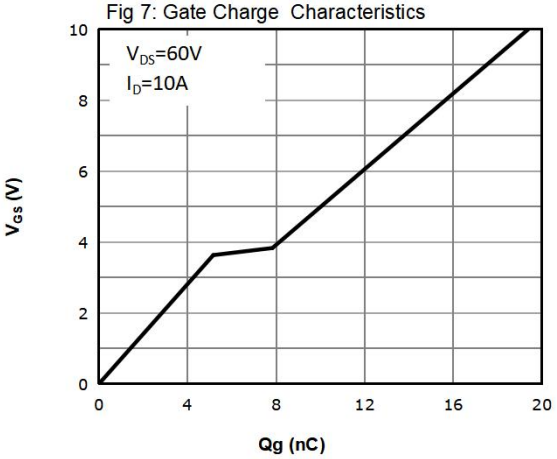


Fig 8: Body-diode Forward Characteristics

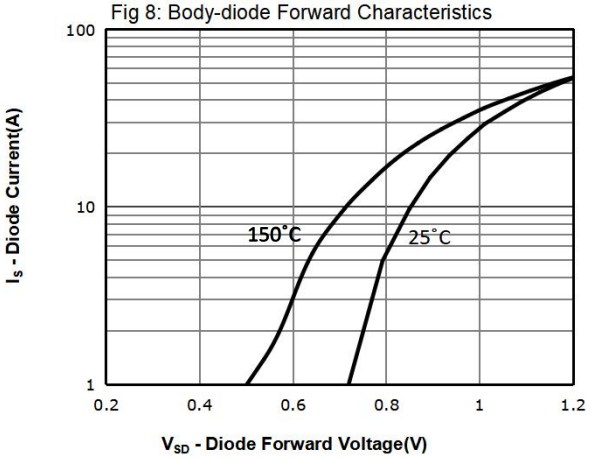


Fig 9: Safe Operating Area

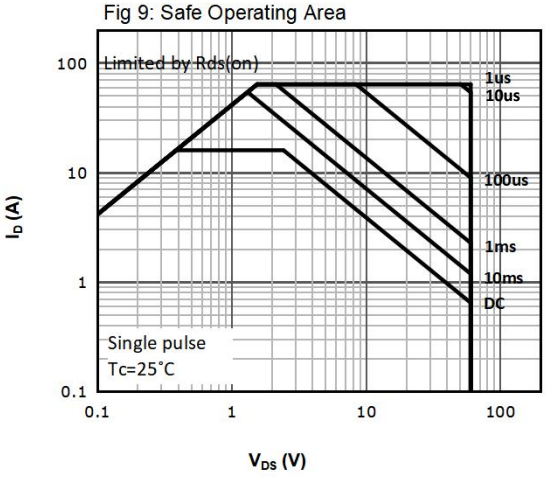
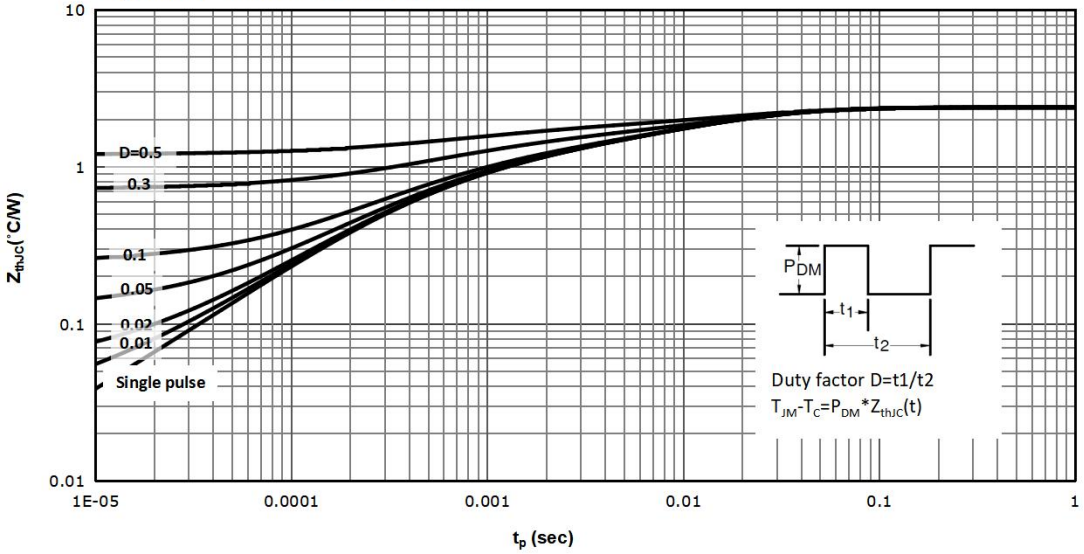


Fig 10: Max. Transient Thermal Impedance



P-Channel Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	-60	-	-	V	V_{GS}=0V, I_D=-250uA
Gate threshold voltage	$V_{GS(th)}$	-1	-1.4	-2	V	V_{DS}=V_{GS}, I_D=-250uA
Zero gate voltage drain current	I_{DSS}	- -	-0.08 -	-1 -5	μA	V_{DS}=-60V, V_{GS}=0V T_j=25°C T_j=125°C
Gate-source leakage current	I_{GSS}	-	±10	±100	nA	V_{GS}=±20V, V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	- -	66.0 53.0	79.0 64.0	mΩ	V_{GS}=-4.5V, I_D=-10A V_{GS}=-10V, I_D=-10A
Transconductance	g_{fs}	-	18.3	-	S	V_{DS}=-10V, I_D=-10A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	1168	-	pF	V_{GS}=0V, V_{DS}=-60V, f=1MHz
Output Capacitance	C_{oss}	-	73	-		
Reverse Transfer Capacitance	C_{rss}	-	56	-		
Gate Total Charge	Q_G	-	25.5	-	nC	V_{GS}=-10V, V_{DS}=-60V, I_D=-10A, f=1MHz
Gate-Source charge	Q_{gs}	-	4.7	-		
Gate-Drain charge	Q_{gd}	-	6.0	-		
Turn-on delay time	$t_{d(on)}$	-	7.2	-	ns	V_{GS}=-10V, V_{DD}=-30V, R_{G_ext}=2.7Ω, ID=-10A
Rise time	t_r	-	28.0	-		
Turn-off delay time	$t_{d(off)}$	-	46.1	-		
Fall time	t_f	-	68.5	-		
Gate resistance	R_G	-	12.1	-	Ω	V_{GS}=0V, V_{DS}=0V, f=1MHz

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	-0.92	-1.20	V	V_{GS}=0V, I_{SD}=-10A
Body Diode Reverse Recovery Time	t_{rr}	-	21.7	-	ns	I_F=10A, dI/dt=100A/μs
Body Diode Reverse Recovery Charge	Q_{rr}	-	15.4	-	nC	

P-Channel Typical Performance Characteristics

Fig 1: Output Characteristics

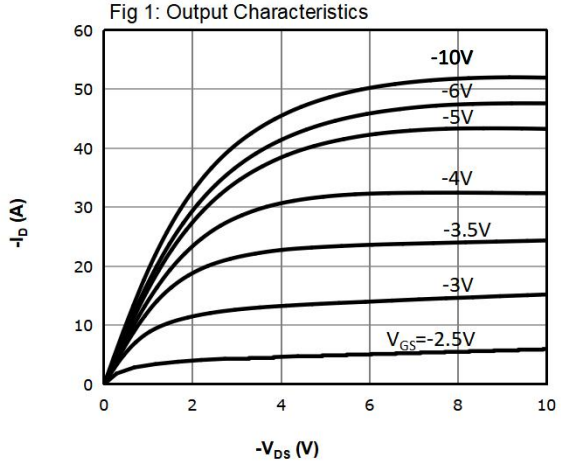


Fig 2: Transfer Characteristics

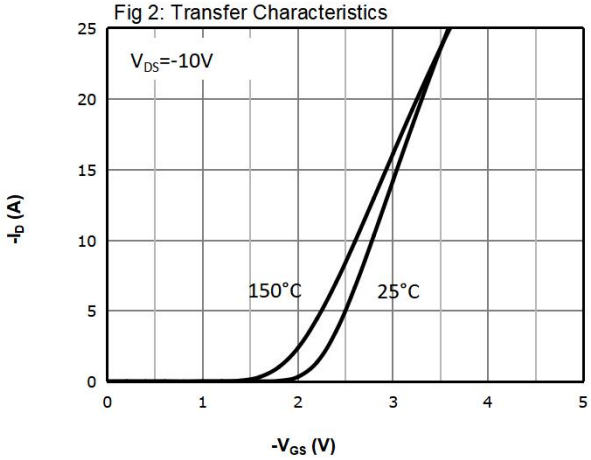


Fig 3: Rds(on) vs Drain Current and Gate Voltage

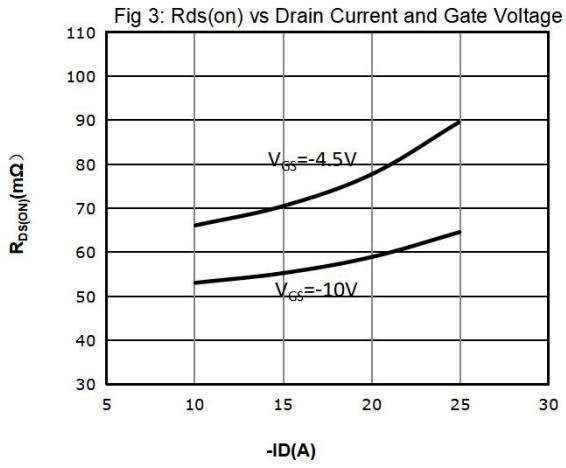


Fig 4: Rds(on) vs Gate Voltage

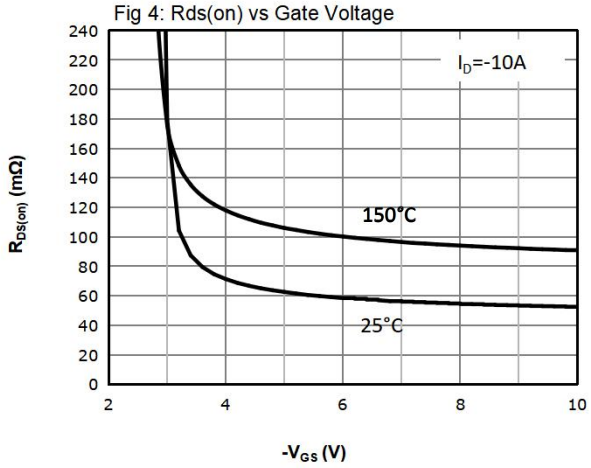


Fig 5: Rds(on) vs. Temperature

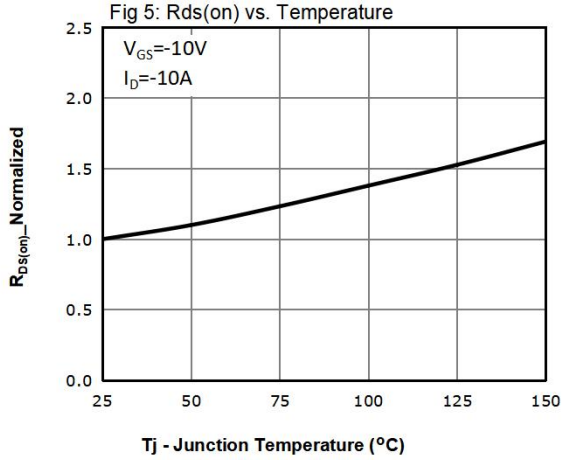


Fig 6: Capacitance Characteristics

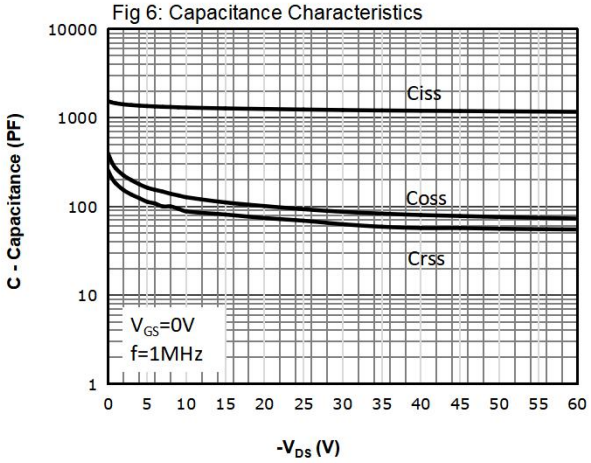


Fig 7: Gate Charge Characteristics

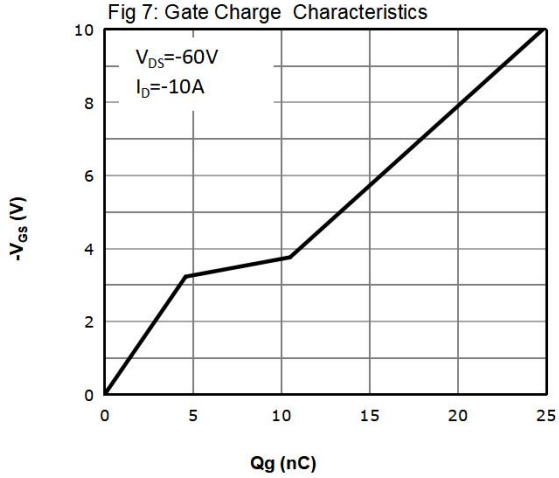


Fig 8: Body-diode Forward Characteristics

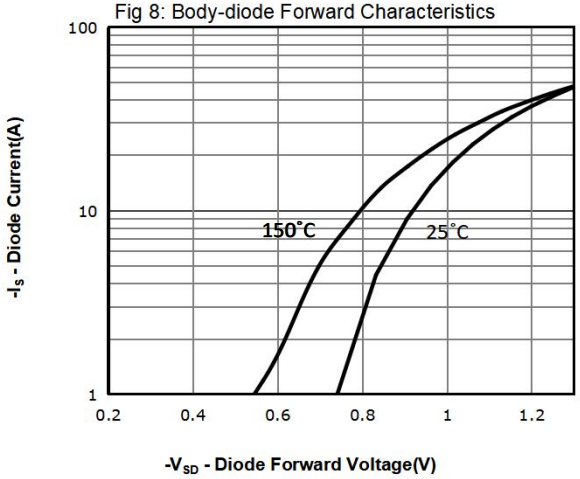


Fig 9: Safe Operating Area

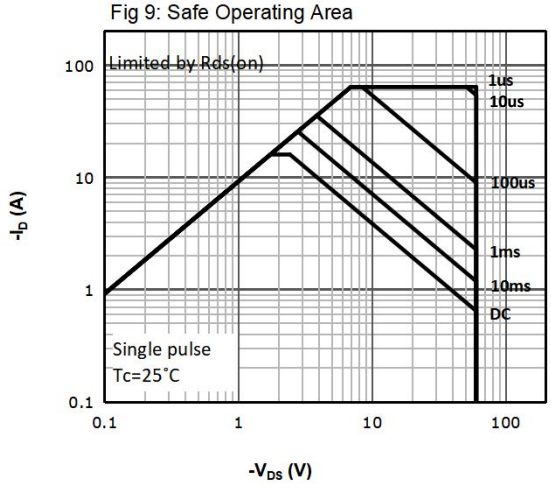
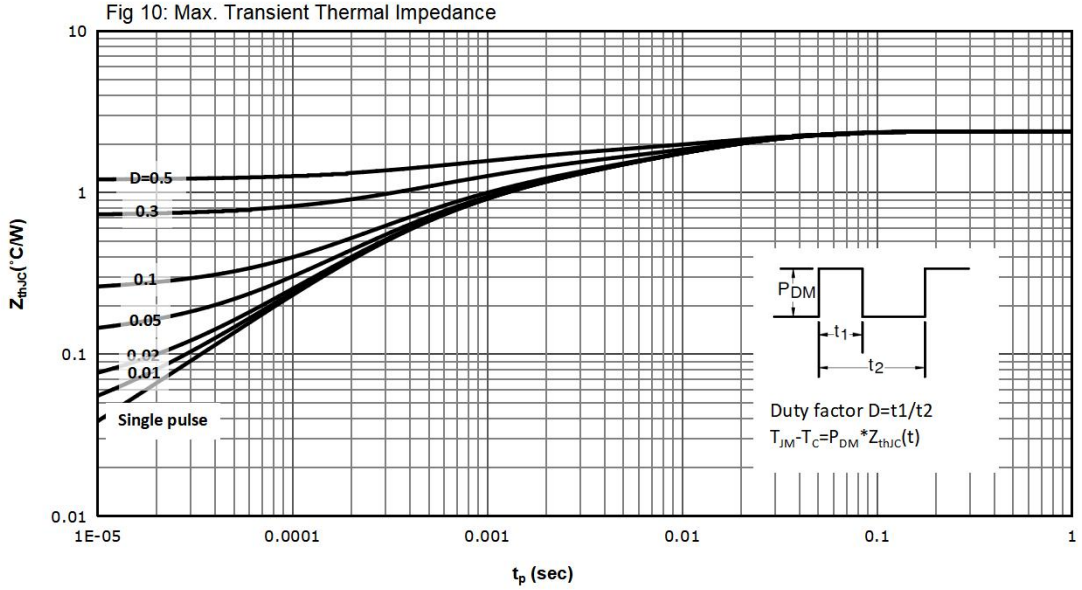
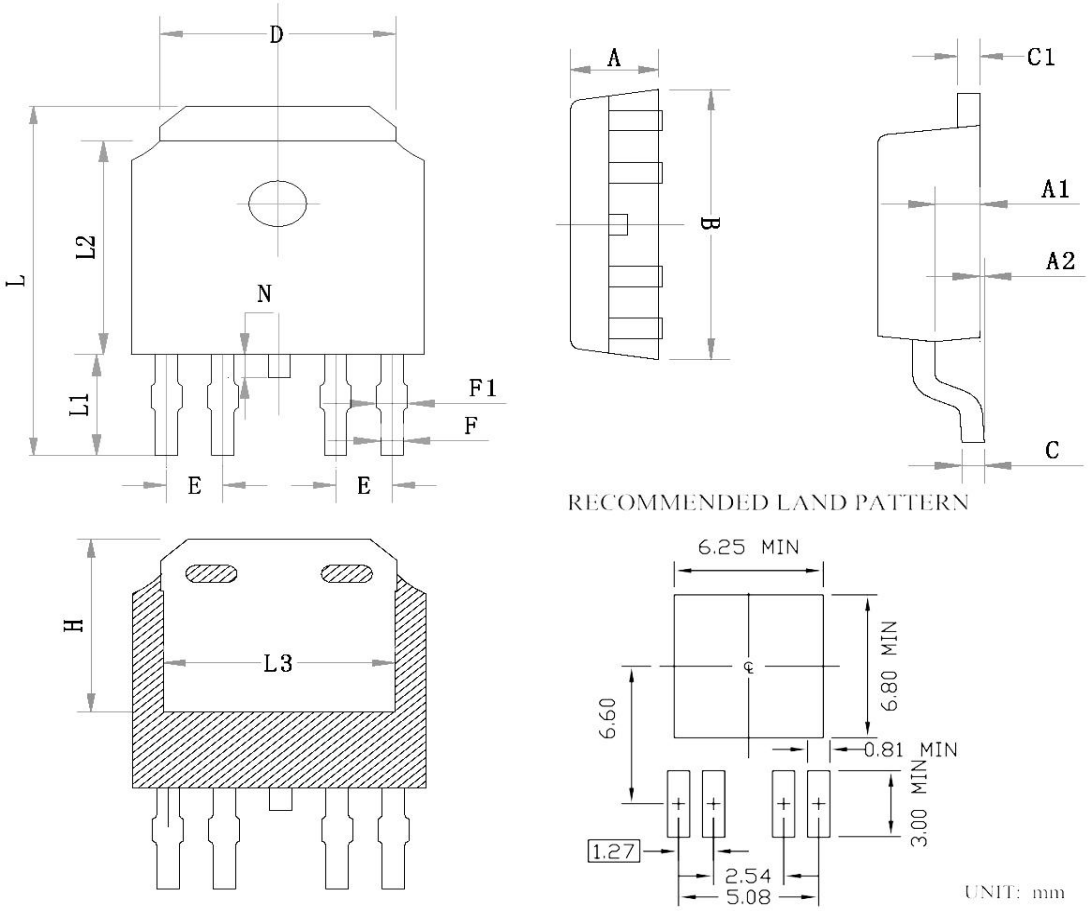


Fig 10: Max. Transient Thermal Impedance



Package Outline: TO-252-5



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	2.20	2.30	2.40	0.087	0.091	0.094
A1	0.91	1.01	1.11	0.036	0.040	0.044
A2	0.00	----	0.25	0.000	----	0.010
B	6.40	6.60	6.80	0.252	0.260	0.268
C	0.45	0.50	0.58	0.018	0.020	0.023
C1	0.45	0.50	0.58	0.018	0.020	0.023
D	5.12	5.32	5.52	0.202	0.209	0.217
E	1.27 BSC			0.050 BSC		
F	0.40	0.50	0.60	0.016	0.020	0.024
F1	0.45	0.65	0.80	0.018	0.026	0.031
H	4.57	4.85	5.10	0.180	0.191	0.201
L	9.40	9.90	10.20	0.370	0.390	0.402
L1	2.40	2.75	3.00	0.094	0.108	0.118
L2	5.40	5.80	6.25	0.213	0.228	0.246
L3	5.00	5.30	5.50	0.197	0.209	0.217
N	0.45	----	1.20	0.018	----	0.047

NOTE

1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
MOLD FLASH SHOULD BE LESS THAN 6MIL

2.DIMENSION L IS MEASURED IN GAUGE PLANE.

3.TOLERANCE 0.10mm UNLESS OTHER WISE SPECIFIED.

4.CONTROLLING DIMENSION IS MILLIMETER.
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

Revision History

Revison	Date	Major changes
1.1	2022/12/15	Release of formal version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semicondutor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

This product is suitable for reflow soldering up to 260℃, not very suitable for wave soldering.
The reliability of this product is not guaranteed under specific conditions when accepted by customers.
CRM(CQ) reserves the right to improve product design, function and reliability without notice.